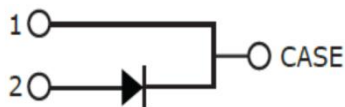


TO-220-2



Package Pin Definitions

- Pin1- Cathode
- Pin2- Anode

Part Number	Marking	Package
XCH08065NAS	08065NAS	TO-220-2

$$V_{RRM} = 650 \text{ V}$$

$$I_F(T_C=157^\circ\text{C}) = 8\text{A}$$

$$V_F(T_J=25^\circ\text{C}) = 1.3\text{V}$$

Features

- Low reverse current
- Good surge current capability
- Low capacitive charge
- No reverse recovery current
- Halogen free, RoHs compliant

Benefits

- System efficiency improvement over Si diodes
- Higher switching frequency
- Increased power density
- Essentially no switching losses

Applications

- Switch mode power supplies (SMPS)
- Uninterruptible power supplies
- On Board Charger
- UPS

Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test conditions	Value	Unit
V_{RRM}	Repetitive peak reverse voltage		650	V
V_{RSM}	Non-repetitive peak reverse voltage		650	V
I_F	Continuous forward current	$T_C=25^\circ\text{C}$ $T_C=135^\circ\text{C}$ $T_C=157^\circ\text{C}$	30 14 8	A
I_{FRM}	Repetitive forward surge current	$T_C=25^\circ\text{C}$, $t_p=10\text{ms}$, Half Sine Pulse $T_C=110^\circ\text{C}$, $t_p=10\text{ms}$, Half Sine Pulse	38 25	A
I_{FSM}	Non-Repetitive forward surge current	$T_C=25^\circ\text{C}$, $t_p=10\text{ms}$, Half Sine Pulse $T_C=110^\circ\text{C}$, $t_p=10\text{ms}$, Half Sine Pulse	64 53	A
$\int i^2 dt$	i^2t value	$T_C=25^\circ\text{C}$, $t_p=10\text{ms}$, Half Sine Pulse $T_C=110^\circ\text{C}$, $t_p=10\text{ms}$, Half Sine Pulse	20.5 14	A ² S
P_{tot}	Power dissipation	$T_C=25^\circ\text{C}$ $T_C=110^\circ\text{C}$	93 40	W
T_J	Operating junction temperature		-55~175	$^\circ\text{C}$
T_{stg}	Storage temperature		-55~175	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Value			Unit
		Min.	Typ.	Max.	
$R_{th(jc)}$	Thermal resistance from junction to case		1.60		°C/W

Electrical Characteristics ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Static Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
V_{DC}	DC blocking voltage	$T_j=25^{\circ}\text{C}$	650			V
V_F	Diode forward voltage	$I_F=8\text{A } T_j=25^{\circ}\text{C}$ $I_F=8\text{A } T_j=175^{\circ}\text{C}$		1.3 1.5	1.5	V
I_R	Reverse current	$V_R=650\text{V } T_j=25^{\circ}\text{C}$ $V_R=650\text{V } T_j=175^{\circ}\text{C}$		1 5	50 200	μA

AC Characteristics ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
Q_C	Total capacitive charge	$V_R=400\text{V } T_j=25^{\circ}\text{C}$ $Q_C = \int_0^{V_R} C(V)dV$		28		nC
C	Total capacitance	$V_R=0\text{V } f=1\text{MHz}$ $V_R=200\text{V } f=1\text{MHz}$ $V_R=400\text{V } f=1\text{MHz}$		650 51 39		pF

Typical Performance

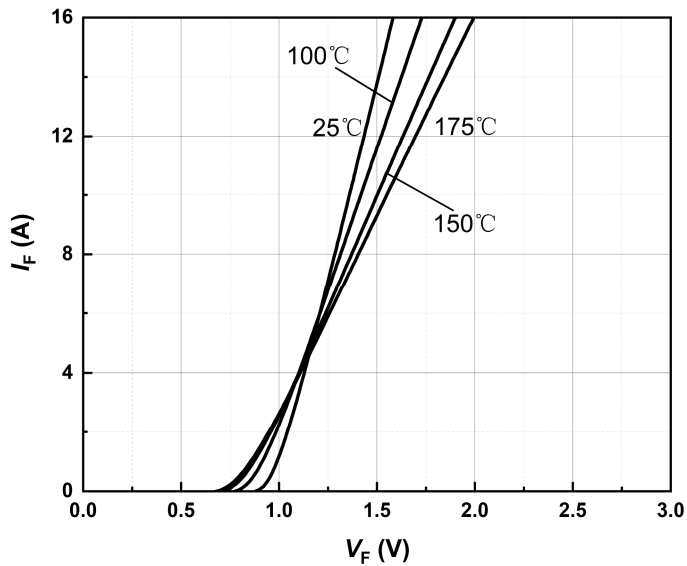


Figure 1. Typical forward characteristics

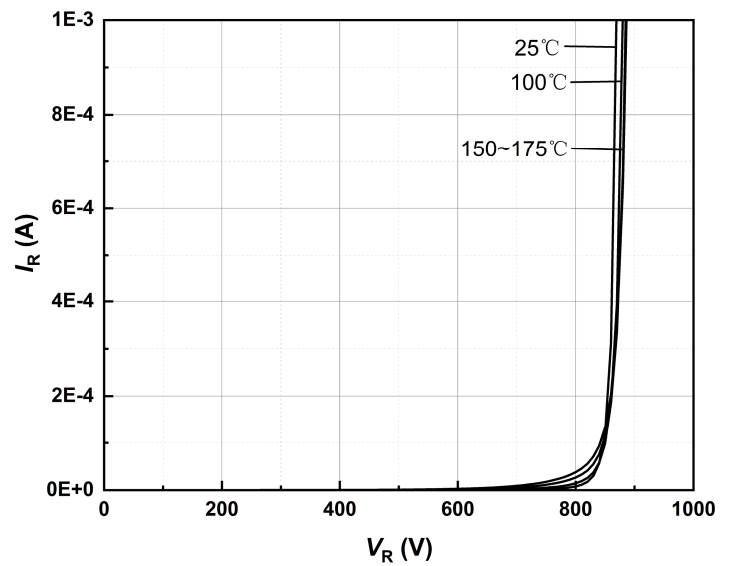


Figure 2. Typical reverse current as function of reverse voltage

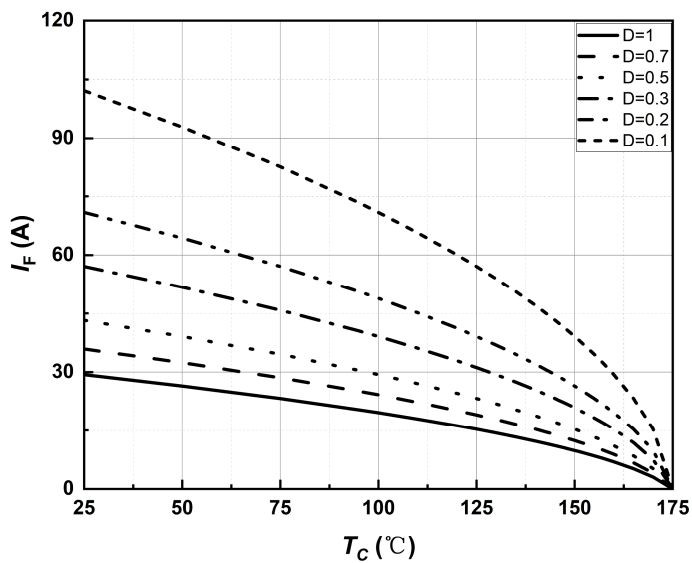


Figure 3. Diode forward current as function of temperature, D =duty cycle

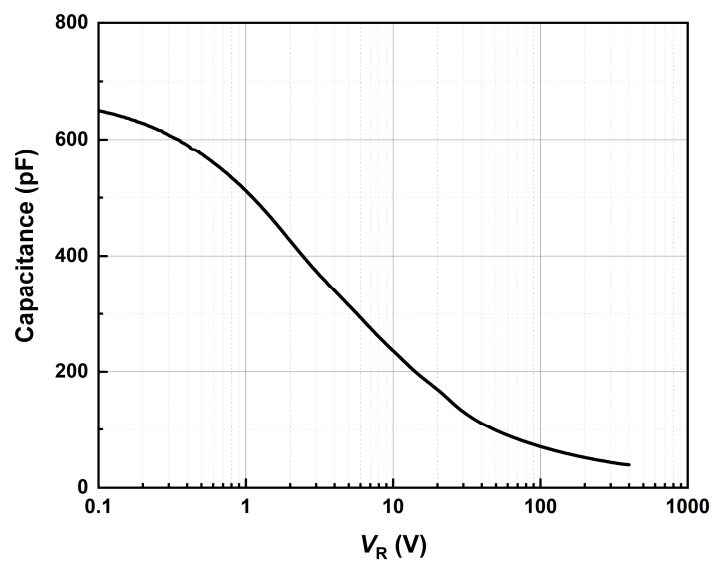


Figure 4. Typical capacitance as function of reverse voltage, $C=f(V_R)$; $T_J=25^\circ\text{C}$

Typical Performance

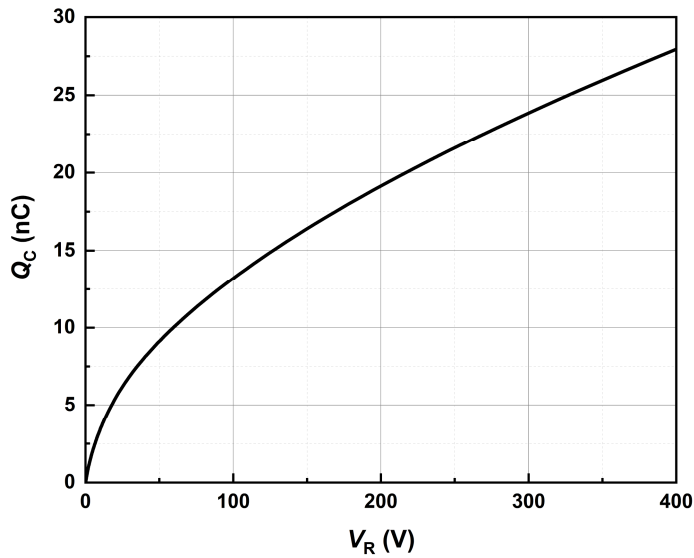


Figure 5. Typical reverse charge as function of reverse voltage

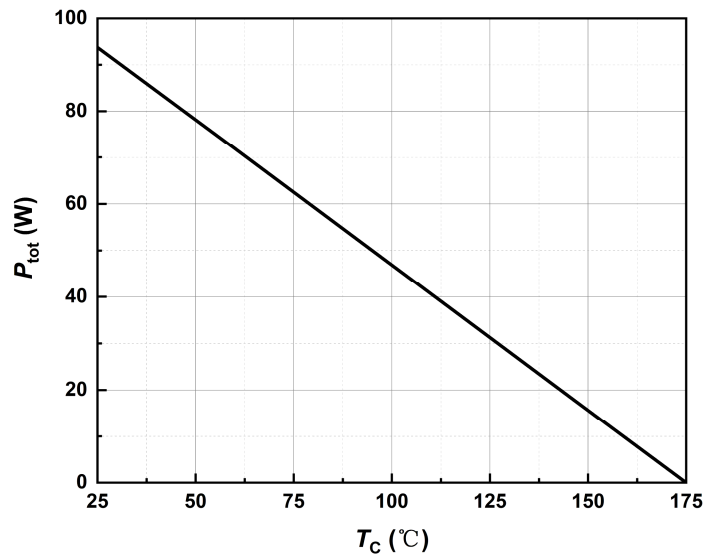


Figure 6. Power dissipation as function of case temperature

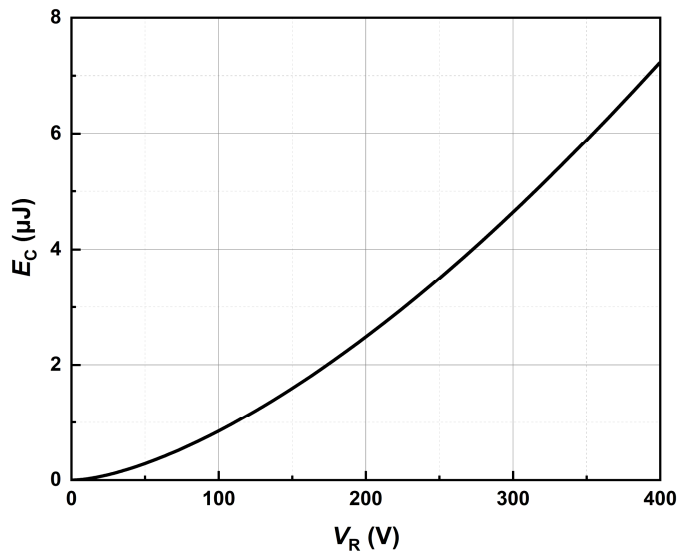


Figure 7. Capacitance stored energy

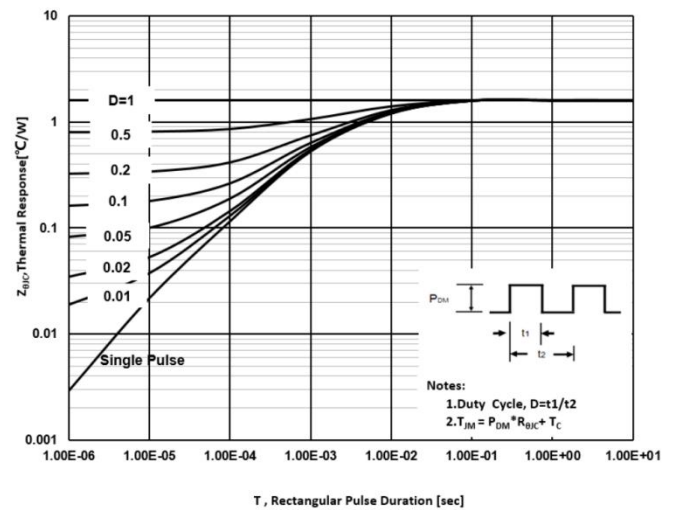
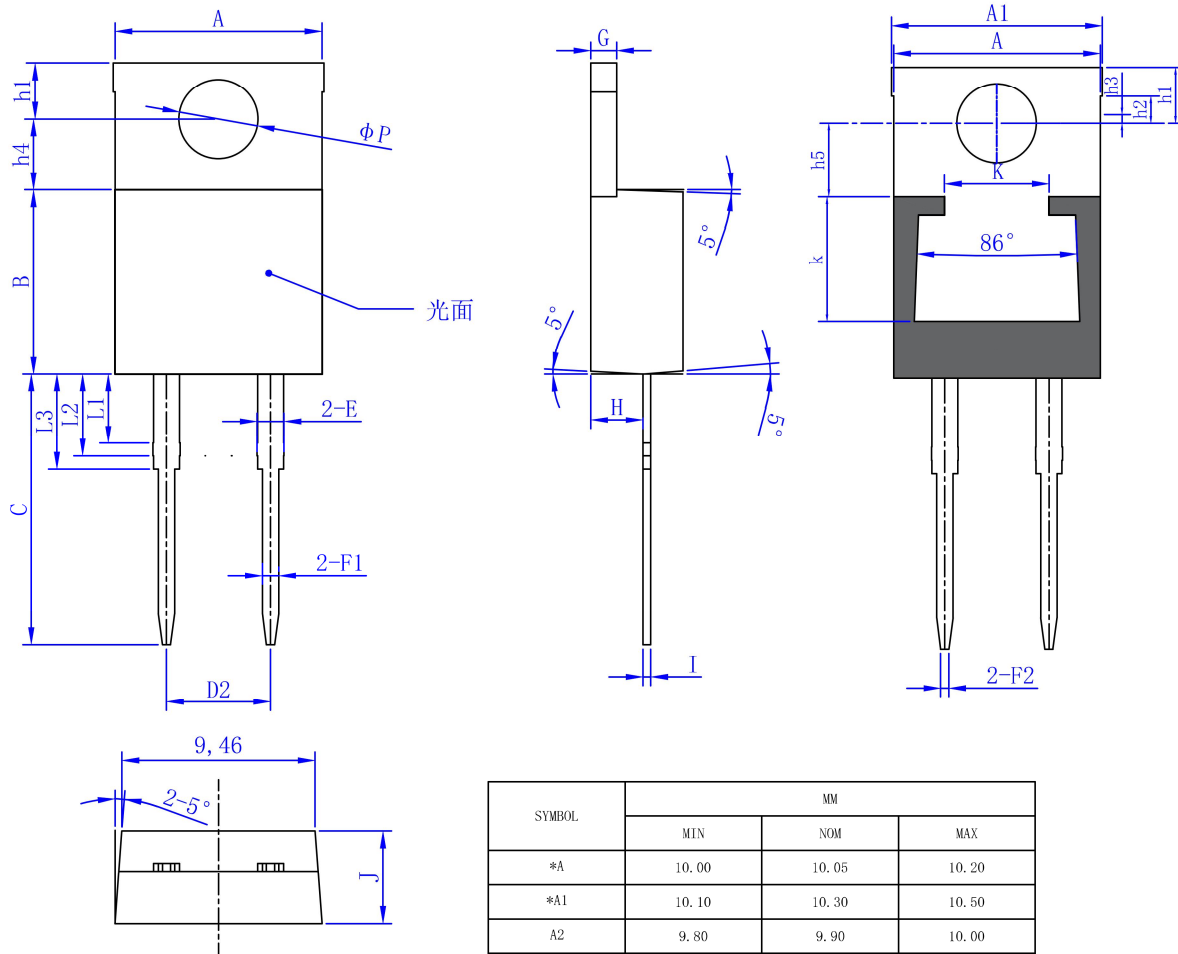


Figure 8. Max. transient thermal impedance

Package Dimensions



SYMBOL	MM		
	MIN	NOM	MAX
*A	10.00	10.05	10.20
*A1	10.10	10.30	10.50
A2	9.80	9.90	10.00
*B	8.80	9.00	9.20
*C	12.80	13.10	13.40
*D2		5.08BSC	
E	1.27	1.32	1.40
*F1	0.75	0.80	0.85
*G	1.26	1.27	1.28
*H	2.40	2.55	2.70
h1	2.70	2.74	2.80
h2	1.27	1.32	1.37
h3	0.40	0.42	0.45
*h4	3.40	3.45	3.50
h5	3.60	3.63	3.65
*I	0.35	0.38	0.45
*J	4.45	4.50	4.60
*K	4.90	5.00	5.10
*k	6.00	6.03	6.05
L1	3.30	3.40	3.50
L2	3.90	4.00	4.10
*L3	4.50	4.60	4.70
* ϕP	3.75	3.80	3.90

带*为检验尺寸, C之尺寸参考原则必须满足有“尖脚”