

TPNCV4274CDT50RKG

40V, 2 μ A I_q, 500mA Low-Dropout Linear Voltage Regulator

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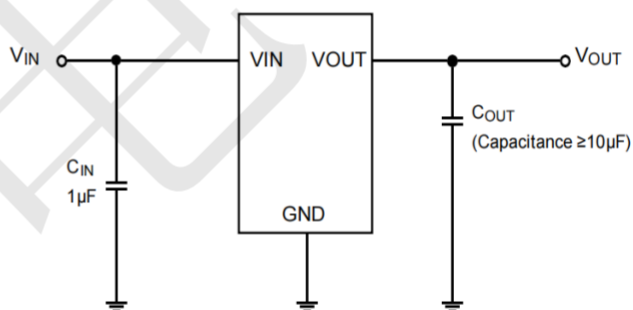
Features

- Wide Input Voltage Range: 3.0V to 40V
- Low Power Consumption: 2 μ A (Typ)
- Maximum Output Current: 500mA
- Low Dropout Voltage:
V_{DROP} = 350mV @ I_{OUT} = 100mA (Typ.)
V_{DROP} = 1900mV @ I_{OUT} = 500mA (Typ.)
- High PSRR: 85dB @ 1kHz
- Output Voltage Accurate: $\pm 2\%$
- Excellent Line/Load Regulation
- Good Transient Response
- Integrated Short-Circuit Protection
- Over-Temperature Protection
- Output Current Limit
- Low Temperature Coefficient
- Stable with Ceramic Capacitor
- RoHS Compliant and Lead (Pb) Free
- -40°C to +85°C Operating Temperature Range
- Fixed Output Voltage Versions: 3.0, 3.3, 3.6 and 5.0V.
- Available in Green TO252-3L Packages

Applications

- Powering MCUs and CAN/LIN transceivers
- Battery-powered equipment
- EV and HEV battery management systems
- Portable, Battery Powered Equipment
- Car Audio/Video Equipmen
- Body control modules

Application Circuits

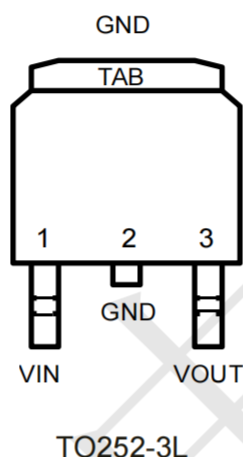


Ordering Information

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OUTPUT VOLTAGE: 30=3.0V
33=3.3V
36=3.6V
50=5.0V

Pin Configuration (TOP VIEW)



Absolute Maximum Ratings

Item	Symbol	Rating	Unit
Supply Input Voltage	V _{IN}	-0.3 ~ 40	V
V _{OUT} to V _{IN}	V _{OUT} _ V _{IN}	-35 ~ -0.3	V
Regulated Output Voltage	V _{OUT}	-0.3 ~ 6.0	V
Output Current	I _{OUT}	Internally limited	mA
Power Dissipation P _D @T _A =+25°C	P _D	1650	mW
Thermal Resistance (Junction to air)	θ_{JA}	75	°C /W
Human Body Model (HBM)		±4000	V
Charged Device Mode (CDM)		±2000	V
Machine Mode (MM)		200	V
Storage Temperature Range	T _{STG}	-65 ~ +150	°C
Operating Junction Temperature	T _J	+150	°C
Lead Temperature (Soldering 10s)	T _{LEAD}	+260	°C

Note:

- 1、Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these conditions is not implied. Exposure to absolute-maximum-rated conditions for extended period may affect device reliability.
- 2、Ratings apply to ambient temperature at +25°C
- 3、The package thermal impedance is calculated in accordance to JESD 51-7.

Recommended Operating Conditions

Item	Min	Max	Unit
Operating Ambient Temperature	-40	+85	°C
Input Voltage	3.0	40	V
Output Voltage	1.8	5.0	V

Electronic Characteristics

Test Conditions: V_{IN} = V_{OUT} + 2V, C_{IN} = 1μF, C_{OUT} = 10μF, T_A = 25°C, unless otherwise specified

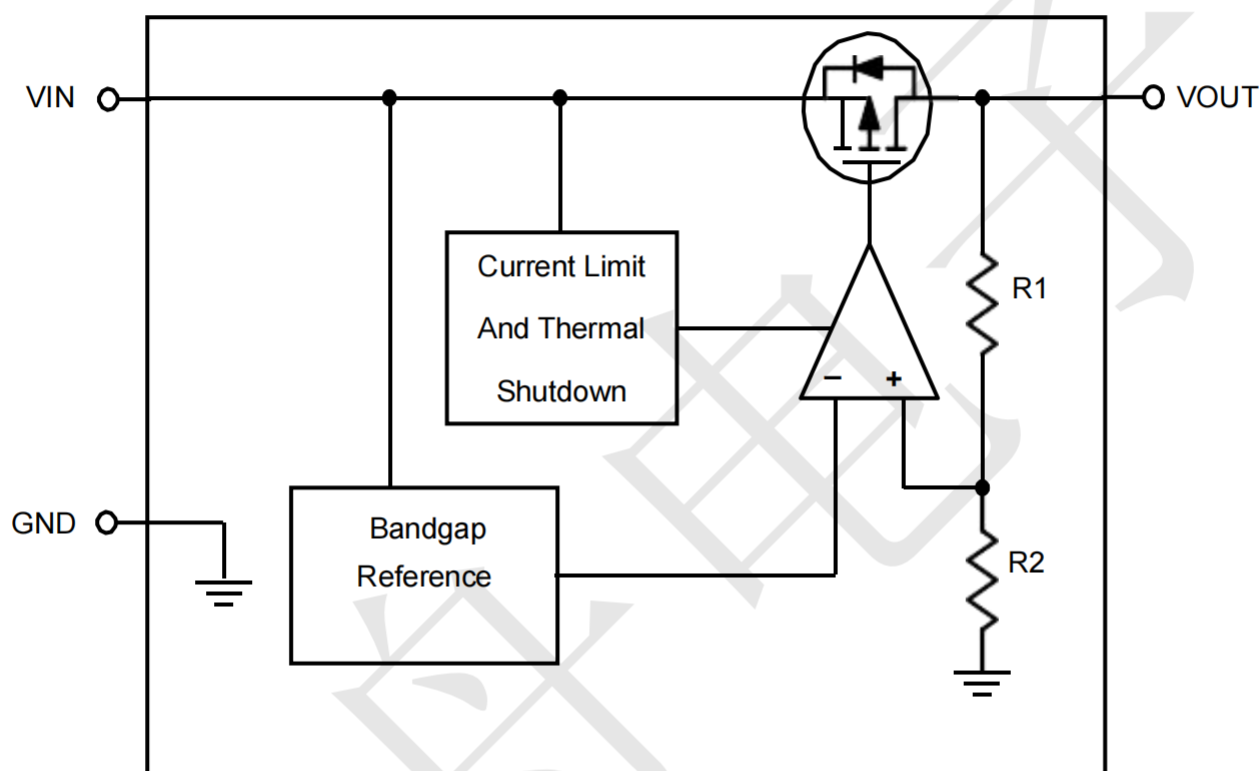
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V _{IN}	Input Voltage	—	3.0	—	40	V
I _Q	Quiescent Current	V _{IN} = 12V, No Load	—	2	5	μA
V _{OUT}	Output Voltage	V _{IN} = 12V I _{OUT} = 10mA	V _{OUT} x 0.98	—	V _{OUT} x 1.02	V
I _{OUT}	Output Current	—	—	500	—	mA
V _{DROP}	Dropout Voltage	I _{OUT} = 100mA, V _{OUT} = V _{OUTNOM} - 0.1V	—	350	500	mV
		I _{OUT} = 500mA V _{OUT} = V _{OUTNOM} - 0.1V	—	1900	2100	mV
Δ V _{LOAD}	Load Regulation	V _{IN} = 12V 1mA ≤ I _{OUT} ≤ 100mA	—	0.02	0.025	%/mA
Δ V _{LINE}	Line Regulation	V _{OUTNOM} + 2V ≤ V _{IN} ≤ 45V I _{OUT} = 1mA	—	0.01	0.02	%/V
I _{LIMIT}	Current Limit	—	—	650	—	mA
T _{OTSD}	Thermal Shutdown Temperature	—	—	+150	—	°C
THY _{OTSD}	Thermal Shutdown Hysteresis	—	—	+20	—	°C
PSRR	Power Supply Rejection Ratio	V _{IN} = 12V, I _{OUT} = 10mA V _{OUT} = 3.3V @1kHz	—	85	—	dB
V _{on}	Output Noise Voltage	C _{OUT} = 10μF, I _{OUT} = 30mA BW = 10Hz~100kHz	—	100	—	μVrms

Note : All limits specified at room temperature (T_A = 25°C) unless otherwise specified. All room temperature limits are 100% production tested. All limits at temperature extremes are ensured through correlation using standard Statistical Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).

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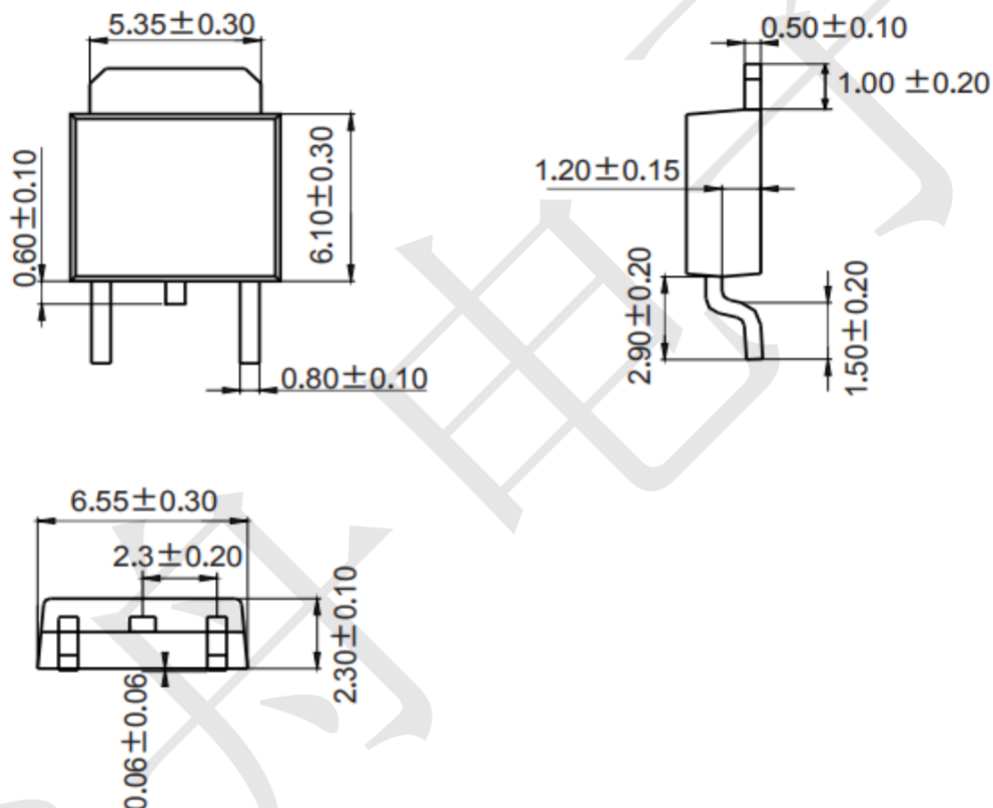
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Package information (Unit: mm)

TO252-3L



Mounting Pad Layout (Unit: mm)

