

规格承认书

Specification for approval

产品名称： 40D561K 压敏电阻

Product Type: 40D561K varistor

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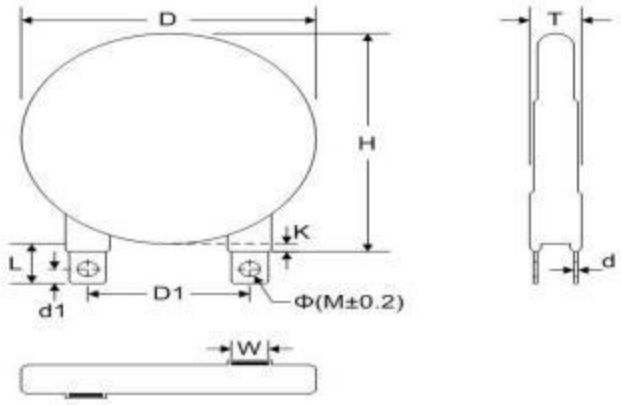
网址: <http://www.szshaoxin.com>

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1.OUTLINE

1.1	APPEARANCE	WITHOUT ANY CRACK, MARKING SHOULD BE CLEAR	
1.2	DIMENSIONS	DIMENSIONS (mm)	
		H(max.)	50
		L(min.)	14.5
		D(max.)	45
		D1(±1.0)	25.4
		T(max.)	8.3
		d(±0.25)	0.5
		d1(±0.3)	3.7
		K(max.)	3.2
		W(±0.5)	7.0
		ΦM(±0.2)	3.2
		Epoxy Colour: Blue	

2.ELETRICAL PARAMETER

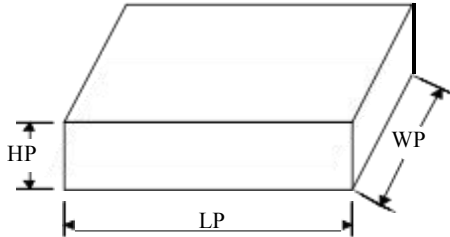
2.1	MAX ALLOWABLE VOLTAGE	AC: 350(V) DC: 460(V)	At 1mA DC
2.2	VARISTOR VOLTAGE	504-616(V)	V0. 1mA □ V1mA ■
2.3	RATED WATTAGE	1.4(W)	
2.4	MAX CLAMPING VOLTAGE	IP: 300(A) Vc: 925(V)	Test Current Waveform 8/20 μs
2.5	WITHSTANDING SURGE CURRENT	1Time 40000A 2Time 30000A	Test Current Waveform 8/20 μs
2.6	MAX ENERGY	810(J)	Test Current Waveform 10/1000 μs
2.7	TYPICAL CAPACITANCE	4000(pf)	@ 1KHz
2.8	LEAKAGE CURRENT	≦ 50 (μA)	At 80% of Varistor Voltage
2.9	NONLINEAR EXPONENT (α)	≧ 10	$\alpha = \log \frac{I_1}{I_2} / \log \frac{V_1}{V_2}$
2.10	TEMPERATURE COEFFICENT OF VARISTOR VOLTAGE	≧ ±0.05%/°C max	$\frac{V_{c85^{\circ}C} - V_{c25^{\circ}C}}{V_{c25^{\circ}C}} \times \frac{1}{60} \times 100(\% ^{\circ}C)$
2.11	IMPULSE LIFE	≧ ±10%(V1mA)	Test Current Waveform 8/20 μs

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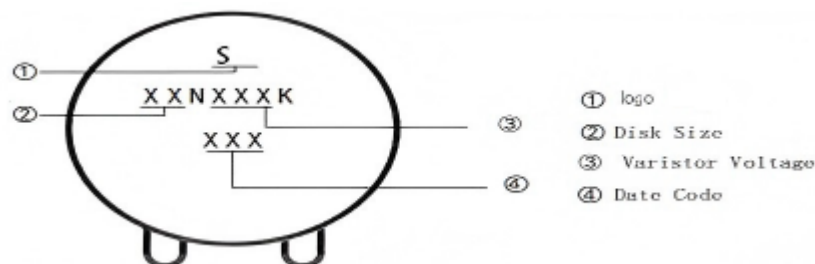
3.PACKING

3.1	Quantity	50PCS				
3.2	Packing Dimension		LP	245max		
			HP	65 max		
			WP	185max		
			Unit: mm			

4.MATERIAL LIST

4.1	Drawing	/					
4.2	Material Chart RoHs	Item	ComPosition	Manufacturer			
		Coating	Epoxy Resin	Made in China, and in line with the UL 94-V0 testing, meet the environmental requirements			
		Lead	CU /CPwire	Made in China, meet the environmental requirements			
		Electrode	Silver	Made in China, meet the environmental requirements			
		Disk	Zinc Oxide	Zip Over LLC			
		Solder	Sn:96.5%CU 0.5%Ag3.0%	Made in China, meet the environmental requirements			

5. MARKING CODE



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6.MECHANICAL REQUIREMENTS

6.1	Tensile of Terminations	No Outstanding Damage	1.0Kgf; 10Sec.
6.2	Bending of Terminations	No Outstanding Damage	0.5Kgf; 90° ,3 Times
6.3	Vibration	No Outstanding Damage	Freq:10-55hz;Amp:0.75mm, 1Min .
6.4	Solderability	Min. 95% of The Terminal Should Be Covered With Solder Uniformly	Solder Temp:265±5℃ Immersed Time: ≤5Sec.
6.5	Resistance of soldering heat	△ V1 mA/ V1 mA ≒±5%	Solder Temp: 265±5℃ Immersed Time: 10± 1Sec.

7.ENVIRONMENTAL REQUIREMENTS

7.1	High Temperature Storage	△V1mA/V1mA ≒±5%	Ambient Temp: 125±2℃ Duration:1000h
7.2	Low Temperature Storage	△V1mA/V1mA ≒±5%	Ambient Temp: -40±2℃ Duration:1000h
7.3	High Humidity Storage/Damp Heat	△V1mA/V1mA ≒±5%	Ambient Temp: 40±2℃ 90-95% R.H. Duration:1000h
7.4	Temperature Cycle	△V1mA/V1mA ≒±5%	5 Times
			Step Temperature (℃) Period (min)
			1 -40±3 30 ±3
			2 Room Temp 15 ±3
			3 125±3 30 ±3
			4 Room Temp 15 ±3
7.5	High Temperature Load	△V1mA/V1mA ≒±10%	Ambient temp:85±2℃ Duration:1000h Load: MAX. Allowable Voltage
7.6	High Humidity Load	△V1mA/V1mA ≒±10%	Ambient Temp: 40±2℃ 90-95%R.H.Duration:1000H Load: MAX. Allowable Voltage
7.7	Operating Temperature Range	-40℃ ~ +85℃	-40℃ ~ +85℃
7.8	Storage Temperature Range	-40℃ ~ +125℃	-40℃ ~ +125℃

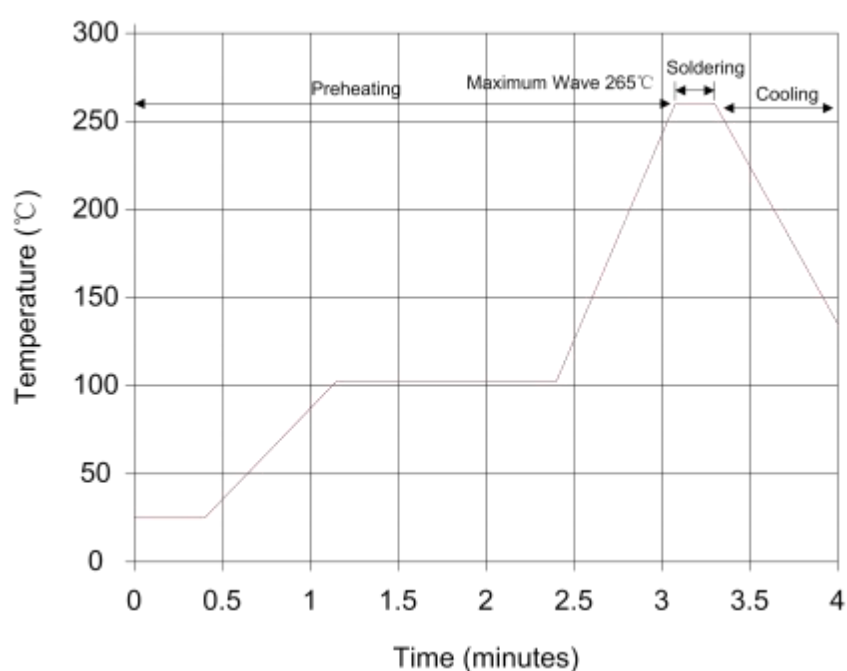
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Soldering Recommendation

Wave Lead Free Soldering Recommendation



Item	Conditions
Peak Temperature	265°C
Dipping Time	10 seconds (max.)
Soldering	1 time

Recommendation Reworkin g Conditions with Solderin g Iron

Item	Conditions
Temperature of Soldering Iron-tip	360°C (max.)
Soldering Time	3 seconds (max.)
Distance from Varistor	2mm (min.)