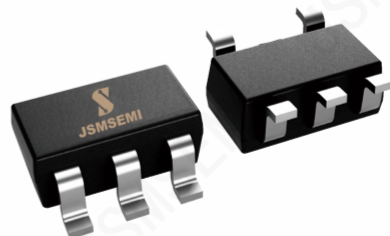


1. Description

The IRS44273LTRPBF device is a low voltage power MOSFET and IGBT in phase gate driver. Proprietary latch-immune CMOS technology enables single-chip integrated architectures with high robustness. The IRS44273LTRPBF logic input level is compatible with CMOS or TTL logic output levels down to 3.3V. The output driver has Internal Undervoltage Lockout (UVLO) circuitry with hysteresis and buffer stage of output current. The IRS44273LTRPBF is designed to operate over a wide VCC range of 5 V to 25V and wide temperature range of -40°C to 125°C .



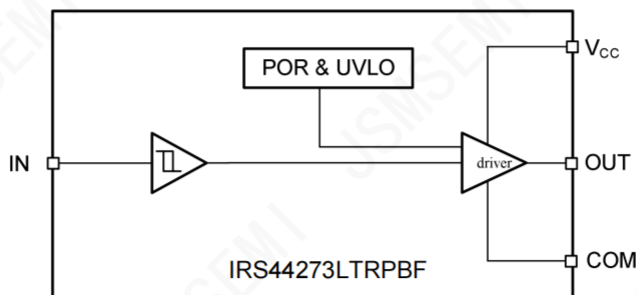
2. Features

- CMOS schmidt trigger input
- Input output in-phase
- Compatible with 3.3V input logic
- 5 to 25-V Single-Supply Range
- High capacitance load driving capability
- Operating Temperature Range of -40 to 125°C
- Undervoltage Lockout
 - Undervoltage Lockout turn-on threshold 4.0V
 - Undervoltage Lockout turn-off threshold 3.9V
- Turn on/Turn off Delays:
 - $T_{on}/T_{off} = 25\text{ns}/25\text{ns}$
- 2-A Peak Source and Sink-Drive Current

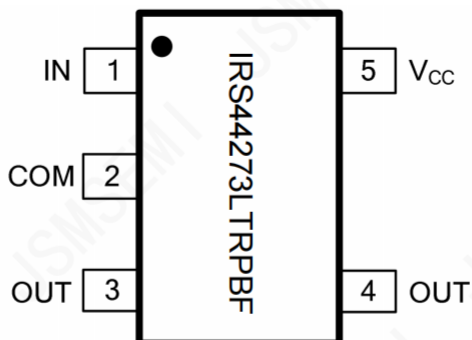
3. Applications

- Switch-Mode Power Supplies
- General Gate Driver
- Driving MOSFETs and IGBTs

Pin Configuration



4.Pin Configuration and Functions



Pin Functions

PIN	NAME	DESCRIPTION
1	IN	Logic input.
2	COM	Ground: All signals are referenced to this pin.
3	OUT	Gate drive output.
4	OUT	Gate drive output.
5	VCC	Bias supply input.

5. Absolute Maximum Ratings

Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. All voltages are with respect to COM unless otherwise noted, Currents are positive into, negative out of the specified terminal, environment temperature is 25 °C.

Symbol	Definition	MIN	MAX	UNIT
V_{CC}	Supply voltage range	-0.3	25	V
V_O	OUT voltage range	-0.3	$V_{CC}+0.3$	
V_{IN}	IN voltage	-12	25	

Thermal Information

Symbol	Definition	MIN	MAX	UNIT
R_{thJA}	thermal resistance		151	°C/W
T_S	Storage temperature	-55	+150	°C
T_J	Operating junction temperature		+150	
T_L	Lead temperature		300	

Recommended Operating Conditions

To properly operate, device should be used in the following recommended conditions. All voltages are with respect to COM unless otherwise noted, Currents are positive into, negative out of the specified terminal, environment temperature is 25 °C.

Symbol	Definition	MIN	MAX	UNIT
V_{CC}	Supply voltage range	5.0	20	V
V_O	OUT voltage range	0	V_{CC}	
V_{IN}	IN voltage	-10	V_{CC}	
T_A	ambient temperature	-40	125	°C

Electrical Characteristics

TA= 25°C, VCC=15V, CL=1nF(unless otherwise noted)

Symbol	Definition	MIN	TYP	MAX	UNIT
V _{IH}	Input signal high threshold	2.7			V
V _{IL}	Input signal low threshold			0.8	V
V _{CCUV+}	Undervoltage Lockout (UVLO) turn-on threshold VCC		4.0		V
V _{CCUV-}	Undervoltage Lockout (UVLO) turn-off threshold VCC		3.9		V
V _{CCUVHY}	UVLO threshold hysteresis VCC		0.1		V
I _{IN+}	Input current (I _N =5V)		50	100	μA
I _{IN-}	Input current (I _N =0V)			5	μA
V _{OH}	High output voltage			0.35	V
V _{OL}	Low output voltage			0.35	V
I _Q	VCC quiescent supply current		180	400	μA
I _{O+}	Output high short-circuit pulse current		2		A
I _{O-}	Output low short-circuit pulse current		2		A
t _R	Rise time			30	ns
t _F	Fall time			30	ns
t _{ON}	Turn-on propagation delay		25	50	ns
t _{OFF}	Turn-off propagation delay		25	50	ns

6. Function Description

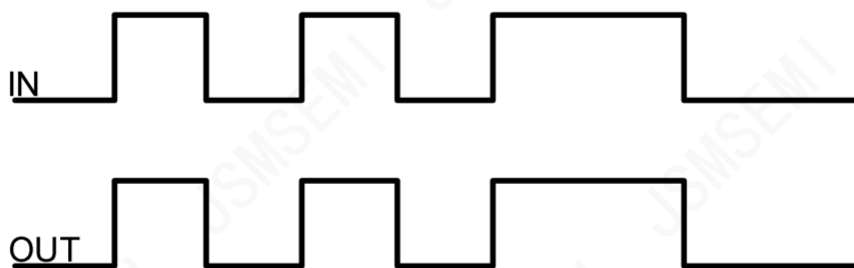


Figure 6 - 1 Input-Output waveform

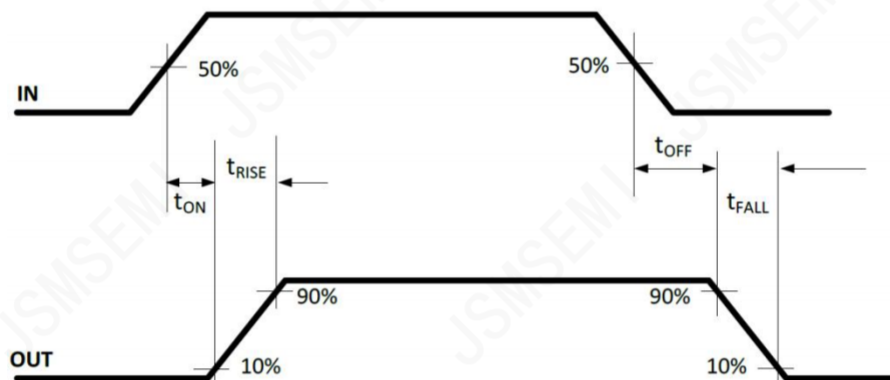


Figure 6 - 2 Propagation Time Waveform Definition

7. Function Block Diagram

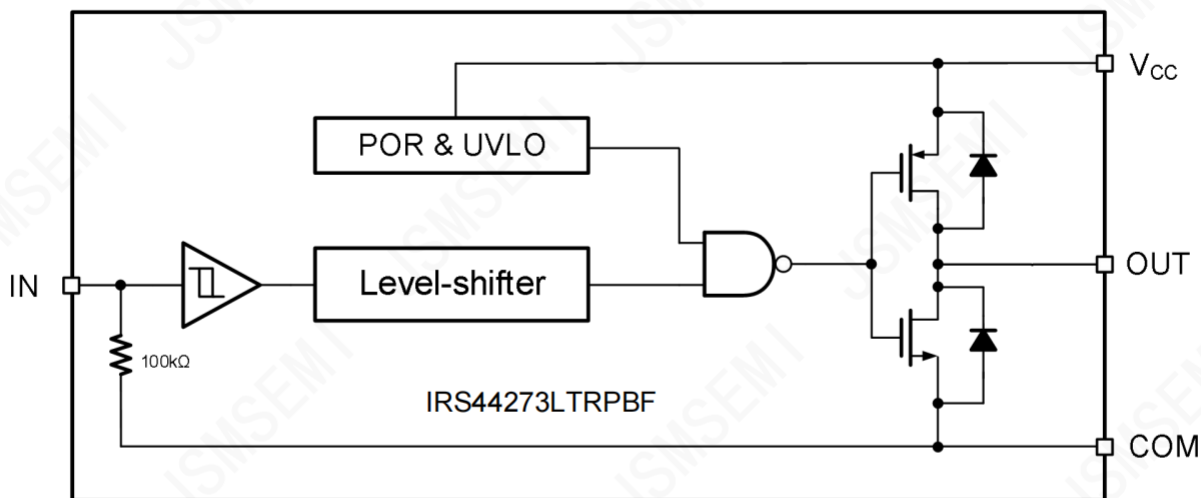


Figure 7-1 Function Block Diagram of IRS44273LTRPBF

8. Application message

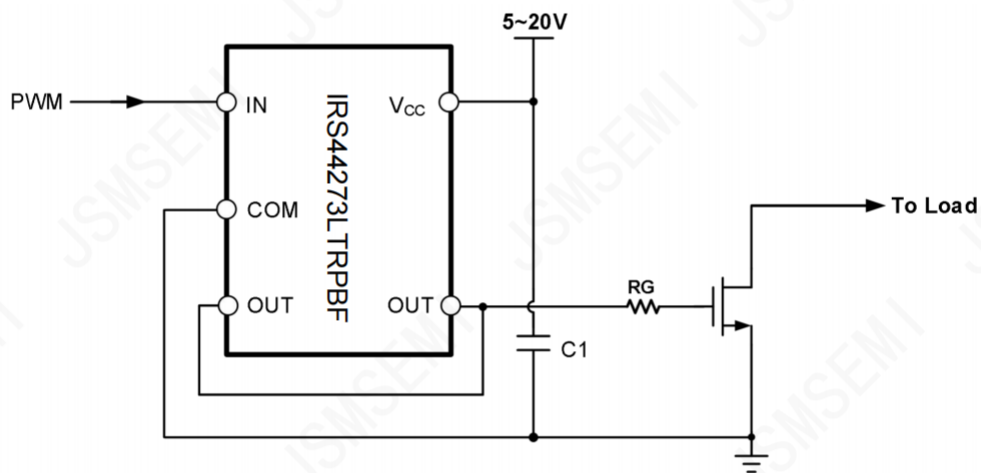
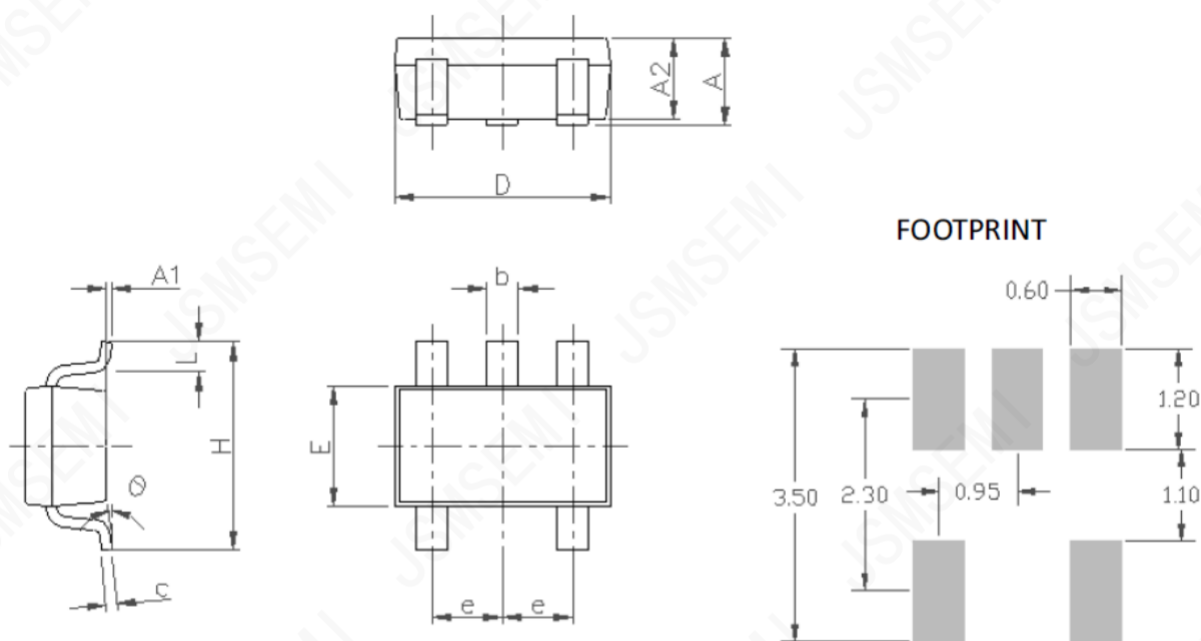


Figure8-2 Typical application circuit of IRS44273LTRPBF

8 PACKAGING INFORMATION

SOT23-5 Package Outlines



SOT23-5 Package Dimensions

Size Symbol	MIN(mm)	TYP(mm)	MAX(mm)	Size Symbol	MIN(mm)	TYP(mm)	MAX(mm)
A	0.9	-	1.45	E	1.50	-	1.75
A1	0.00	-	0.15	e	-	0.95	-
A2	0.90	-	1.30	H	2.60	-	3.00
b	0.30	-	0.50	L	0.30	-	0.60
c	0.09	-	0.20	θ	0.00	-	10.00
D	2.80	-	3.05				

Revision History

Rev.	Change	Date
V1.0	Initial version	6/27/2021

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