

P-Channel Enhancement Mode MOSFET

1 Features

- $V_{DS} = -30V$, $I_D = -5A$
- $R_{DS(ON)} < 45m\Omega$ @ $V_{GS} = -10V$
- $R_{DS(ON)} < 55m\Omega$ @ $V_{GS} = -4.5V$
- High density cell design for extremely low $R_{DS(ON)}$
- Lead free product is acquired
- Package DFN2020-6L

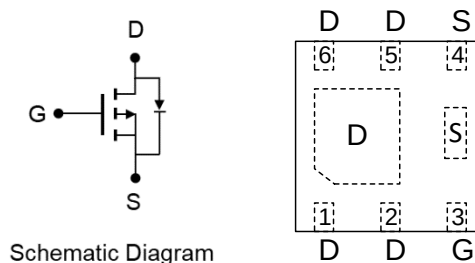
2 Applications

- Cellular Handsets and Accessories
- Personal Digital Assistants
- Portable Instrumentation
- Load switch

3 Description

The SLMN430V4CP provide the best combination of fast switching, low on-resistance and cost-effectiveness. The MOSFET is the P-Channel enhancement mode power field effect transistors with high cell density, trench technology. This high density process and design have been optimized switching performance and especially tailored to minimize on-state resistance.

4 Pin Configuration and Top View



DFN2020-6L

5 Device Information

PART NUMBER	PACKAGE	BODY SIZE (NOM)
SLMN430V4CP	DFN2020-6L	2.00 mm x 2.00mm

6 Absolute maximum Ratings @25°C

RATING	SYMBOL	VALUE	UNITS
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	-5	A
Pulsed Drain Current	I_{DP}	-20	A
Total power dissipation	P_D	1.5	W
Channel temperature	T_J	-55 to +150	°C
Range of storage temperature	T_{STG}	-55 to +150	°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	90	°C/W

7 Electrical characteristics (@25°C unless otherwise specified)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = -250μA, V _{GS} = 0V	-30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-0.5	-0.9	-1.1	V
Drain Cut Off Current	I _{DSS}	V _{DS} = -30V, V _{GS} = 0V			-1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±12V, V _{DS} = 0V			±0.1	μA
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} = -10V, I _D = -5A		37	45	mΩ
		V _{GS} = -4.5V, I _D = -4A		43	55	mΩ
Dynamic Characteristics						
Total Gate Charge	Q _G	V _{DS} =-24V, I _D =-4A, V _{GS} = -10V		7.8		nC
Gate-Source Charge	Q _{GS}			2.1		nC
Gate-Drain Charge	Q _{GD}			2.4		nC
Input Capacitance	C _{ISS}	V _{DS} = -15V, V _{GS} = 0V, f = 1MHz		815		pF
Reverse Transfer Capacitance	C _{RSS}			50		pF
Output Capacitance	C _{OSS}			60		pF
Turn-On Delay Time	t _{d(on)}	V _{GS} =-10V, V _{DS} =-15V, I _D =-4A R _G =2.5Ω		12.4		ns
Turn-Off Delay Time	t _{d(off)}			43.2		ns
Rise Time	t _r			5.6		ns
Fall Time	t _f			6.8		ns
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V _{SD}	I _S = -4A, V _{GS} = 0V		-0.7	-1.5	V

8 Typical Characteristics

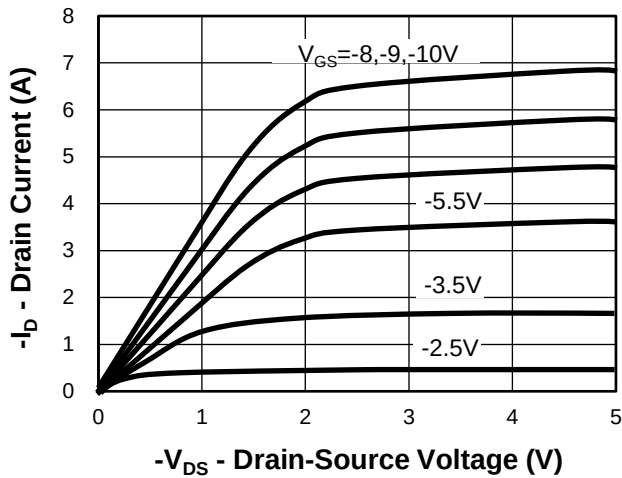


Fig 1. Output Characteristics

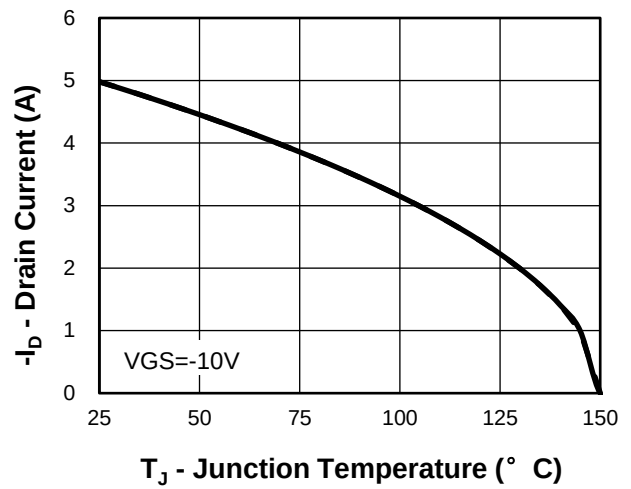


Fig 2. Drain Current

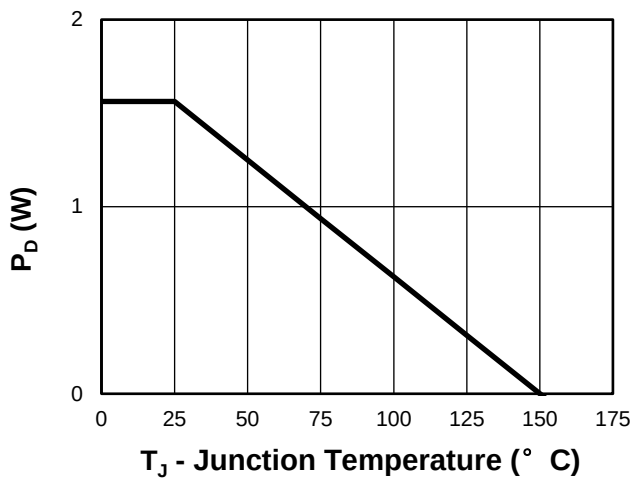


Fig 3. Power Dissipation

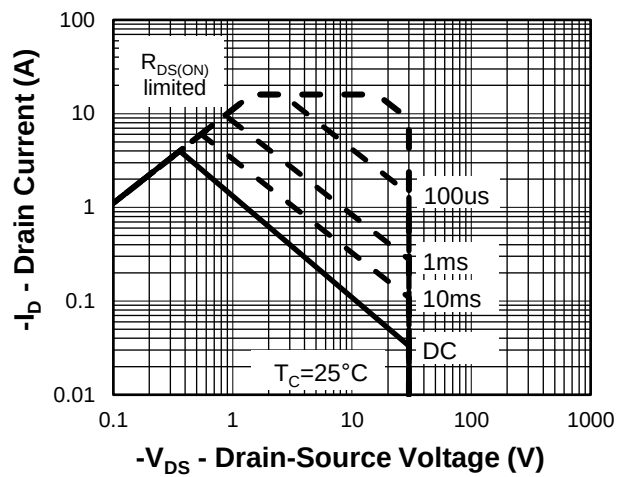


Fig 4. Safe Operation Area(SOA)

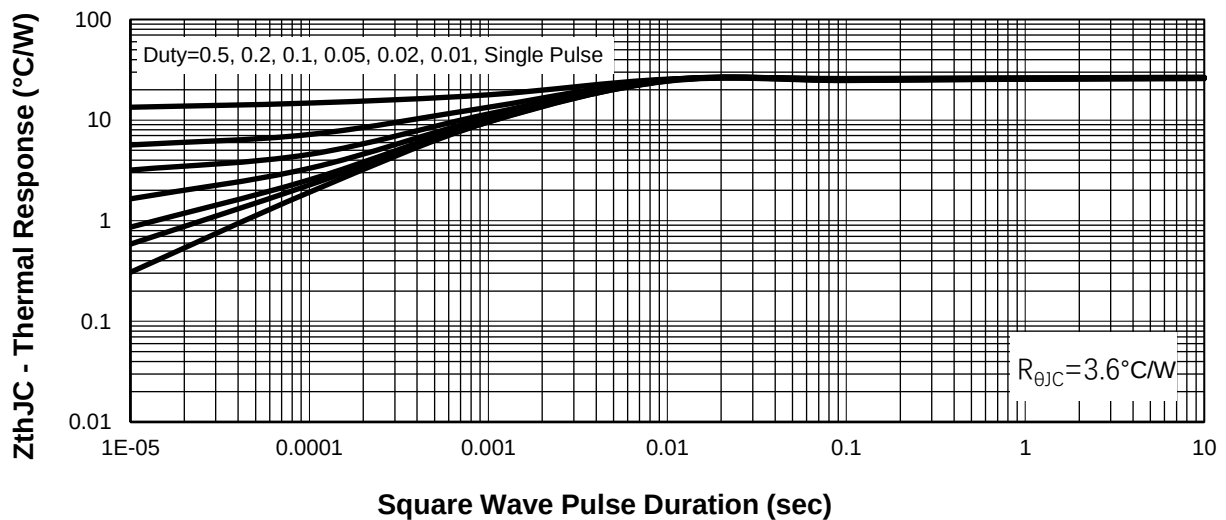
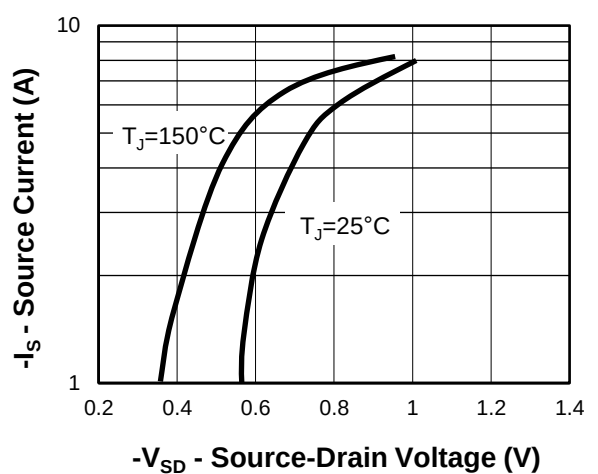
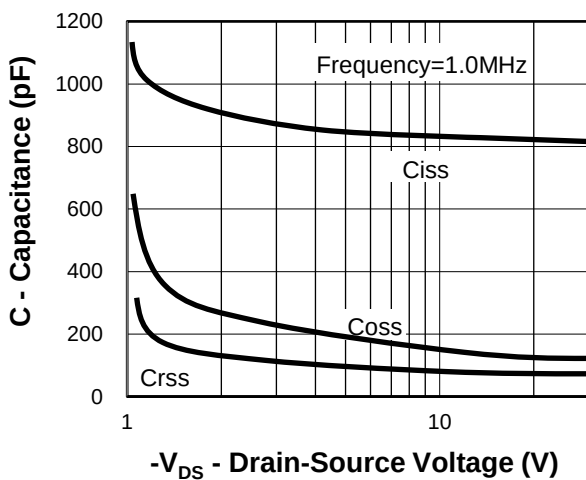
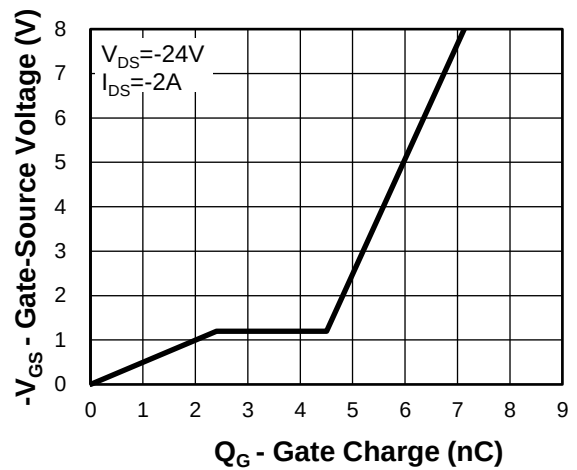
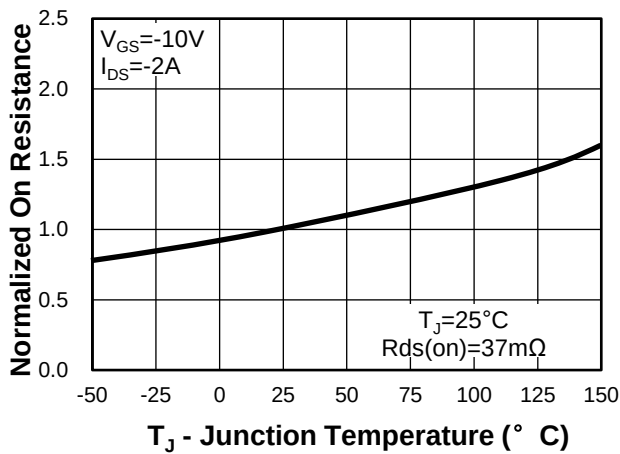
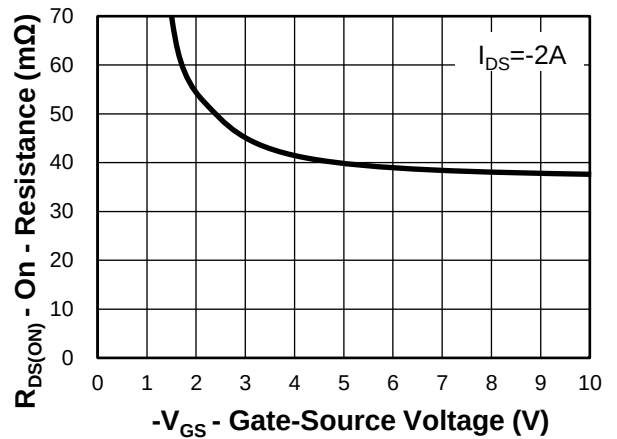
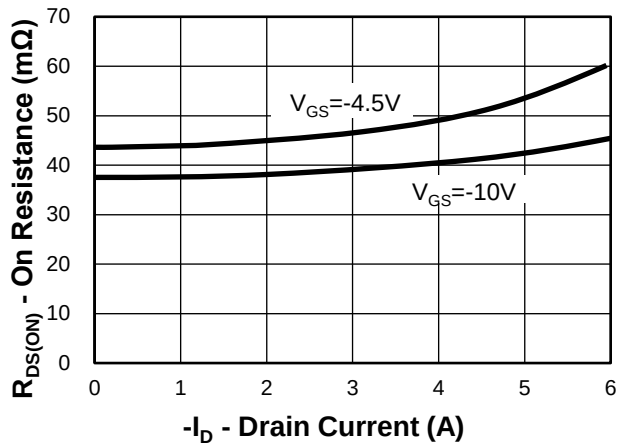
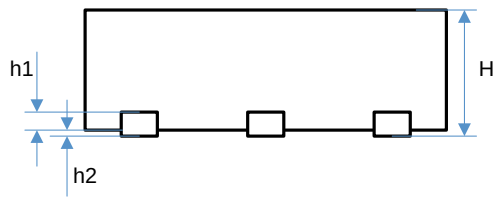


Fig 5. Thermal Transient Impedance

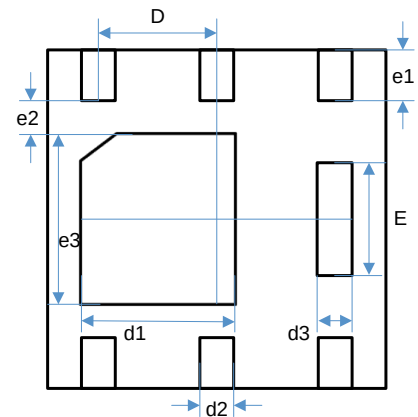
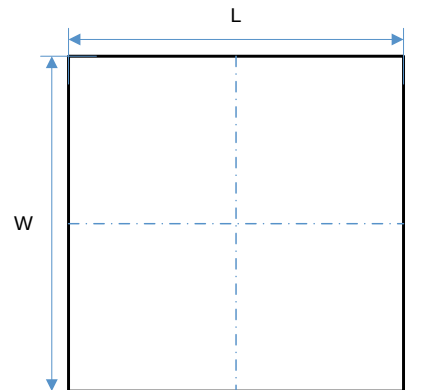


9 Product dimension

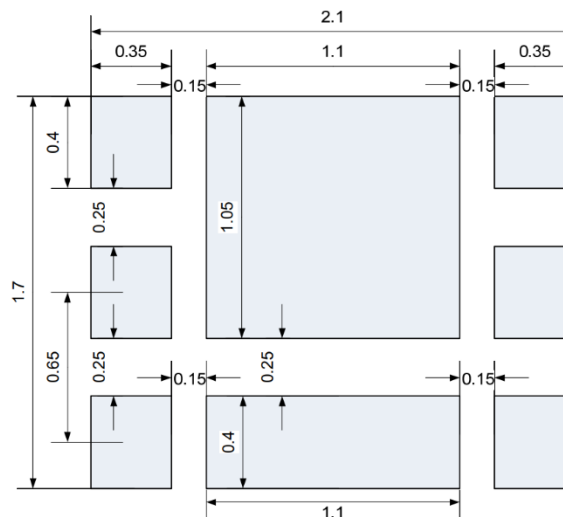
DFN2020-6L



DIM	UNITS (mm)		
	MIN.	TYP.	MAX.
L	1.924	-	2.076
W	1.924	-	2.076
D	-	0.650	-
E	0.46	-	0.66
d1	0.80	-	1.00
d2	0.25	-	0.35
d3	0.20	-	0.40
e1	0.174	-	0.326
e2	0.20	-	-
e3	0.85	-	1.05
H	0.70	-	0.80
h1	-	0.203	-
h2	0.00	-	0.05



10 PCB Layout Footprints (Units: mm)



11 Ordering Information

Part Number	Packaging	Reel Size
SLMN430V4CP	3000/Tape & Reel	7 inch