



Features

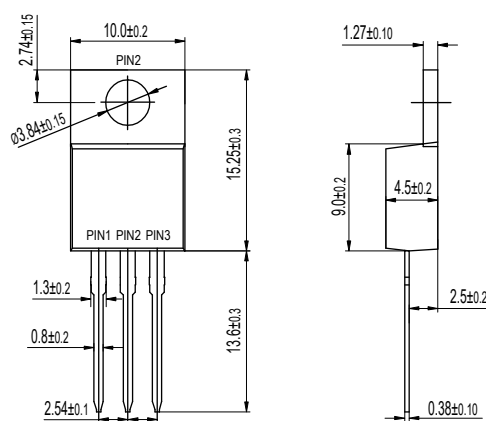
- Glass passivated chip junctions
- Super fast recovery time for switching mode application
- High Forward Surge Capability
- Low Reverse Current
- Lead free in compliance with EU RoHS 2011/65/EU directive

Mechanical Data

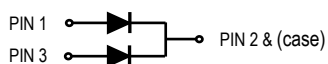
- Circuit figure: Common Cathode
- Leads: Solderable per mil-std-202, Method 208
- Polarity: as marked
- Mounting torque: 5 in-lbs maximum
- Terminals: Puretin plated
- Weight: TO-220AB 1.85 grams

Package outline

Dimensions in millimeters



TO-220AB





MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (TA=25°C)

RATINGS	SYMBOL	MUR1060CT	UNIT
Maximum repetitive reverse voltage	VRRM	600	V
Maximum RMS voltage	VRMS	420	V
Maximum DC blocking voltage	VDC	600	V
Maximum average forward current	IAV	16	A
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	IFSM	250	A
Typical thermal resistance per diode(Note 1) TO-220AB ITO-220AB TO-262AB TO-263(D ² PAK)	R θ -JC	2.5 4.0 2.5 2.5	°C/W
Operating junction temperature range	TJ	-55 to +150	°C
Storage temperature range	TSTG	-55 to +150	°C
CHARACTERISTICS	SYMBOL		UNIT
Typical forward voltage per leg at 8A	VF	1.70	V
Maximum average reverse current at rated DC blocking voltage TJ=25°C TJ=125°C	IR	5 250	μA
Typical reverse recovery time(Note 2)	TRR	35	nS

Notes: 1. Thermal resistance from junction to case.

2. Test conditions: IF=0.5A, IR=1.0A, IRR=0.25A.



FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE

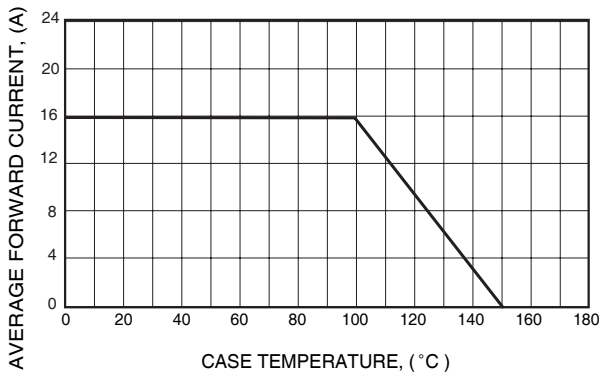


FIG. 3 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

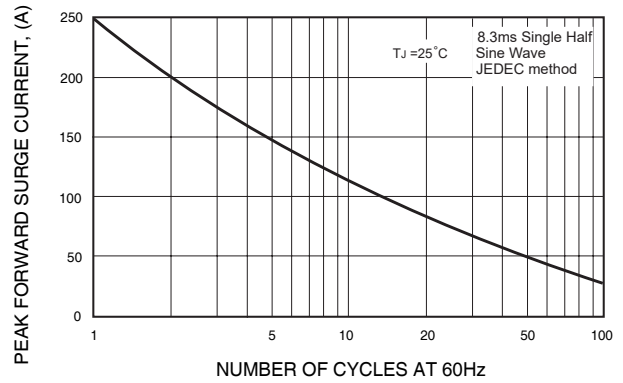


FIG. 4 - TYPICAL REVERSE CHARACTERISTICS

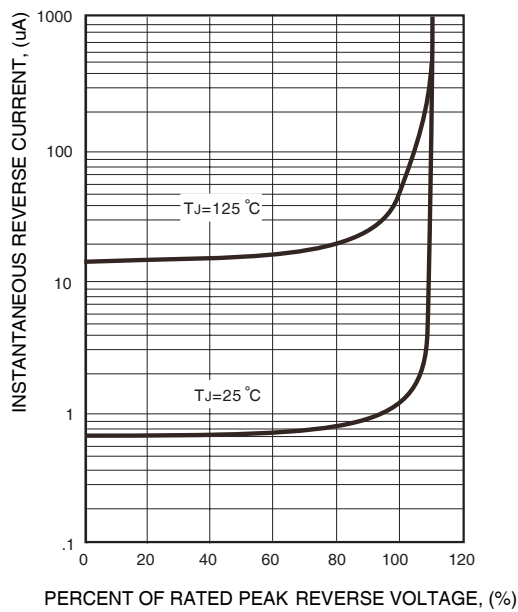


FIG. 2 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

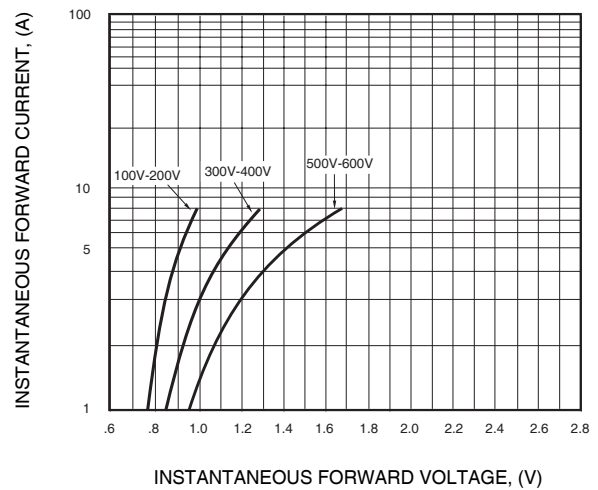


FIG.6- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTICS

