

1000mA Linear Voltage Regulators

Features

- Quiescent Current: 2mA(Typ.)
- Input Voltage Range: 15V(Max.)
- Output Current: 1000mA(Max.)
- Low Dropout Voltage: 1.3V@1A
- Output Voltage: 1.2V, 1.5V, 1.8V, 2.5V, 3.3V, 5.0V
- Operation Ambient Temperature: -40~85° C
- Typical accuracy: 2%

Applications

- PC Motherboard
- LCD Monitor
- Graphic Card
- Modem
- Printer

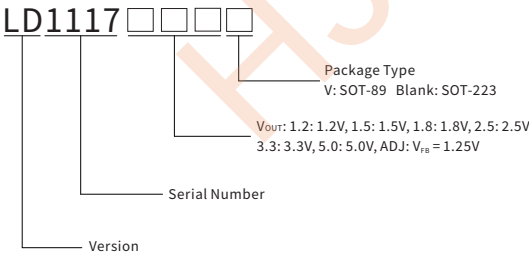
General Description

LD1117 Series are low-dropout three-terminal Linear regulators with a dropout of 1.15V(typ.)at 1A output current. They are divided into two versions, fixed voltage output version and adjustable voltage output version. The fixed output voltage is 1.2V, 1.5V, 1.8V, 2.5V, 3.3V, 5.0V and the adjustable version can provide the output voltage from 1.25V to 12V with only 2 external resistors.

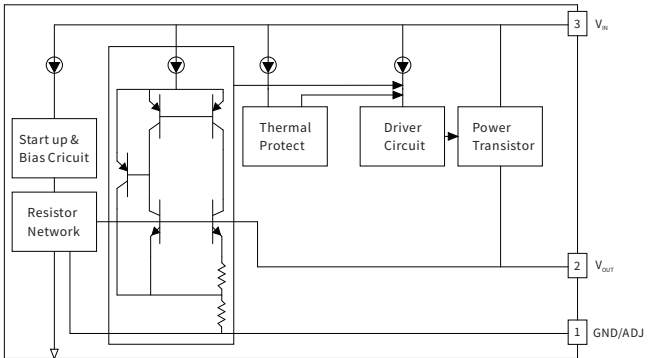
Reference News

SOT-89 Marking:	SOT-223 Marking:
LD1117-ADJV: 1117-Adj	LD1117-ADJ: 1117-ADJ
LD1117-1.2V: 1117-1.2	LD1117-1.2: 1117-1.2
LD1117-1.5V: 1117-1.5	LD1117-1.5: 1117-1.5
LD1117-1.8V: 1117-1.8	LD1117-1.8: 1117-1.8
LD1117-2.5V: 1117-2.5	LD1117-2.5: 1117-2.5
LD1117-3.3V: 1117-3.3	LD1117-3.3: 1117-3.3
LD1117-5.0V: 1117-5.0	LD1117-5.0: 1117-5.0

Part Numbering



Block Diagram

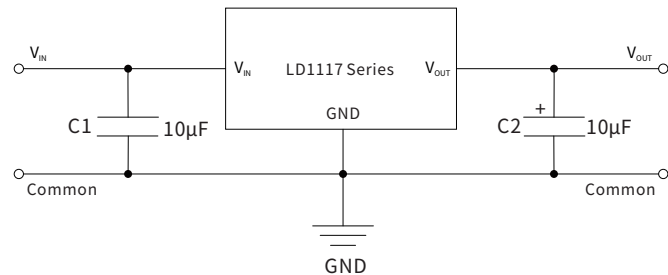


Ordering Information

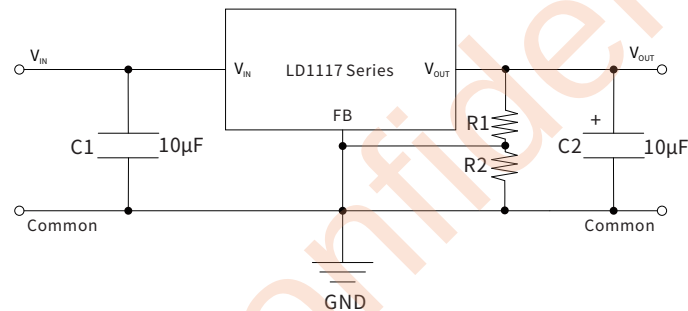
Package	Package Code	Unit Weight(g)	Reel(pcs)	Box(pcs)	Carton(pcs)	Delivery Mode	MSL
SOT-89	R1	0.045	1000	8000	48000	7"	3
SOT-223	R3	0.231	3000	6000	30000	13"	3

● Typical Application Circuit

Fixed Output Voltage Version



Adjustable Fixed Output Voltage Version



$$V_{OUT}=V_{REF} \times (1+R2/R1)+I_{ADJ} \times R2$$

The output voltage of adjustable version follows the equation:

$$V_{OUT}=1.25 \times (1+R2/R1) + I_{ADJ} \times R2.$$

We can ignore I_{ADJ} because I_{ADJ} (approximately 50uA) is much less than the current of R1 (approximately 2~10mA).

(1) To meet the minimum load current (>10mA) requirement, R1 is recommended to be 125Ω or lower. As LD1117XX-ADJ can keep itself stable at load current about 2mA, R1 is not allowed to be higher than 625Ω.

(2) Using a bypass capacitor (C_{ADJ}) between the FB pin and ground can improve ripple rejection. This bypass capacitor prevents ripple from being amplified as the output voltage is increased. The impedance of C_{ADJ} should be less than R1 to prevent ripple from being amplified. As R1 is normally in the range of 100Ω~500Ω, the value of C_{ADJ} should satisfy this equation:

$$1/(2\pi \times \text{fripple} \times C_{ADJ}) < R1$$

● Absolute Maximum Ratings ($T_A = 25^{\circ}\text{C}$ Unless otherwise specified)⁽¹⁾⁽²⁾

Parameter	Symbol	Value	Unit
Input Voltage Limit	V_{IN}	15	V
Operating Current	I_O	1000	mA
Thermal Resistance Junction-Ambient	$R_{\theta JA}$	100	$^{\circ}\text{C} / \text{W}$
Solder Temperature/Time	T_d	260/10	$^{\circ}\text{C} / \text{S}$
Operating Ambient Temperature	T_A	+85	$^{\circ}\text{C}$
Junction and Storage Temperature	T_J, T_{stg}	-40 to +125	$^{\circ}\text{C}$

LD1117 Series

Low Dropout Linear Regulator

Note:

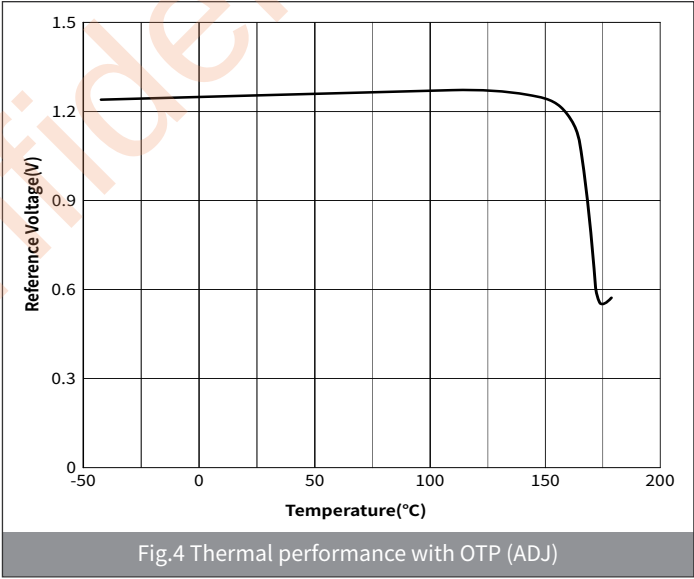
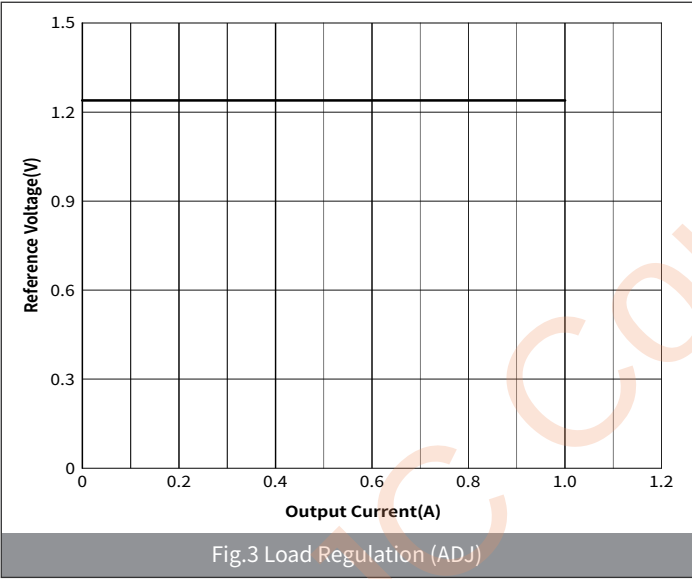
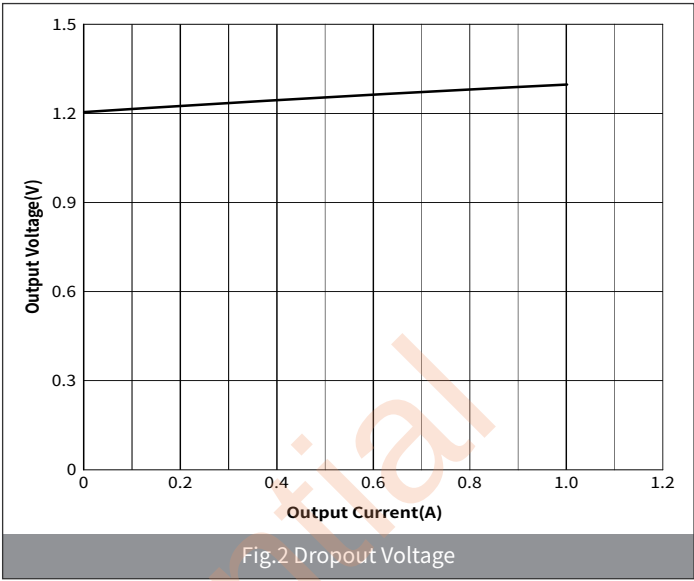
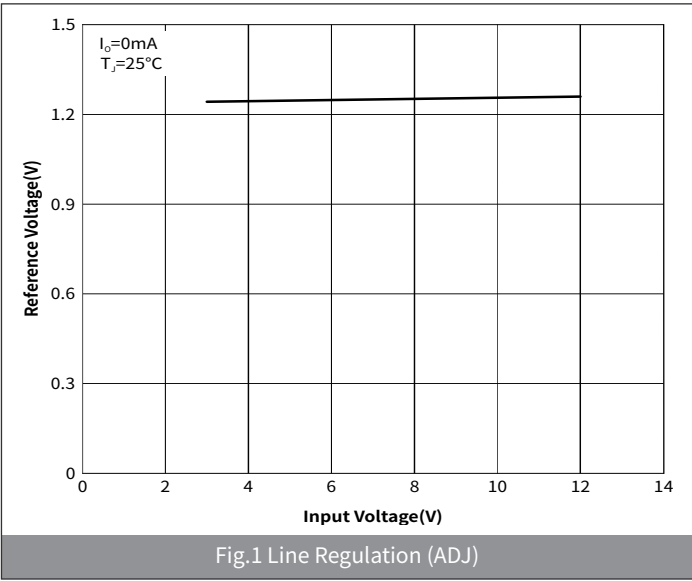
(1) These are stress ratings only. Stresses exceeding the range specified under Absolute Maximum Ratings may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

(2) The Power Dissipation is : $P_D = (T_{J(MAX)} - T_C) / R_{\theta JC}$.

● Electrical Characteristics ($V_I \leq 10V$, $T_J = 25^\circ C$ Unless otherwise specified)

Parameter	Symbol	Part No.	Conditions	Min.	Typ.	Max.	Unit
Reference Voltage	V_{Ref}	ADJ	$I_{OUT} = 10mA$ $V_{IN} = V_{OUT} + 2V$	1.231	1.25	1.268	V
			$I_{OUT} = 10mA$ to 1A $V_{IN} = V_{OUT} + 2V$	1.225	1.25	1.275	
Output Voltage	V_{OUT}	1.2 to 5.0V	$I_{OUT} = 10mA$ $V_{IN} = V_{OUT} + 2V$	$V_{OUT} \times 0.985$	V_{OUT}	$V_{OUT} \times 1.015$	V
			$I_{OUT} = 10mA$ to 1A $V_{IN} = V_{OUT} + 2V$	$V_{OUT} \times 0.98$	V_{OUT}	$V_{OUT} \times 1.02$	
Line Regulation	LNR	ADJ	$I_{OUT} = 10mA$ $V_{IN} = V_{OUT} + 1.5V$ to 10V	--	5	30	mV
		1117-1.2	$I_{OUT} = 10mA$ $V_{IN} = V_{OUT} + 1.5V$ to 12V	--	4	20	mV
		1117-1.5	$I_{OUT} = 10mA$ $V_{IN} = V_{OUT} + 1.5V$ to 12V	--	4	30	
		1117-1.8	$I_{OUT} = 10mA$ $V_{IN} = V_{OUT} + 1.5V$ to 12V	--	5	40	
		1117-2.5	$I_{OUT} = 10mA$ $V_{IN} = V_{OUT} + 1.5V$ to 12V	--	8	50	
		1117-3.3	$I_{OUT} = 10mA$ $V_{IN} = V_{OUT} + 1.5V$ to 12V	--	9	60	
		1117-5.0	$I_{OUT} = 10mA$ $V_{IN} = V_{OUT} + 1.5V$ to 12V	--	10	70	
Load Regulation	LDR	1117-ADJ	$V_{IN} = V_{OUT} + 1.5V$ $I_{OUT} = 10mA$ to 1A	--	2	25	mV
		1117-1.2		--	2	24	
		1117-1.5		--	2	30	
		1117-1.8		--	3	36	
		1117-2.5		--	4	50	
		1117-3.3		--	6	66	
		1117-5.0		--	9	100	
Dropout Voltage	V_D	LD1117 Series	$I_{OUT} = 100mA$, $\Delta V_R = 1\%$	--	1.2	1.3	V
			$I_{OUT} = 1A$, $\Delta V_R = 1\%$	--	1.3	1.5	
Quiescent Current	I_Q	LD1117-1.2	$V_{IN} = 10V$	--	2	5	mA
		1.5V to 5.0V	$V_{IN} = 12V$	--	2	5	mA
Min Load Current	I_L	LD1117-ADJ	$V_{IN} = 5V$, $V_{ADJ} = 0V$	--	2	10	mA
Adjust Pin Current	I_{ADJ}	LD1117-ADJ	$V_{IN} = 5V$, $I_{OUT} = 10mA$ to 1A	--	55	120	uA
Adjust Current Change	ΔI_{ADJ}	LD1117-ADJ	$V_{IN} = 5V$, $I_{OUT} = 10mA$ to 1A	--	0.2	10	uA
Temperature Coefficien	LD1117 Series		--	--	± 100	--	PPM

● Ratings And Characteristics Curves (T_A = 25°C Unless otherwise specified)



LD1117 Series

Low Dropout Linear Regulator

Package Outline Dimensions (SOT-89)

Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	4.40	4.60	0.173	0.181
B	1.35	1.83	0.053	0.072
C	3.75	4.25	0.148	0.167
D	2.29	2.60	0.090	0.102
E	2.95	3.05	0.116	0.120
a	0.35	0.48	0.014	0.019
b	0.40	0.56	0.016	0.022
L	0.80	1.20	0.031	0.047
G	0.35	0.44	0.014	0.017
H	1.40	1.60	0.055	0.063

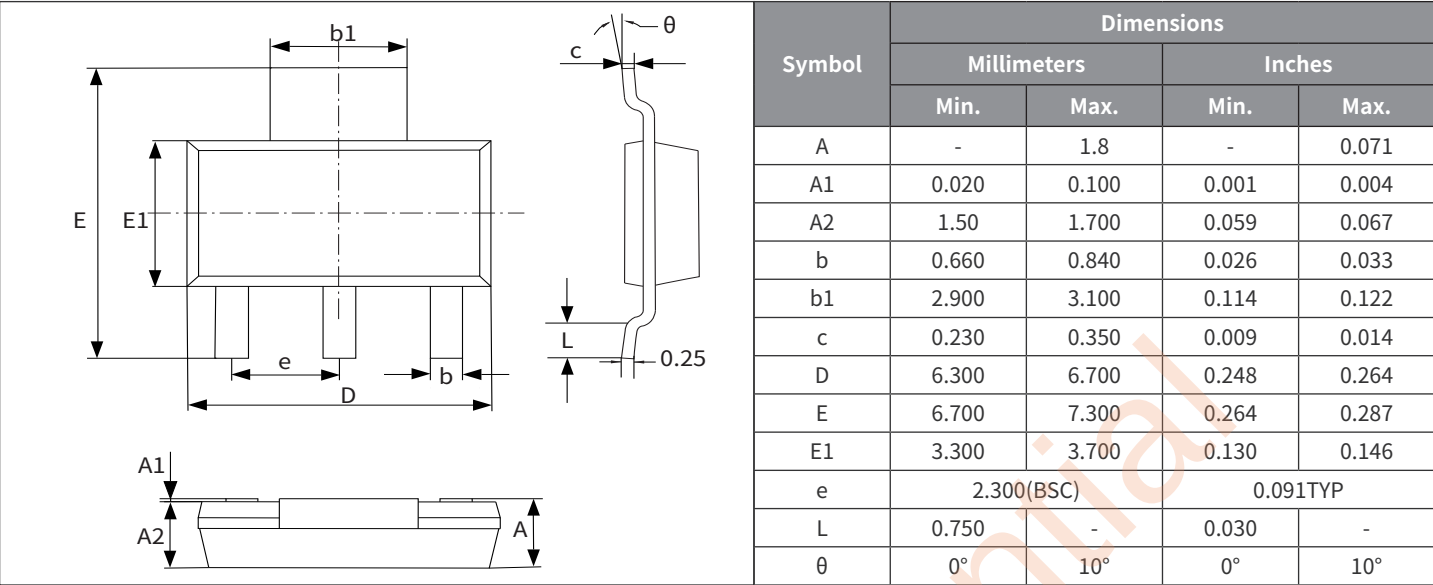
Suggested Pad Layout

Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
c	0.80	1.00	0.032	0.04
d	1.30	1.50	0.052	0.060
e	0.70	0.90	0.028	0.036
J	1.80	2.00	0.072	0.080
K	1.40	1.60	0.056	0.064
X	2.50	2.70	0.100	0.108
X1	1.30	1.50	0.052	0.060
Y	4.30	4.50	0.172	0.180
Y1	3.10	3.30	0.124	0.132
θ	-	45°	-	45°

LD1117 Series

Low Dropout Linear Regulator

Package Outline Dimensions (SOT-223)



Suggested Pad Layout

