

1

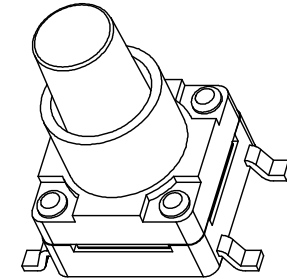
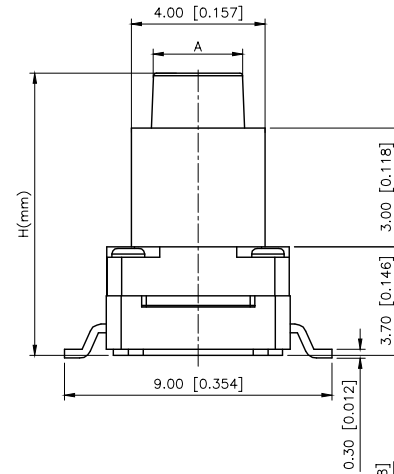
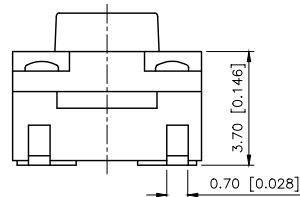
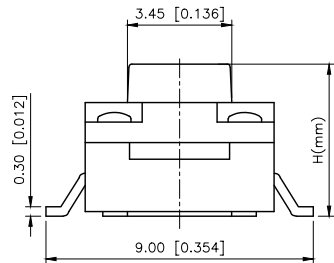
2

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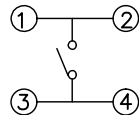
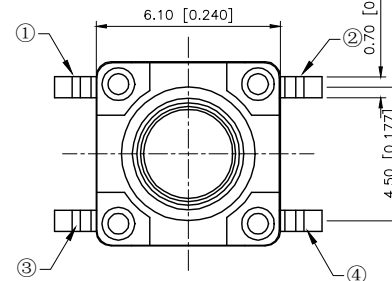
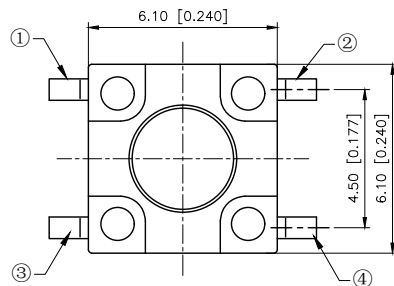
REVISIONS

REV.	ECN NO.	LOCATION	DESCRIPTION	DATE	DESIGN
A0	N/A		INITIAL RELEASE	/	/
A1	N/A		图面升级整理图面	2025.06.07	阿树

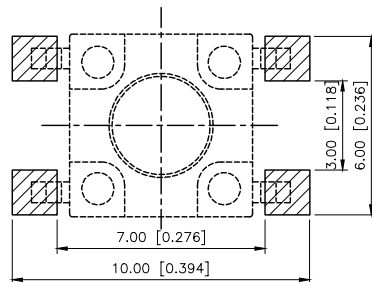


Code-x	H
A	4.30
B	4.50
C	5.00
D	5.50
E	6.00
F	6.50
G	7.00
H	7.50
I	8.00
J	8.50
K	9.00

Code-x	H
L	9.50
M	10.00
N	10.50
O	11.00
P	11.50
Q	12.00
R	12.50
S	13.00
T	13.50
U	14.00
V	15.00
W	15.50
X	16.00
Y	16.50
Z	17.00
18	18.00
19	19.00



CIRCUIT DIAGRAM



PCB LAYOUT PLAN

SPECIFICATION

Contact Rating: 50mA,12V DC
Contact Resistance: 100mΩ max
Insulation Resistance: 100MΩ min.500V DC
Dielectric Strength: 250V AC/1minute
Travel: 0.25mm
Operating Temp: -30℃~+85℃
Storage Temp: -30℃~+80℃

PAPT NO	TS-FS66T-C-D
Operating Force	A:100gf B:160gf C:260gf
Height	Code-x

ITEM	PAPT NAME	QTY	MATERIAL	FINISHING
⑤	Terminal	4	Brass (0.30t)	Plating silvering
④	Contact	1	F.ag/SUS	Natural
③	Base	1	PPA	
②	Cover	1	PPA	
①	Keystake	1	PPA	

AuGLAR	±5°
X.	±0.40
X.x	±0.30
X.xx	±0.20
X.xxx	±0.15
UNSPECIFIED TOLERANCES	



DESIGN	阿树	VIEW:	MODEL TYPE:
CHECKED		UNIT: mm/in	TACT SWITCH
APPROVED		SIZE: A4	PART NO.: TS-FS66T-C-D
			DWG NO.: TS-FS66T-C-D
SCALE	PAGE	REVISION	
N/A	1/1	A1	

1

2

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