

AO3404A

30V N Channel Enhancement MOSFET

Description

The AO3404A is the new generation trench N-channel MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high efficiency power management applications

Features

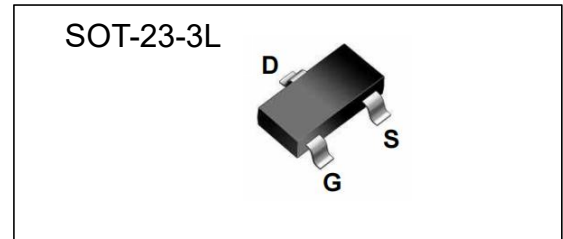
- 30V, 6.5A, $R_{DS(ON)} = 17m\Omega @ V_{GS} = 10V$
- Improved dv/dt capability
- Fast switching

Applications

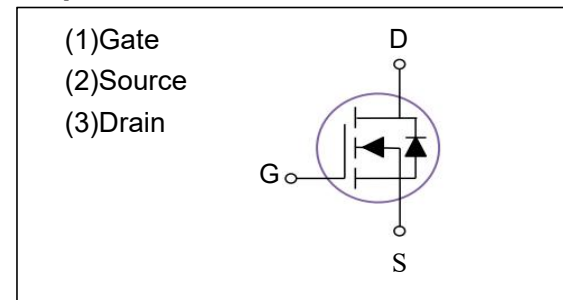
- MB / VGA / Vcore
- Load Switch
- Power Management

V_{DSS}	30V
I_D	6.5A
$R_{DS(ON)}$	$24m\Omega @ V_{GS}=4.5V$
$R_{DS(ON)}$	$17m\Omega @ V_{GS}=10V$

Outline



Equivalent



Packaging specifications

Part No.	Package	Marking	Basic ordering unit.(pcs)
AO3404A	SOT-23-3L	X4**	3000

Absolute Maximum Ratings

Parameter		Symbol	Limit	Units
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	V
Drain Current-Continuous(Note2)	$T_C=25^\circ C$	I_D	6.5	A
	$T_C=70^\circ C$		5.3	A
-Pulsed(Note 1· Note 2)		I_{DM}	26	A
Maximum Power Dissipation	$T_C=25^\circ C$	P_D	1.6	W
	$T_C=70^\circ C$		1	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ C$
Thermal Resistance Junction-Ambient		$R_{\theta JA}$	62	$^\circ C / W$

AO3404A

Electrical Characteristics (T_c=25 °C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V , I _D =250uA	30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V , V _{GS} =0V			1	uA
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V , V _{DS} =0V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250uA	1.2	1.7	2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V , I _D =5A		24	35	mΩ
		V _{GS} =10V , I _D =6A		17	25	mΩ
DYNAMIC CHARACTERISTICS Note4						
Input Capacitance	C _{ISS}	V _{DS} =25V , V _{GS} =0 V , f =1.0MHz		346		pF
Output Capacitance	C _{OSS}			55		pF
Reverse Transfer Capacitance	C _{RSS}			33		pF
Total Gate Charge	Q _g	V _{DS} =15V , I _D =6A , V _{GS} =4.5V		4.2		nC
Gate-Source Charge	Q _{gs}	V _{DS} =15V , I _D =6A , V _{GS} =4.5V		1.0		nC
Gate-Drain Charge	Q _{gd}			2.2		nC
SWITCHING CHARACTERISTICS Note4						
Turn-On Delay Time	t _{D(ON)}	V _{DD} =15V , I _D =1A , V _{GS} =4.5V , R _G =6Ω		2.8		ns
Rise Time	tr			7.3		ns
Turn-Off Delay Time	tD(OFF)			16		ns
Fall Time	tf			4.8		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Diode Forward Voltage	V _{SD}	V _{GS} =0V , I _S =1A			1	V

Notes:

- 1、 Pulse Test Width < 300us,Duty Cycle< 2%
- 2、 Drain current limited by maximum junction temperature.
- 3、 Guaranteed by design,not subject to production testing.

Typical Performance Characteristics

Fig.1 Continuous Drain Current vs. TC

T_C , Case Temperature ($^{\circ}\text{C}$)

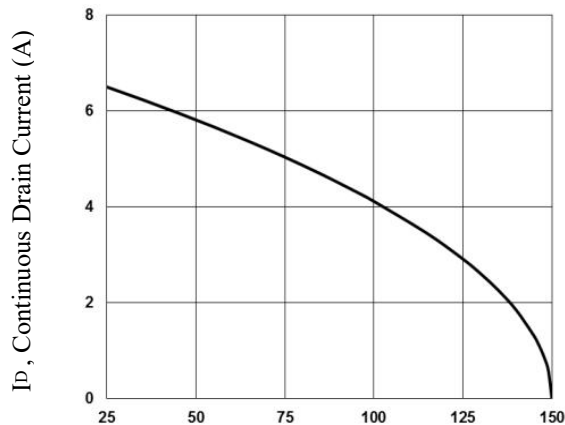


Fig.2 Normalized $R_{DS(on)}$ vs. T_J

T_J , Junction Temperature ($^{\circ}\text{C}$)

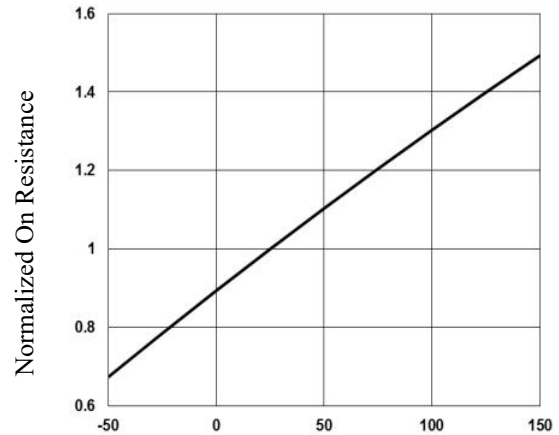


Fig.3 Normalized V_{th} vs. T_J

T_J , Junction Temperature ($^{\circ}\text{C}$)

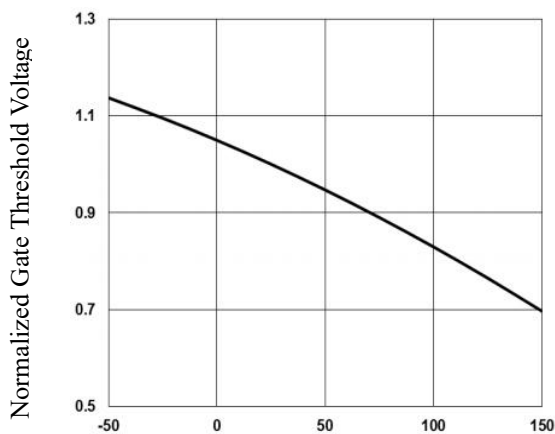


Fig.4 Gate Charge Waveform

Q_g , Gate Charge (nC)

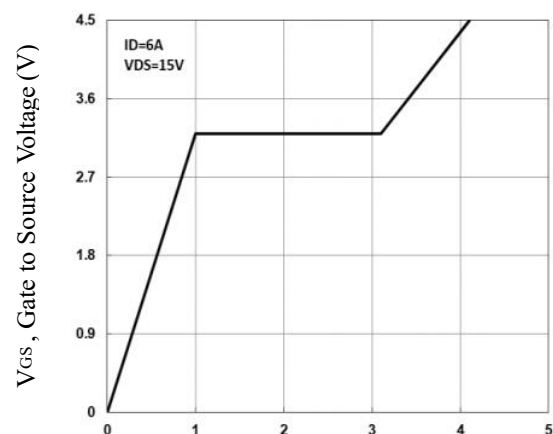


Fig.5 Typical Output Characteristics

V_{DS} , Drain to Source Voltage (V)

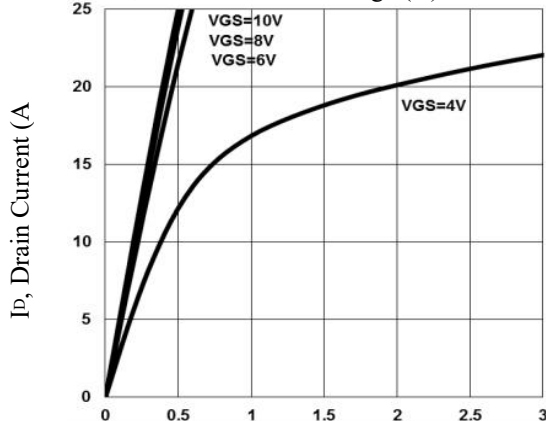


Fig.6 Turn-On Resistance vs. I_D

I_D , Drain Current (A)

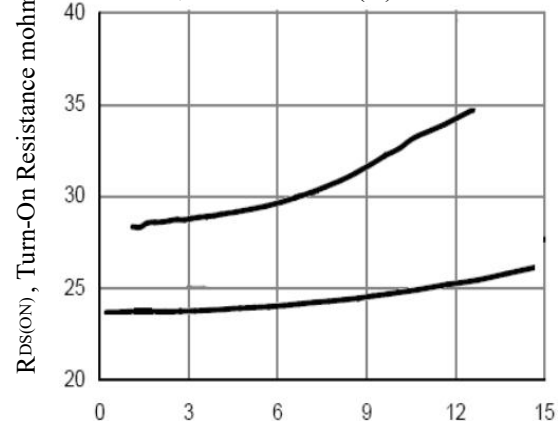


Fig.7 Maximum Safe Operation Area

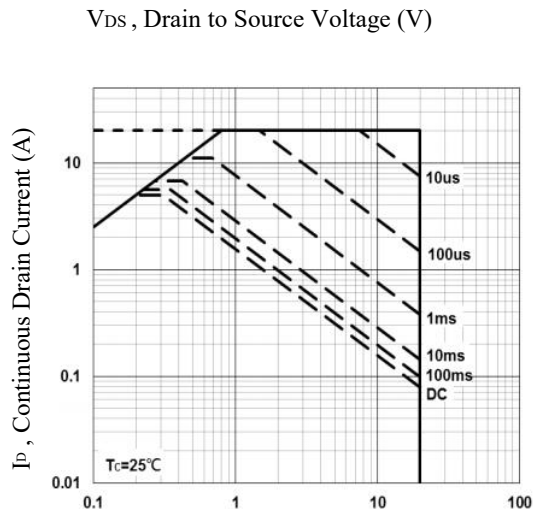


Fig.8 Normalized Transient Impedance

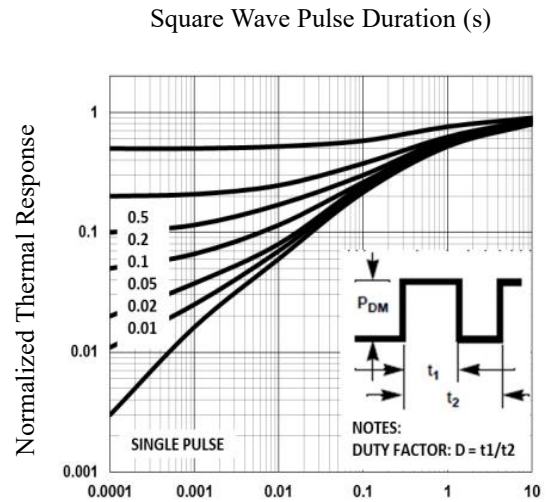


Fig.9 Switching Time Waveform

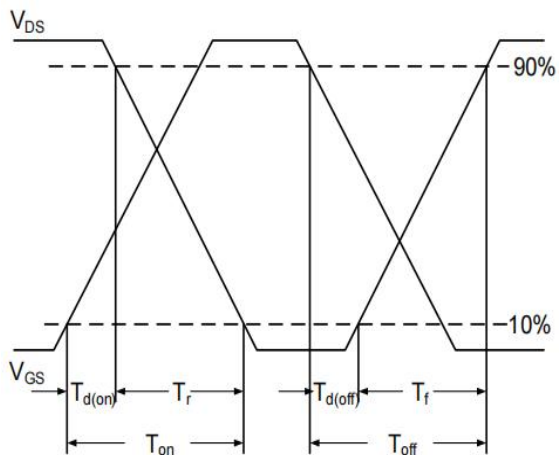
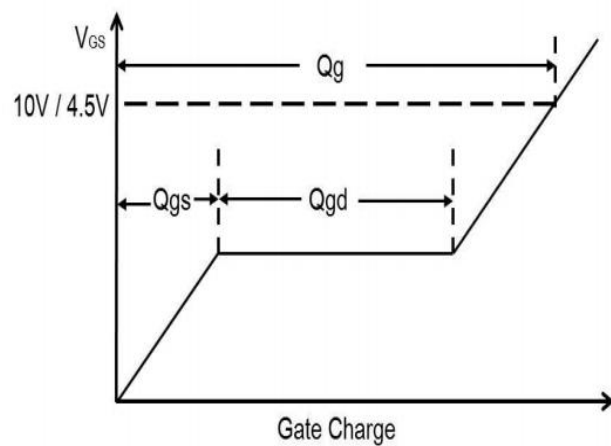
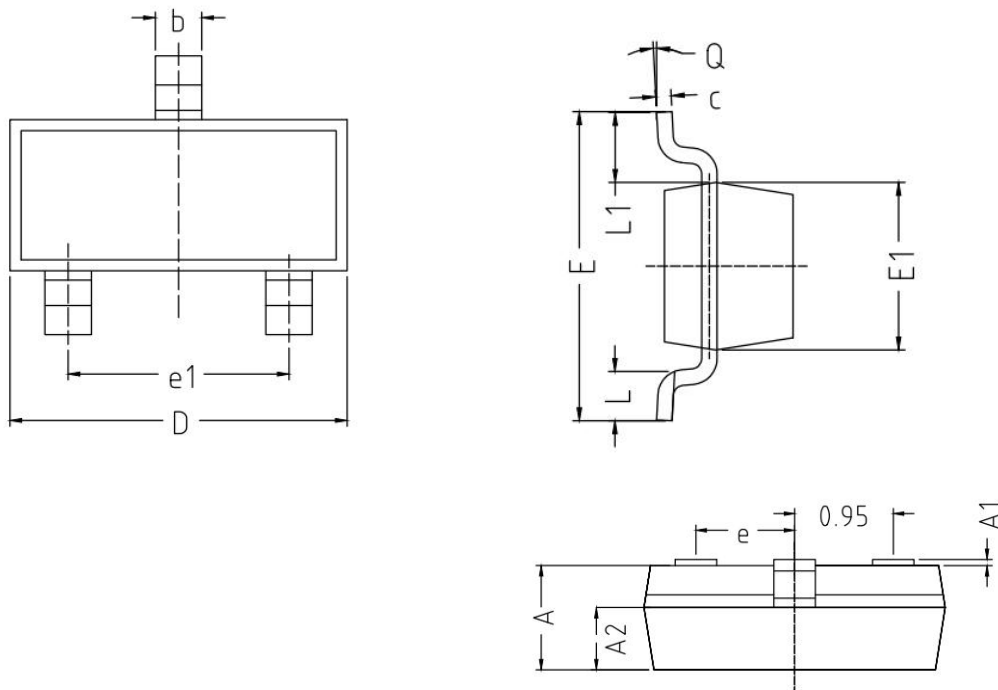


Fig.10 Gate Charge Waveform



SOT-23-3L Package Information



SOT-23-3LPKG

SYMBOL	MIN	MAX
A	1.050	1.100
A1	0.000	0.100
A2	1.000	1.050
b	0.300	0.450
c	0.100	0.200
D	2.800	3.000
E	2.650	2.750
E1	1.500	1.600
e	0.90	0.95
e1	1.800	2.00
L	0.35	0.450
L1	0.40	0.45
Q	8°	9°