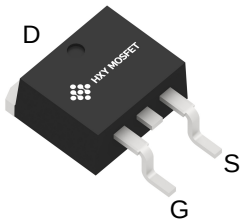
CSD19535KTT

N-SGT Enhancement Mode MOSFET

General Description

The CSD19535KTT use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge,fast switching and excellent avalanche characteristics.This device is specially designed to get better ruggedness and suitable.

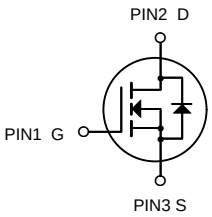


TO-263

General Features

V_{DS} =100V I_D =260A

$R_{DS(ON)}$ < 4.2mΩ @ V_{GS}=10V



N-Channel MOSFET

Applications

Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications

Ordering Information

Product ID	Pack	Brand	Qty(PCS)
CSD19535KTT	TO-263	HXY MOSFET	800

Absolute Maximum Ratings at T_j=25°C unless otherwise noted

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T_C=25°C	I_D	260	A
	T_C=100°C		163	
Pulsed Drain Current[1]		I_{DM}	1028	A
Single Pulse Avalanche Energy[2]		EAS	583	mJ
Total Power Dissipation	T_C=25°C	P_D	379	W
Operating Junction and Storage Temperature Range		T_J , T_{STG}	-55 to 150	°C
Thermal Resistance from Junction-to-Ambient[3]		$R_{\theta JA}$	59	°C/W
Thermal Resistance from Junction-to-Case		$R_{\theta JC}$	0.33	°C/W

Electrical Characteristics（T_J = 25°C, unless otherwise noted）

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	100	-	-	V
Gate-body Leakage current		I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T_J=25°C	I_{DSS}	V_{DS} =100V, V_{GS} = 0V	-	-	1	μA
	T_J=100°C			-	-	100	
Gate-Threshold Voltage		$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	2	3	4	V
Drain-Source on-Resistance[4]		$R_{DS(on)}$	V_{GS} = 10V, I_D = 20A	-	3.4	4.2	mΩ
Forward Transconductance[4]		g_{fs}	V_{DS}=10V, I_D=20A	-	76	-	S
Dynamic Characteristics[5]							
Input Capacitance		C_{iss}	V_{DS} = 50V, V_{GS} =0V, f =1MHz	-	9030	-	pF
Output Capacitance		C_{oss}		-	1505	-	
Reverse Transfer Capacitance		C_{rss}		-	40	-	
Gate Resistance		R_g	f =1MHz	-	2.3	-	Ω
Switching Characteristics[5]							
Total Gate Charge		Q_g	V_{GS} = 10V, V_{DS} = 50V, I_D=20A	-	150	-	nC
Gate-Source Charge		Q_{gs}		-	32.5	-	
Gate-Drain Charge		Q_{gd}		-	49	-	
Turn-on Delay Time		$t_{d(on)}$	V_{GS} =10V, V_{DD} =50V, R_G = 3Ω, I_D= 20A	-	27	-	ns
Rise Time		t_r		-	78.5	-	
Turn-off Delay Time		$t_{d(off)}$		-	110	-	
Fall Time		t_f		-	86	-	
Body Diode Reverse Recovery Time		t_{rr}	I_F = 20A, dI/dt=100A/μs	-	88	-	ns
Body Diode Reverse Recovery Charge		Q_{rr}		-	220	-	nC
Drain-Source Body Diode Characteristics							
Diode Forward Voltage[4]		V_{SD}	I_D = 20A, V_{GS} = 0V	-	-	1.2	V
Continuous Source Current	T_C=25°C	I_S	-	-	-	260	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150°C.
2. The EAS data shows Max. rating . The test condition is V_{DD}=50V, V_{GS}=10V, L=0.4mH, I_{AS}=54A.
3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics

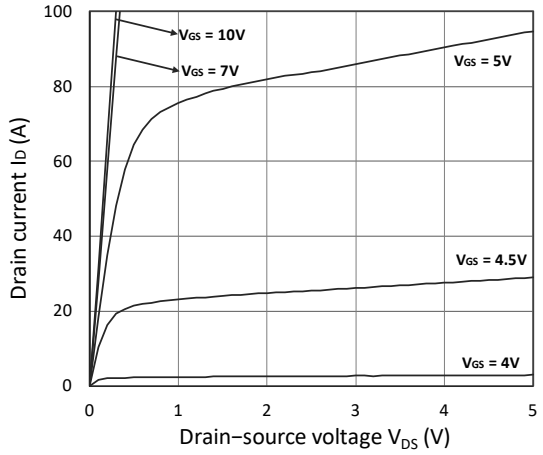


Figure 1. Output Characteristics

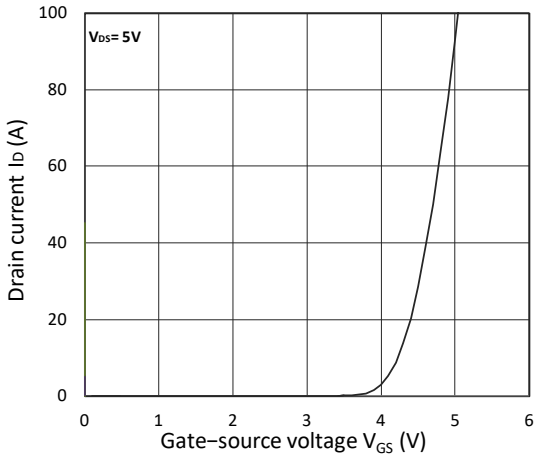


Figure 2. Transfer Characteristics

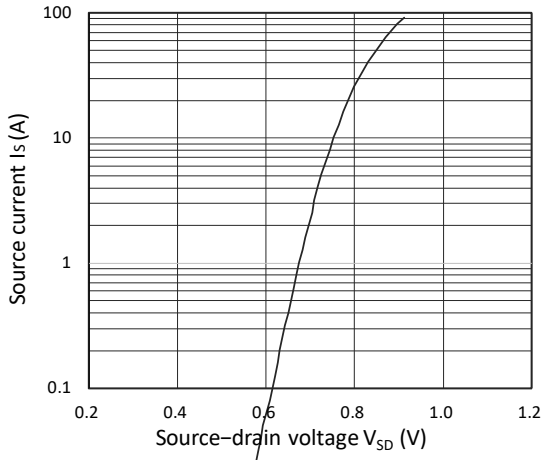


Figure 3. Forward Characteristics of Reverse

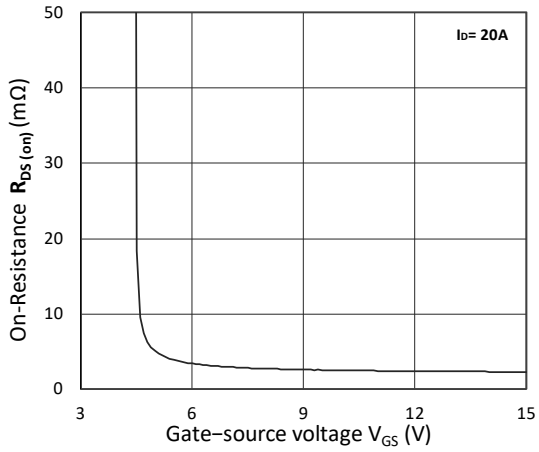


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

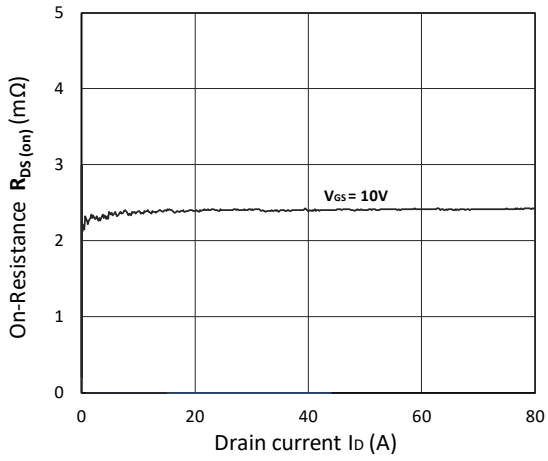


Figure 5. $R_{DS(ON)}$ vs. I_D

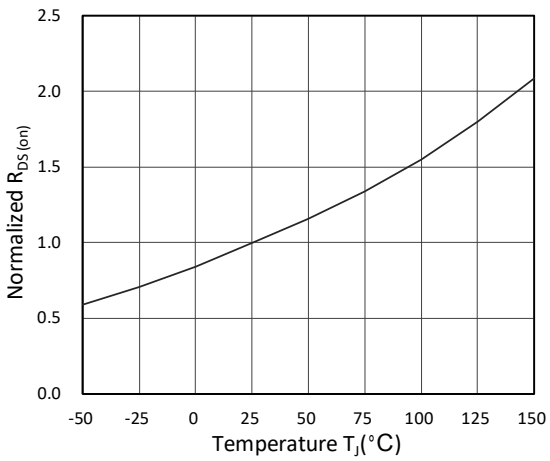


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

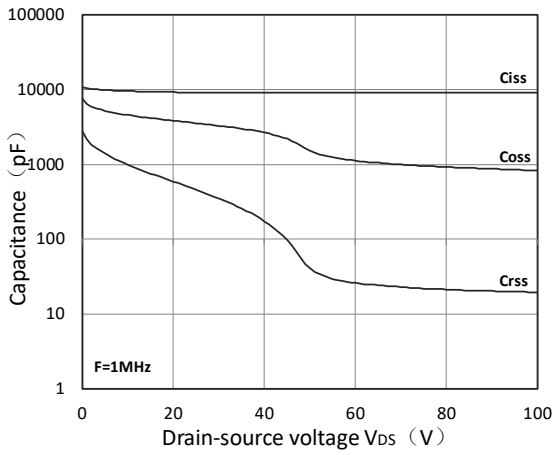


Figure 7. Capacitance Characteristics

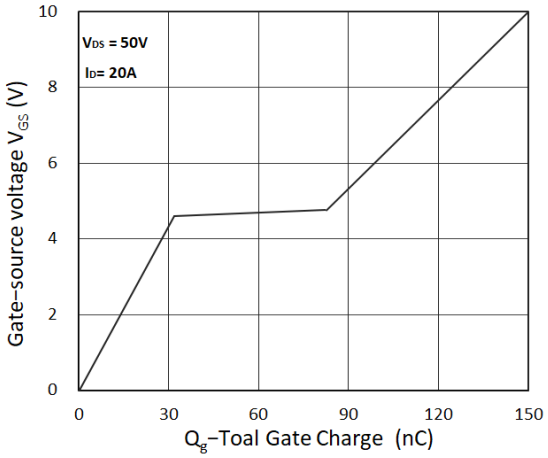


Figure 8. Gate Charge Characteristics

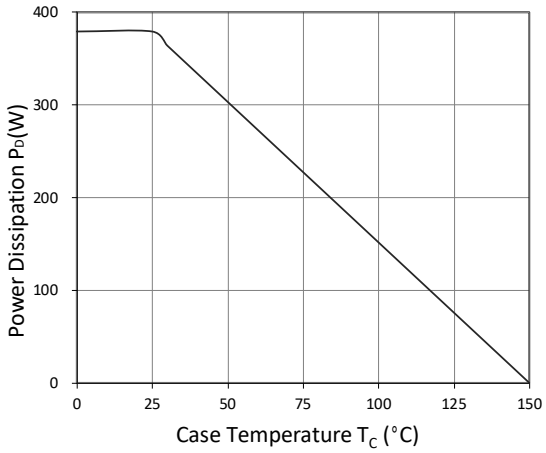


Figure 9. Power Dissipation

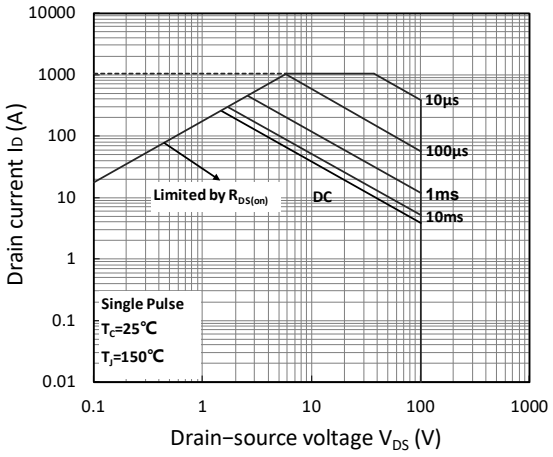


Figure10. Safe Operating Area

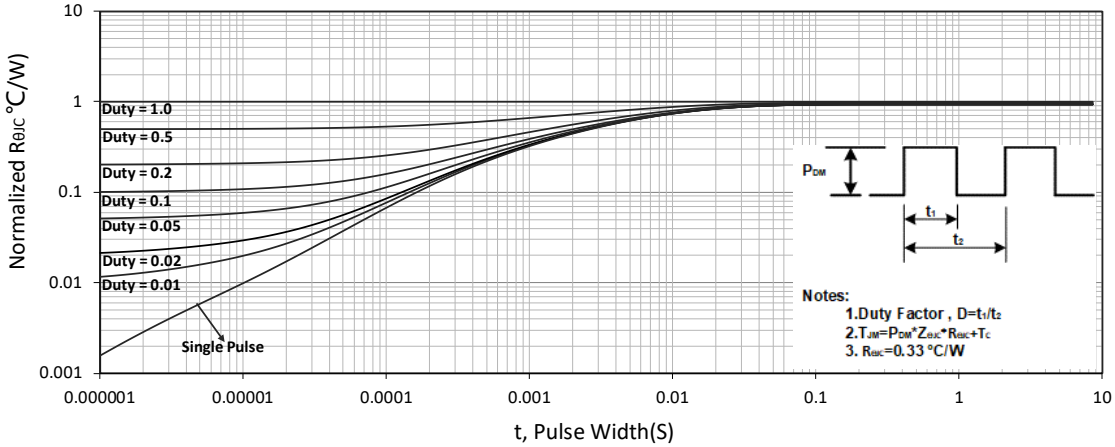


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

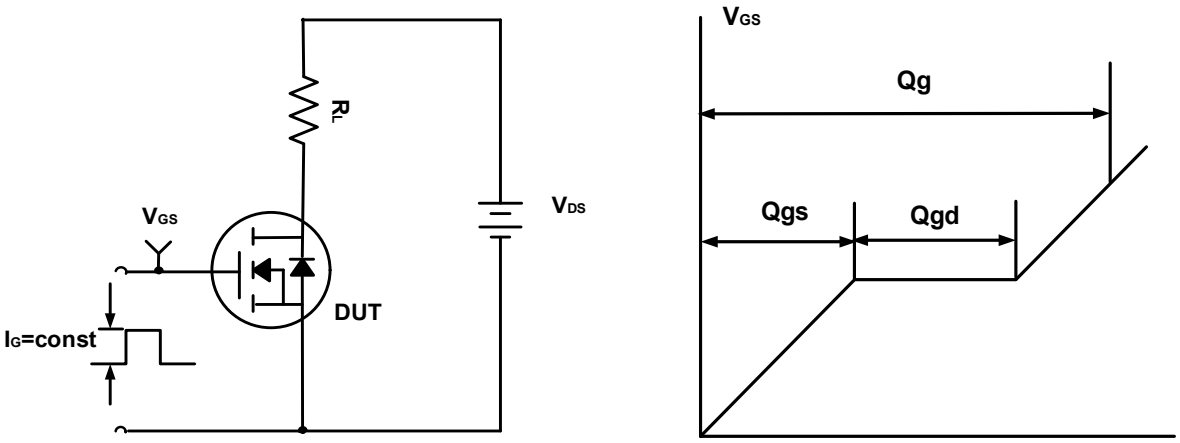


Figure A. Gate Charge Test Circuit & Waveforms

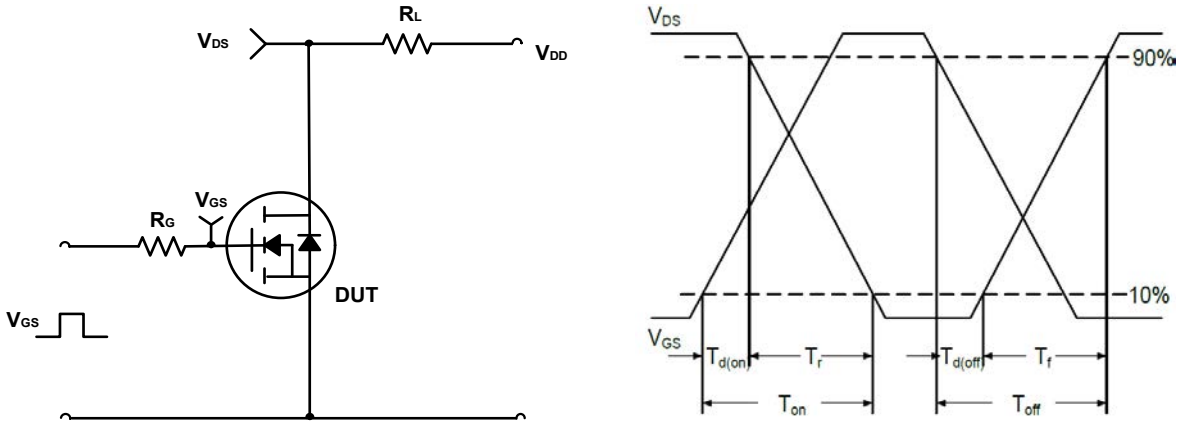


Figure B. Switching Test Circuit & Waveforms

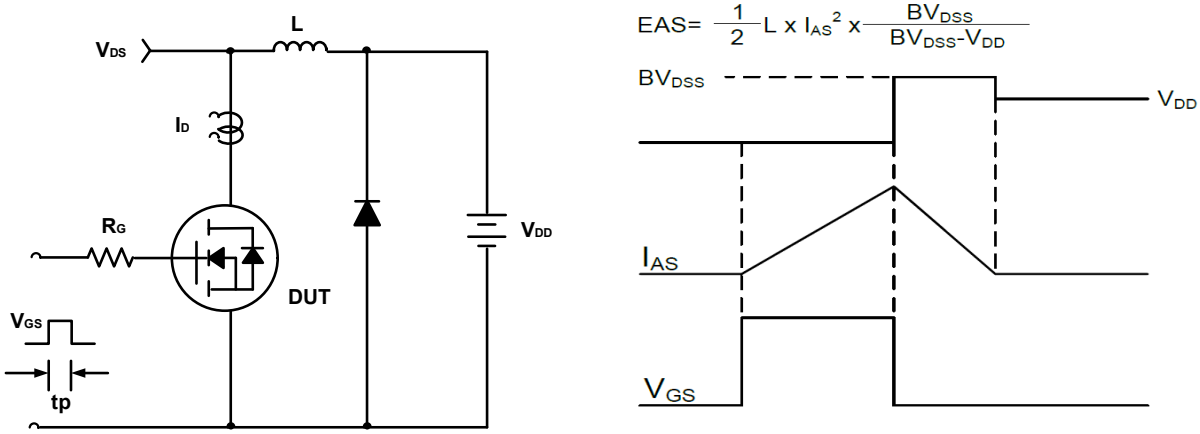
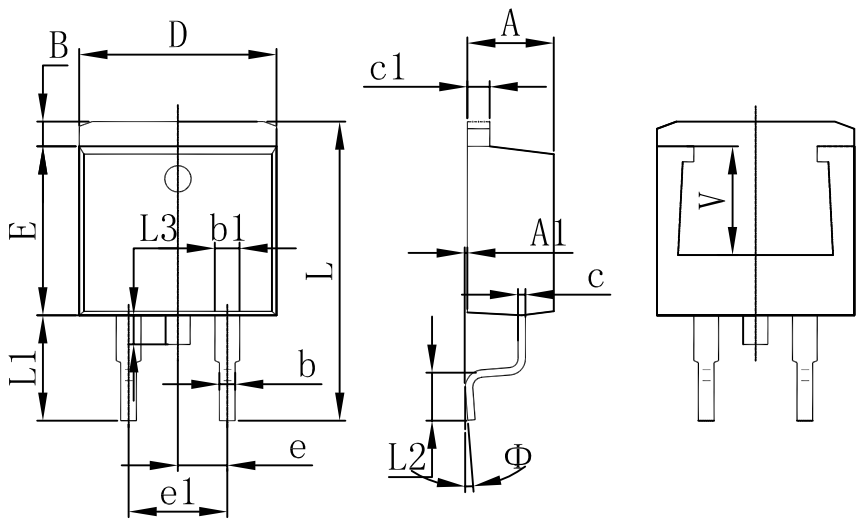


Figure C. Unclamped Inductive Switching Circuit & Waveforms

TO-263 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.470	4.670	0.176	0.184
A1	0.000	0.150	0.000	0.006
B	1.120	1.420	0.044	0.056
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
L	14.940	15.500	0.588	0.610
L1	4.950	5.450	0.195	0.215
L2	2.340	2.740	0.092	0.108
L3	1.300	1.700	0.051	0.067
Φ	0°	8°	0°	8°
V	5.600 REF.		0.220REF.	

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