

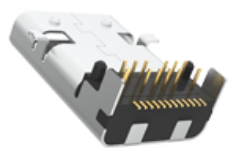
Operation: SMT+DIP 式 SMT& 3.1Type-C 插座 主体加长型



LTEM NO.: MC-313L (Consumer Electronics)

(L11.95 × W8.75 × H3.12 & DUL SMT LOW PCB TYPE)

Type-C Universal Serial Bus Connectors



邀您使用(微信)小程序

硕方电子开关&连接器选型手册

手机扫码即可进入...

Technical parameter

外焊 SMT+DIP

侧向导入 LATERAL

精密部品 NICETY

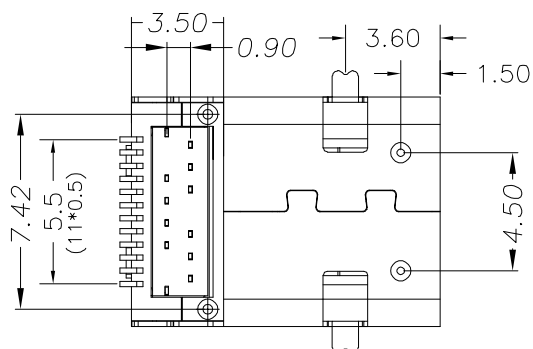
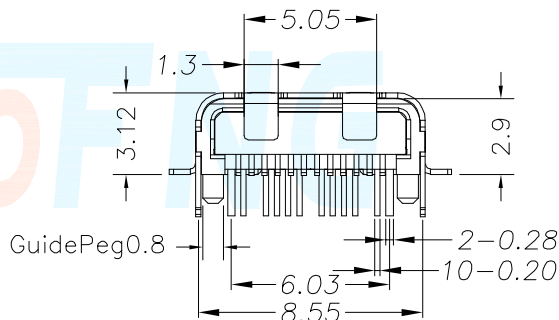
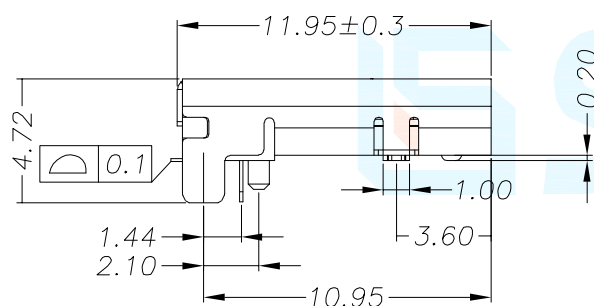
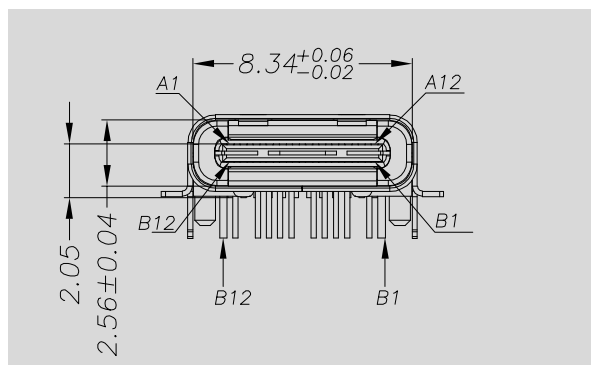
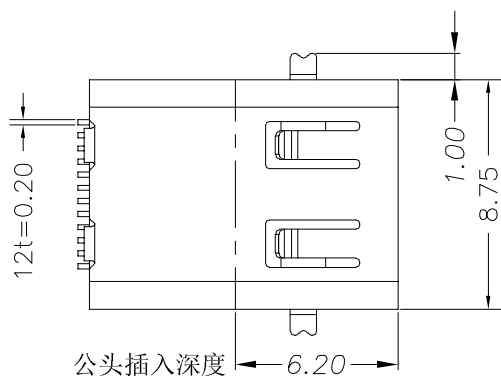
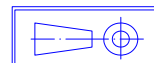
可靠 STABILIZE

环保材质 RoHS

PROJECT	LEVEL	A[better product]	B[average product]
Electrical Properties	Contact Rating	5A, 24V DC	
	Initial Contact Resistance	30mΩ max.	50mΩ max.
	Insulation Resistance	100MΩ min.500V DC	Skey/PD: 100MΩ min.300V DC
Durable Performance	Withstand Voltage	500V AC for 1 minute	350 V AC for 1 minut
	There No Load	6,500 Cycles	5,500 Cycles
	Rated Load	5,500 Cycles	4,500 Cycles
Storage temp.		-25℃~+75℃(Operating Temp:)	

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Unit:mm



Material declaration			
No.	NAME	MATERIAL	DESCRIPTION
① A	TERMINAL 接触端子	COPPER ALLOY 【G/F】	2.0 μm Ni PLATED OVERALL; 0.25 μm Au PLATED CONTACT AREA; GOLD FLASH ON SOLDER AREA.
② B	SHIELDING PLATE 屏蔽壳	STAINLESS 【G/F】	0.25 μm Ni PLATED OVERALL;
③ C	CASE	THERMOPLASTIC	UL 94V-0,COLOR:BLACK;
④ D	SHELL	STAINLESS 【G/F】	0.25 μm Ni PLATED OVERALL;
⑤ E	GUARD PLATE	STAINLESS 【G/F】	STAINLESS STEEL(t=0.10mm)

Operating Force

Inward
Exiting

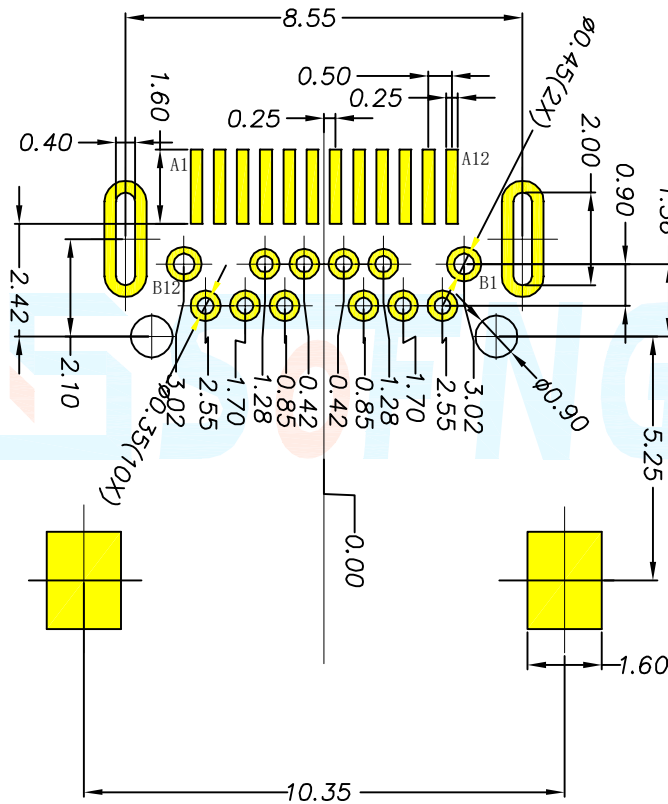
6.0~10.0N. (1N.=100gram-force)

8.0~12.0N. (1N.=100gram-force)

Solder-ability (Max.)

IR Reflow:255°C,5sec.

Manual:350°C,3sec.



PIN ASSIGNMENTS

PIN	Signal NAME	Description	PIN	Signal NAME	Description
A 1	GND	Ground return	B12	GND	Ground return
A 2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of second SuperSpeed RX differential pair
A 3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of second SuperSpeed RX differential pair
A 4	V BUS	Bus Power	B 9	V BUS	Bus Power
A 5	CC1	Configuration Channel	B 8	SBU2	Sideband Use (SBU)
A 6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B 7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A 7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B 6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A 8	SBU1	Sideband Use (SBU)	B 5	CC2	Configuration Channel
A 9	V BUS	Bus Power	B 4	V BUS	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B 3	SSTXn2	Negative half of first SuperSpeed TX differential pair2
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B 2	SSTXp2	Positive half of first SuperSpeed TX differential pair2
A12	GND	Ground return	B 1	GND	Ground return

注記 NOTICE

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