

BSD10NL200SK1

N-Channel Power MOSFET

100 V, 40 A, 20 mΩ

Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency. It has been optimized for low gate charge, low $R_{DS(on)}$, fast switching speed.

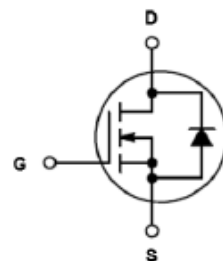
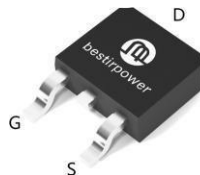
Applications

- Motor Driver
- DC/DC Converter
- Battery Management System

Features

BV_{DSS}	I_D	$R_{DS(on),max}$	$Q_{rr,typ}$
100 V	40 A	20 mΩ	20 nC

- $R_{DS(on),MAX}=5.7m\Omega@V_{GS}=10V$
- Low gate Charge (typical 66.3nC)
- Low C_{rss} (typical 28pF)
- Low On-Resistance
- Excellent FoM(figure of merit)
- 100% UIS and Rg tested



Absolute Maximum Ratings ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage		100	V
V_{GSS}	Gate to Source Voltage		± 20	V
I_D	Continuous Drain Current, ($T_C = 25^\circ\text{C}$) ¹⁾		40	A
	Continuous Drain Current, ($T_C = 100^\circ\text{C}$) ¹⁾		28	
I_{DM}	Drain Current (2)	Pulsed	160	A
E_{AS}	Single Pulsed Avalanche Energy (3)		33	mJ
P_D	Power Dissipation	($T_C = 25^\circ\text{C}$)	53	W
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case	2.35	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	70	

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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Off Characteristics

BV_{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 100\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$	-	-	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS} = \pm 20\text{ V}$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\text{ }\mu\text{A}$	1	-	2.5	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10\text{ V}, I_D = 40\text{ A}$	-	15	20	mΩ

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 50\text{V},$ $f = 1.0\text{MHz}$	-	912	-	pF
C_{oss}	Output Capacitance		-	342	-	pF
C_{rss}	Reverse transfer capacitance		-	18	-	pF
$Q_{g(tot)}$	Total Gate Charge at 10 V	$V_{GS} = 10\text{V}, V_{DD} = 50\text{V}$ $I_D = 20\text{A}$	-	16	-	nC
Q_{gs}	Gate to Source Charge		-	3.6	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	7	-	nC
R_G	Gate Resistance	$f = 1\text{ MHz}$	-	1.9	-	Ω

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}, V_{DD} = 50\text{V}$ $R_G = 3\Omega, I_D = 20\text{A}$	-	6	-	ns
t_r	Turn-On Rise Time		-	25	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	26	-	ns
t_f	Turn-Off Fall Time		-	56	-	ns

Source-Drain Diode Characteristics

I_S	Continuous Diode Forward Current		-	40	-	A
$I_{S\text{ pulse}}$	Diode Pulsed Current ²⁾		-	160	-	
V_{SD}	Diode Forward Voltage	$T_J = 25^\circ\text{C}, I_S = 20\text{A}, V_{GS} = 0\text{V}$	-	0.9	1.4	V
t_{rr}	Reverse Recovery Time	$I_F = 20\text{A}, diF/dt = 100\text{A/us}$	-	30	-	ns
Q_{rr}	Reverse Recovery Charge		-	20	-	nC

Notes

- 1) Limited by maximum junction temperature.
- 2) Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3) $L=0.1\text{mH}, R_G=25\Omega, V_{GS}=10\text{V}, V_{DD}=50\text{V},$ Start $T_J=25^\circ\text{C}$.

Typical Performance Characteristics

Figure 1. Transient Thermal Impedance

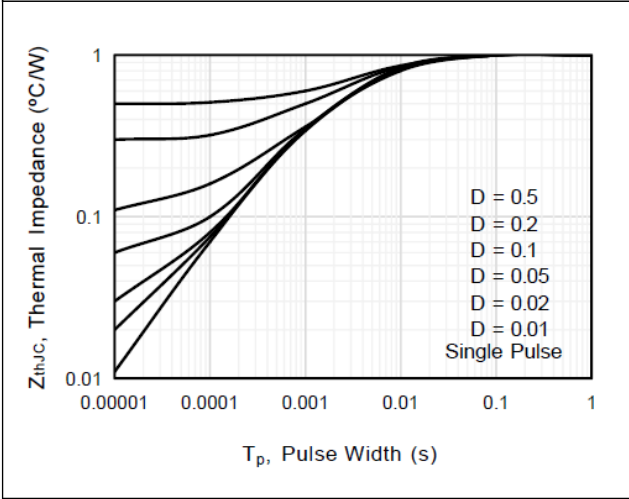


Figure 2 . Safe Operation Area

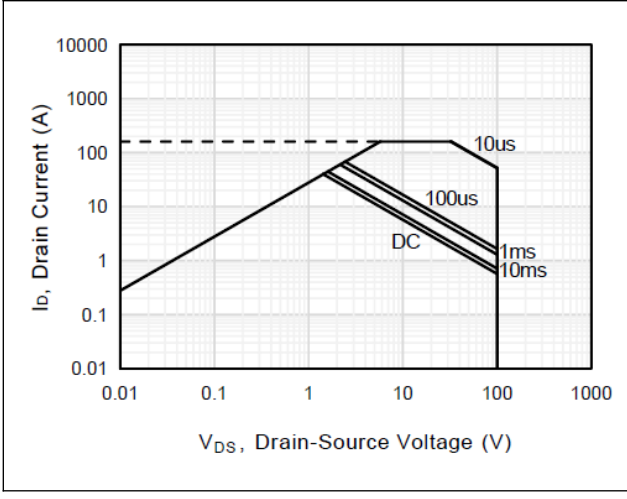


Figure 3 . Output Characteristics

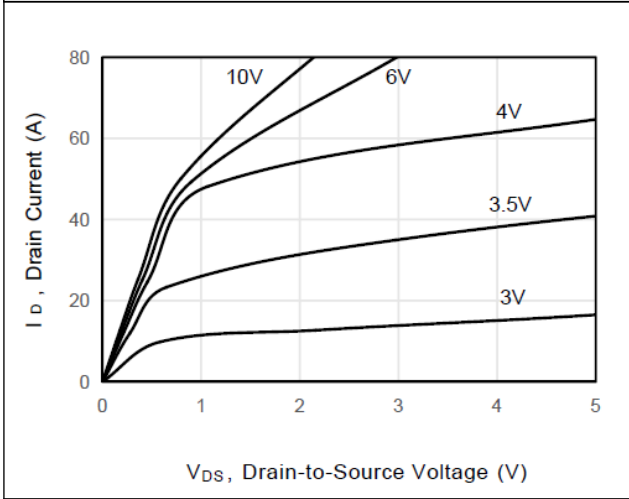


Figure 4 . Transfer Characteristics

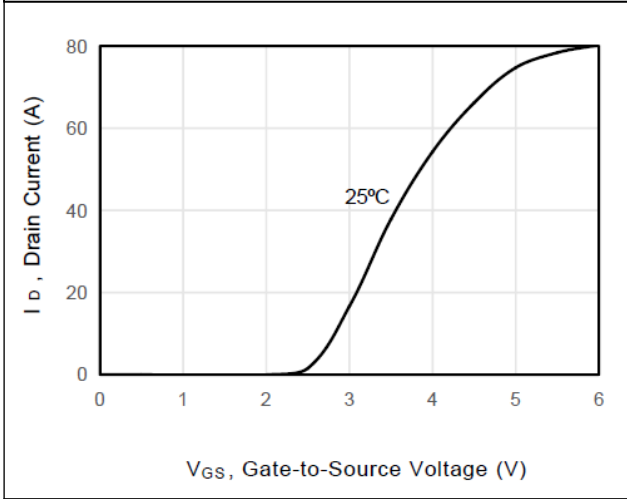


Figure 5 . On-Resistance vs Drain Current

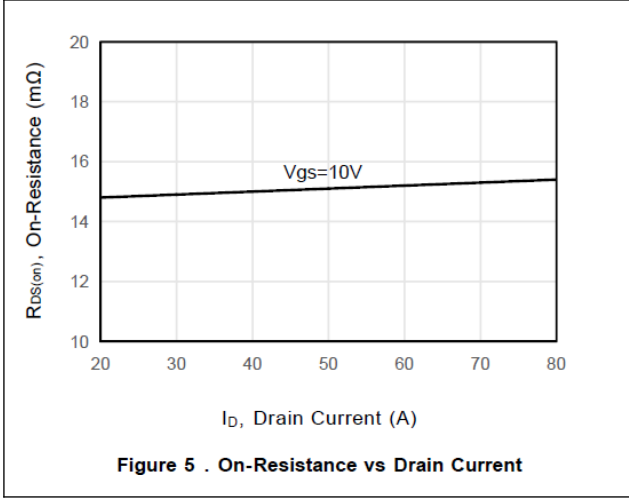
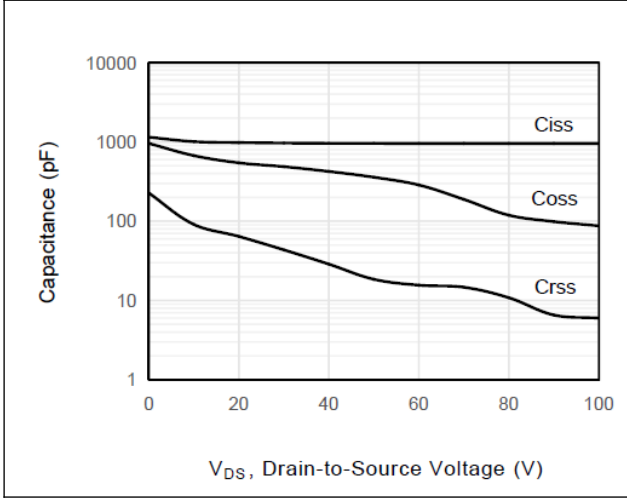


Figure 6 . Capacitance



Typical Performance Characteristics

Figure 7 . Gate Charge

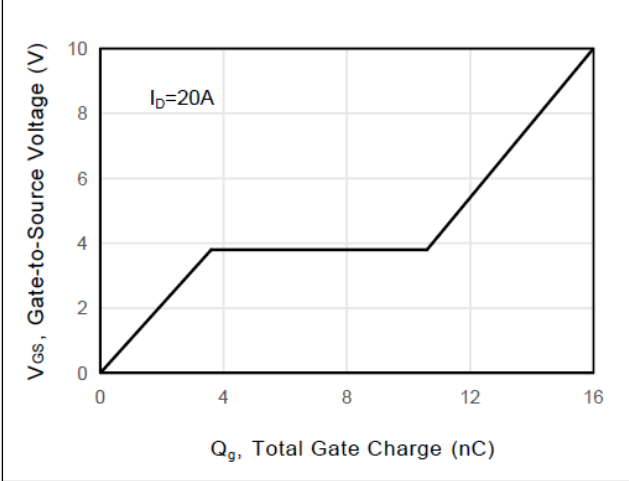


Figure 8 . Body Diode Forward Voltage

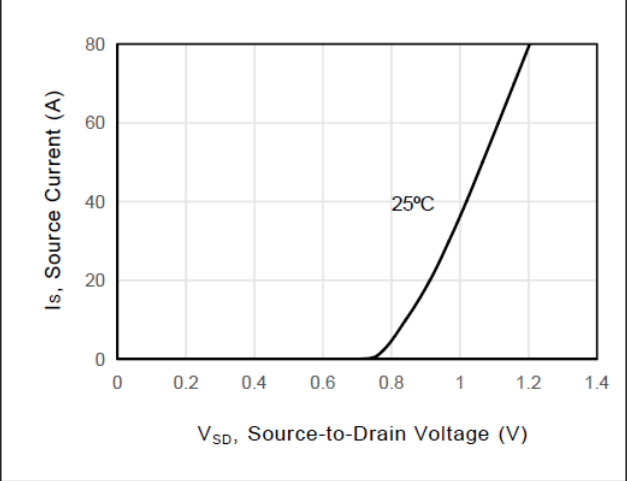


Figure 9. Breakdown Voltage vs Junction Temperature

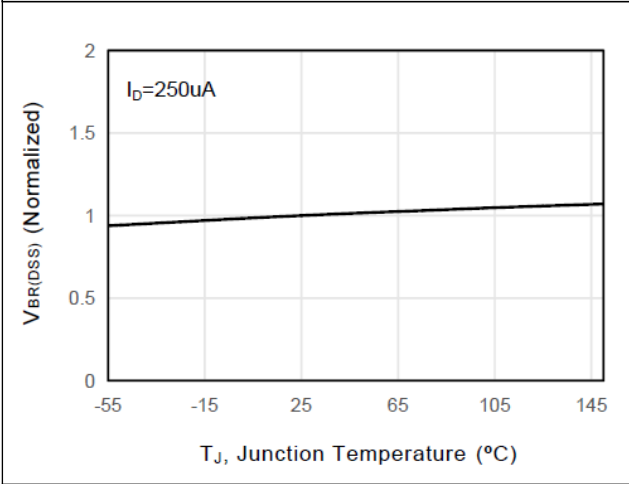


Figure 10. On-Resistance vs Temperature

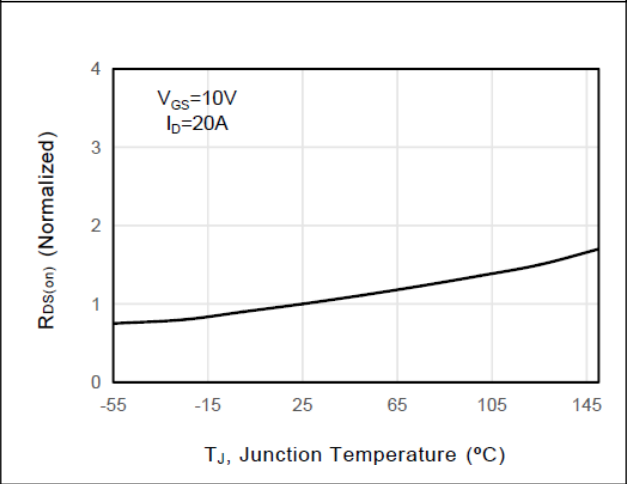
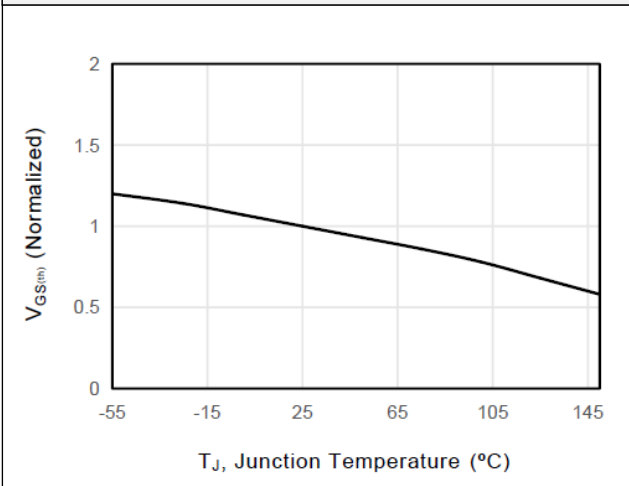


Figure 11. $V_{GS(th)}$ vs Junction Temperature



Test Circuits

Figure 12: Gate Charge Test Circuit and Waveform

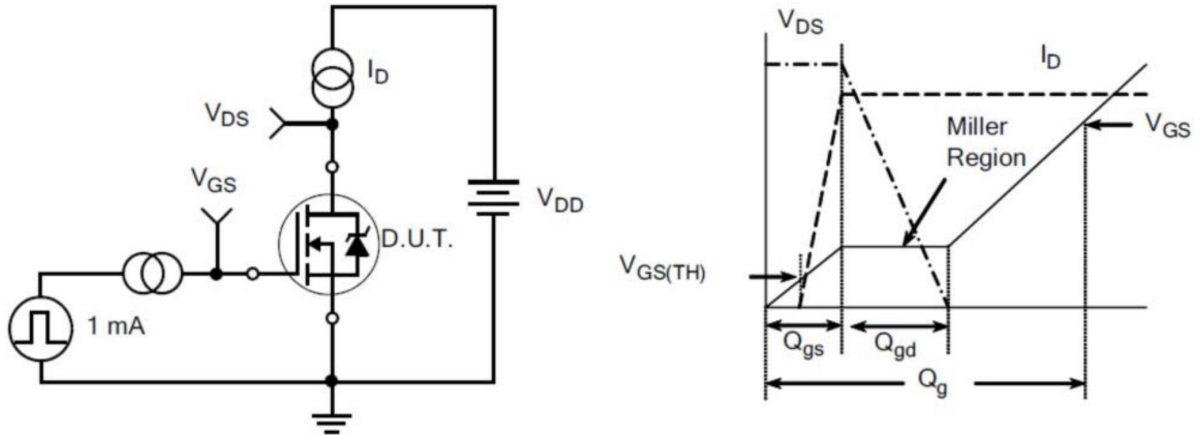


Figure 13: Resistive Switching Test Circuit and Waveform

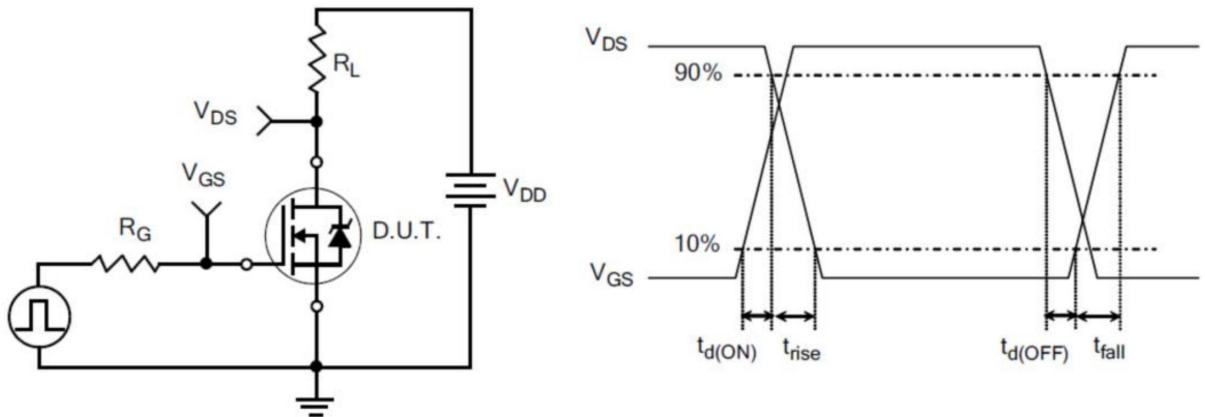
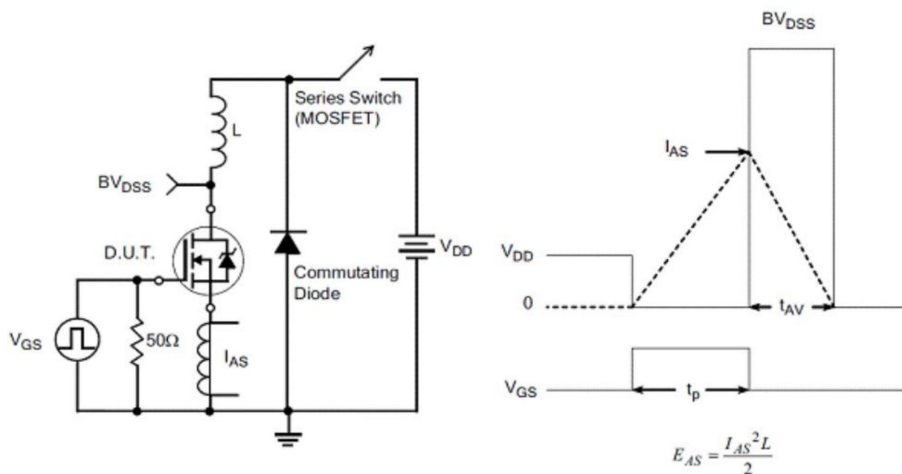
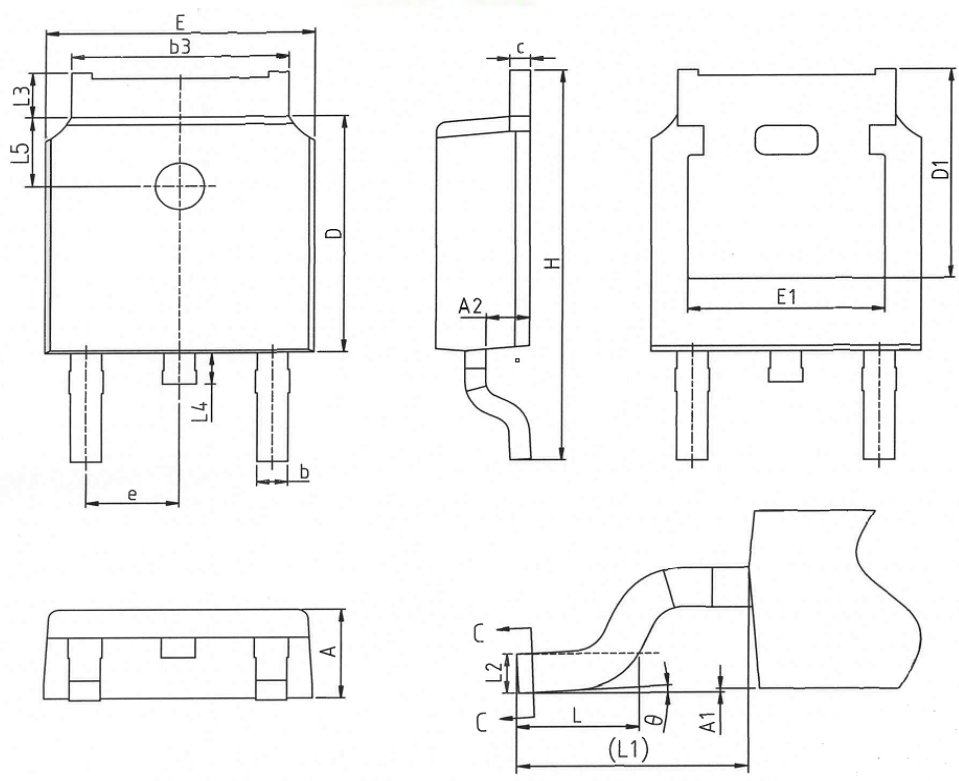


Figure 14: Unclamped Inductive Switching Test Circuit and Waveform



Package Outlines

D-Pak



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	-	0.12
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.73
E1	4.63	-	-
e	2.286BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	-	1.28
L4	0.50	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°

* Dimensions in millimeters

Package Marking and Ordering Information

Part Number	Top Marking	Package	Packing Method	Quantity
BSD10NL200SK1	BSD10NL200SK1	D PAK	Tape & Reel	2500 units

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