BT136

4 A standard and Snubberless™ triacs

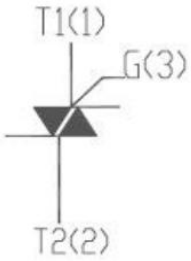
VOLTAGE 600/800 V

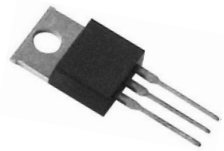
CURRENT 4.0 A

Features

- High current triac
- Low thermal resistance with clip bonding
- High commutation (4 quadrant) or very high commutation (3 quadrant) capability

GK xxx
BT136 600/800x





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LOGO GK xxx CODE

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
VDRM VRRM	Repetitive Peak Off-State Voltage	BT136-600	600	V
		BT136-800	800	
IT(RMS)	R.M.S On-State Current	Tc=110°C	4	A
ITSM	Surge On-State Current	tp=16.7ms/tp=10ms	25/27	A
I²t	I²t for fusing	Tp=10ms	3.1	A²s
PG(AV)	Average Gate Power Dissipation	Tj=125°C	1	W
IGM	Peak Gate Current	tp=20us Tj=125°C	2	A
Tj	Operating Junction Temperature		~40~125	°C
TSTG	Storage Temperature		~40~150	°C

Electrical Characteristics (Tj=25°C unless otherwise specifed)

Symbol	Parameter		Test Conditions	Value				Unit
				D	E	F	G	
IDRM	Repetitive Peak Off-State Current		Tj=25°C	≤10				uA
			Tj=125°C	≤0.5				mA
IRRM	Repetitive Peak Reverse Current		Tj=25°C	≤10				uA
			Tj=125°C	≤0.5				mA
VTM	Forward "on" voltage		IT=5A tp=380us	≤1.7				V
VGD	gate non-trigger voltage		VD=12V, Tj=125°C	≥0.2				V
IH	Holding current		IT=100mA	≤10	≤25	≤30	≤60	mA
VGT	Gate trigger voltage		VD=12V	≤1.5				V
IGT	Gate trigger current	I,II,III	VD=12V,IGT=0.1A	5	10	25	50	mA
		IV		10	25	70	100	mA
di/dt	Critical-rate of rise of commutation current.	I,II,III	IT=6A ,IGT==0.2A, dIg/dt=0.2A/us	≥50				A /us
		IV		≥10				A /us
dv/dt	Critical-rate of rise of commutation voltage		TJ=125°C VD=2/3VDRM Gate	5	10	50	200	V/us

RATING AND CHARACTERISTIC CURVES

FIG.1: Maximum power dissipation versus RMS on-state current

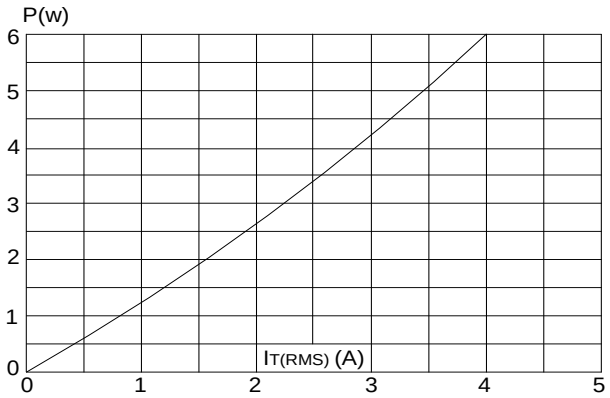


FIG.2: RMS on-state current versus case temperature

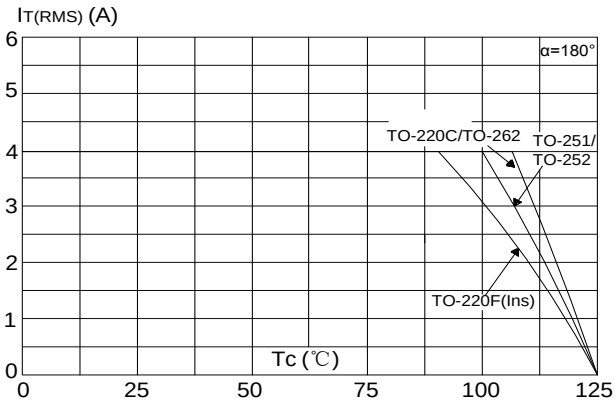


FIG.3: Surge peak on-state current versus number of cycles

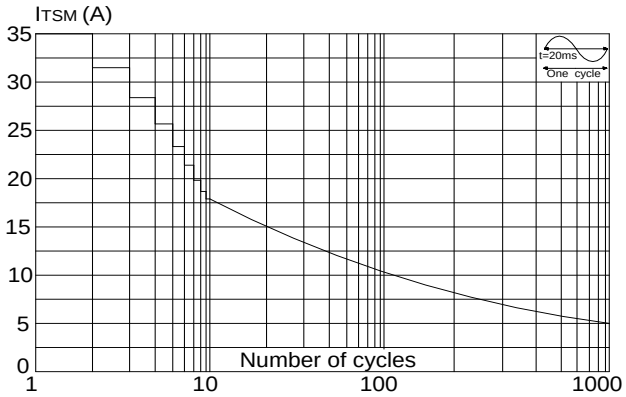


FIG.4: On-state characteristics (maximum values)

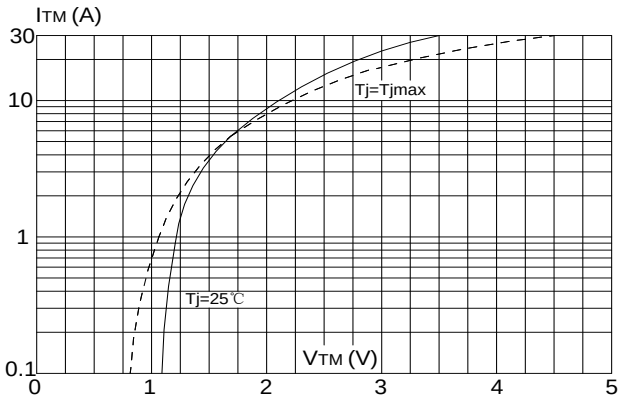


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms, and corresponging value of I t (dI/dt^2< 100A/μs)

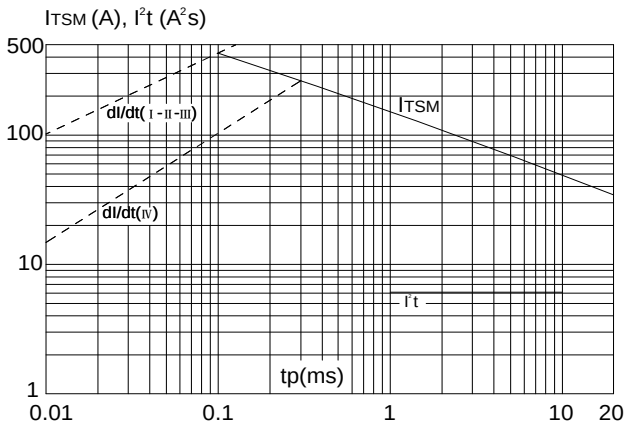
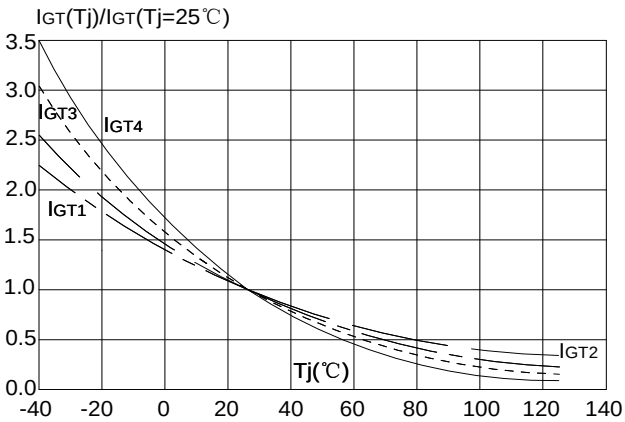


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



RATING AND CHARACTERISTIC CURVES

FIG.7: Relative variations of holding current versus junction temperature

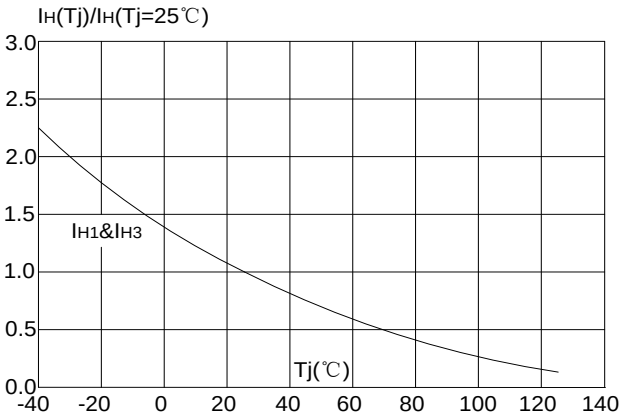
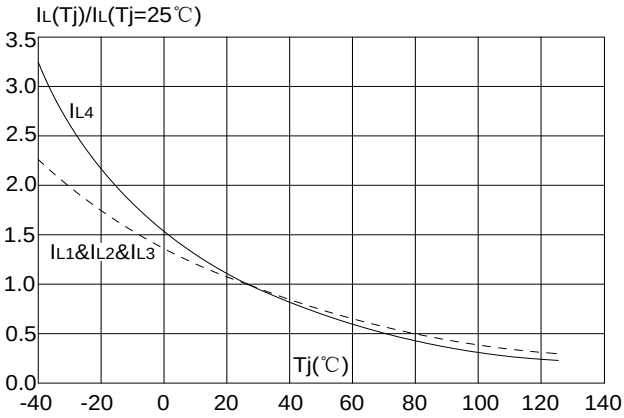


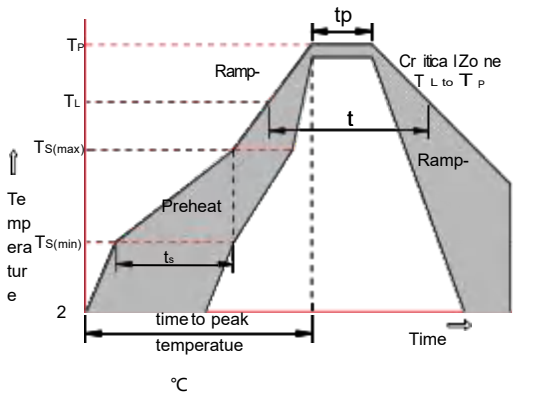
FIG.8: Relative variations of latching current versus junction temperature



Soldering parameters

Reflow Condition		Pb-Free assembly (see as bellow)
Pre Heat	-Temperature Min ($T_{s(min)}$)	+150℃
	-Temperature Max($T_{s(max)}$)	+200℃
	-Time (Min to Max) (ts)	60-180 secs.
Average ramp up rate (Liquid us Temp (T_L) to peak)		3℃/sec. Max
$T_{s(max)}$ to T_L - Ramp-up Rate		3℃/sec. Max
Reflow	-Temperature(T_L)(Liquid us)	+217℃
	-Temperature(t_L)	60-150 secs.
Peak Temp (T_p)		+260(+0/-5)℃
Time within 5℃ of actual Peak Temp (t_p)		30 secs. Max
Ramp-down Rate		6℃/sec. Max
Time 25℃ to Peak Temp (T_P)		8 min. Max
Do not exceed		+260℃

FIG5: Reflow condition



Package Dimensions & Suggested Pad Layout

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