

P-Channel Enhancement Mode MOSFET

1 Features

- $V_{DS} = -20V$, $I_D = -3.0A$
- $R_{DS(ON)} < 115m\Omega$ @ $V_{GS} = -4.5V$
- $R_{DS(ON)} < 160m\Omega$ @ $V_{GS} = -2.5V$
- High density cell design for extremely low $R_{DS(ON)}$
- Lead free product is acquired
- Conventional SOT-23 Package

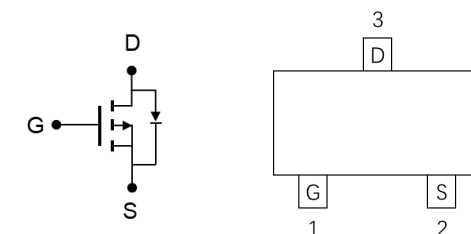
2 Applications

- Cellular Handsets and Accessories
- Personal Digital Assistants
- Portable Instrumentation
- Load switch

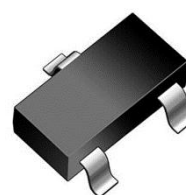
3 Description

The SLMT320V3CP provide the best combination of fast switching, low on-resistance and cost-effectiveness. The MOSFET is the P-Channel enhancement mode power field effect transistors with high cell density, trench technology. This high density process and design have been optimized switching performance and especially tailored to minimize on-state resistance.

4 Pin Configuration and Top View



Schematic Diagram



SOT-23

5 Device Information

PART NUMBER	PACKAGE	BODY SIZE (NOM)
SLMT320V3CP	SOT-23	2.90 mm x 2.40mm

6 Absolute maximum Ratings @25°C

RATING	SYMBOL	VALUE	UNITS
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 8	V
Continuous Drain Current	I_D	-3.0	A
Pulsed Drain Current	I_{DP}	-10	A
Total power dissipation	P_D	1	W
Channel temperature	T_J	-55 to +150	°C
Range of storage temperature	T_{STG}	-55 to +150	°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	125	°C/W

7 Electrical characteristics (@25°C unless otherwise specified)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = -250μA, V _{GS} = 0V	-20			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	-0.4		-1.0	V
Drain Cut Off Current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0V			-1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±8V, V _{DS} = 0V			±0.1	μA
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} = -4.5V, I _D = -1.7A		88	110	mΩ
		V _{GS} = -2.5V, I _D = -1.2A		125	160	mΩ
Forward Transconductance	g _{FS}	V _{DS} = -5V, I _D = -2.8A		7		S
Dynamic Characteristics						
Total Gate Charge	Q _G	V _{DS} =-10V, I _D =-3A, V _{GS} = -4.5V		6.6		nC
Gate-Source Charge	Q _{GS}			0.3		nC
Gate-Drain Charge	Q _{GD}			1.3		nC
Input Capacitance	C _{ISS}	V _{DS} = -10V, V _{GS} = 0V, f = 1MHz		589		pF
Reverse Transfer Capacitance	C _{RSS}			67.2		pF
Output Capacitance	C _{OSS}			91.2		pF
Turn-On Delay Time	t _{d(on)}	V _{GS} =-4.5V, V _{DS} =-10V, I _D =-1.0A, R _G =6Ω		9.7		ns
Turn-Off Delay Time	t _{d(off)}			33.3		ns
Rise Time	t _r			3.6		ns
Fall Time	t _f			4.5		ns
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V _{SD}	I _S = -2A, V _{GS} = 0V		-0.75	-1.0	V

8 Typical Characteristics

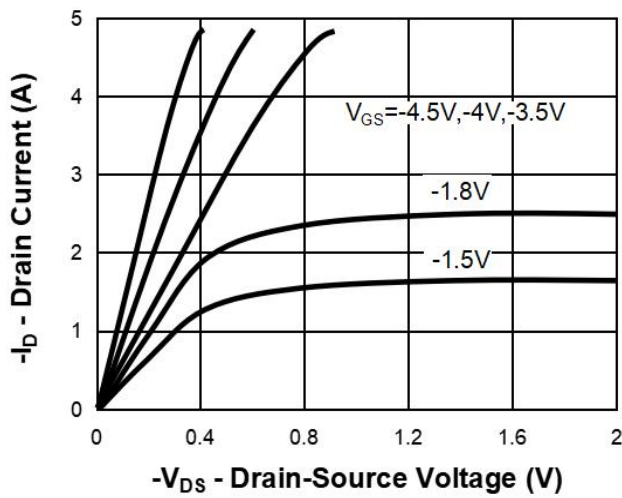


Fig 1. Output Characteristics

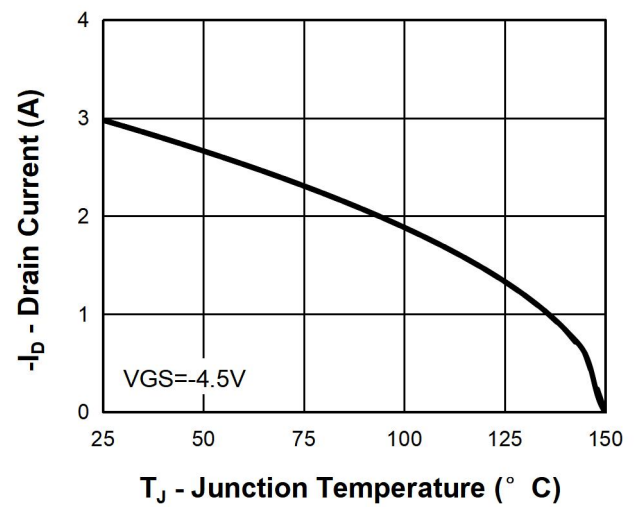


Fig 2. Drain Current

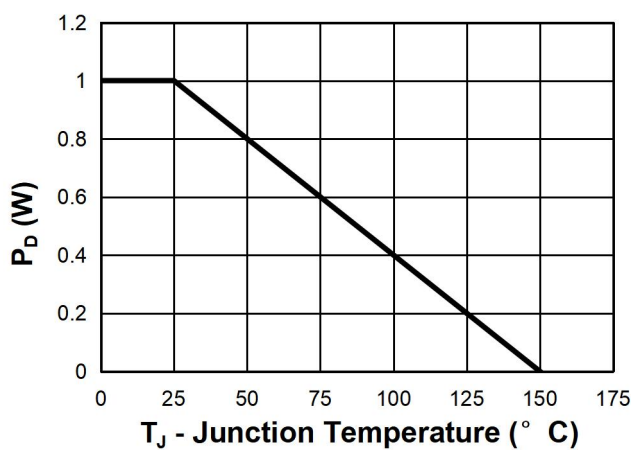


Fig 3. Power Dissipation

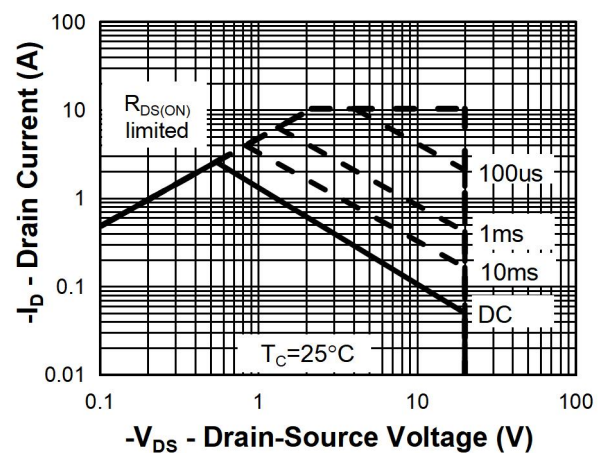


Fig 4. Safe Operation Area(SOA)

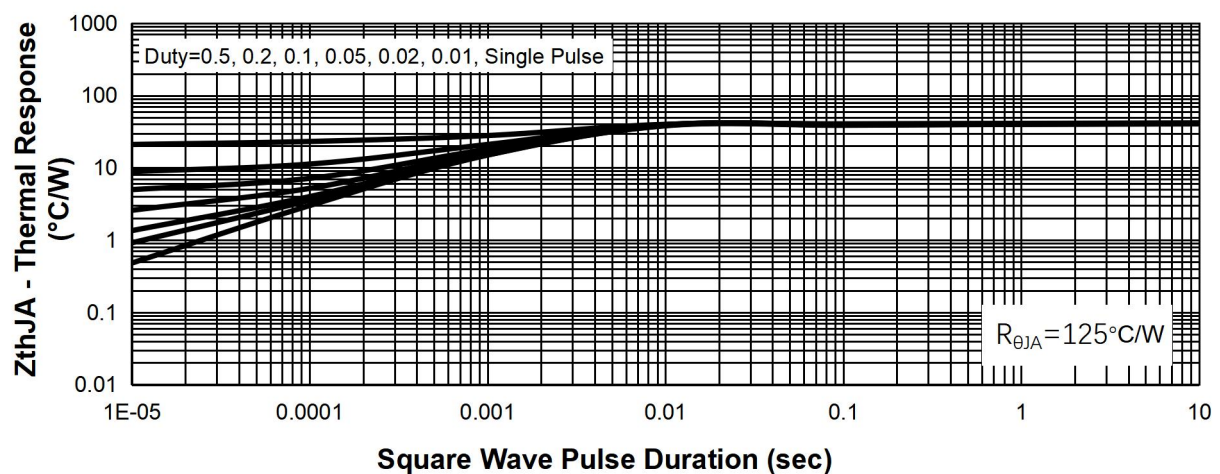


Fig 5. Safe Operation Area(SOA)

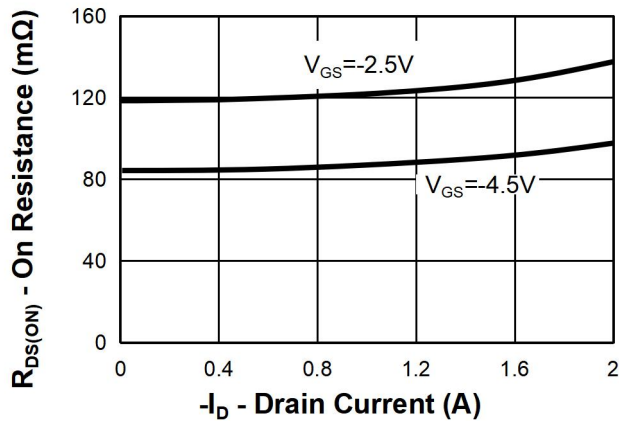


Fig 6. Rdson vs. Drain Current Characteristics

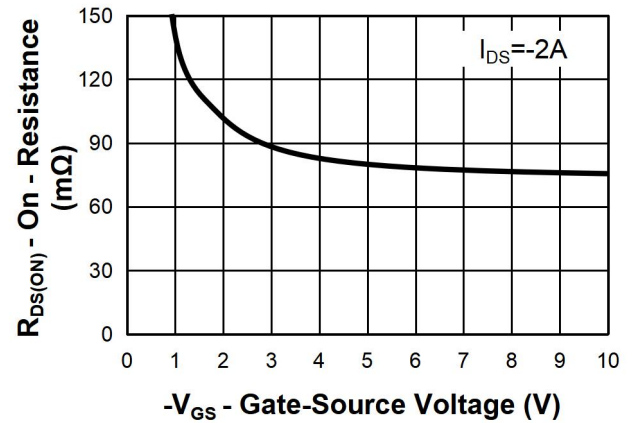


Fig 7. Gate-Source on Resistance

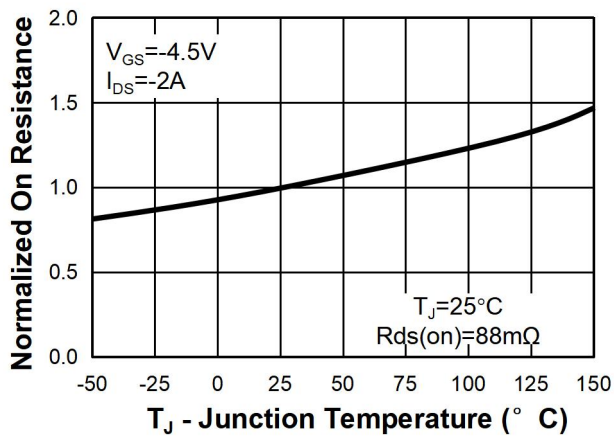


Fig 8. Rdson vs. Junction Tem Characteristics

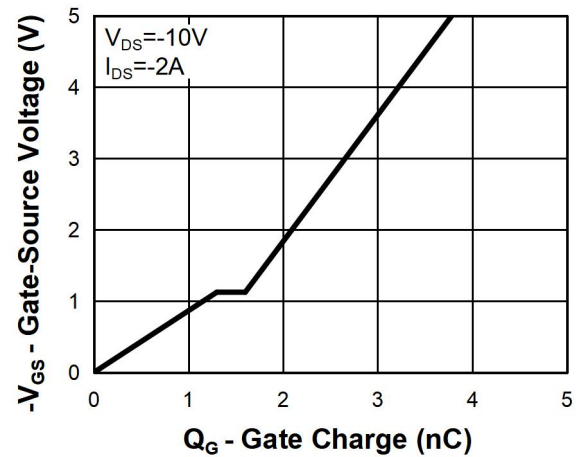


Fig 9. Gate Charge Characteristics

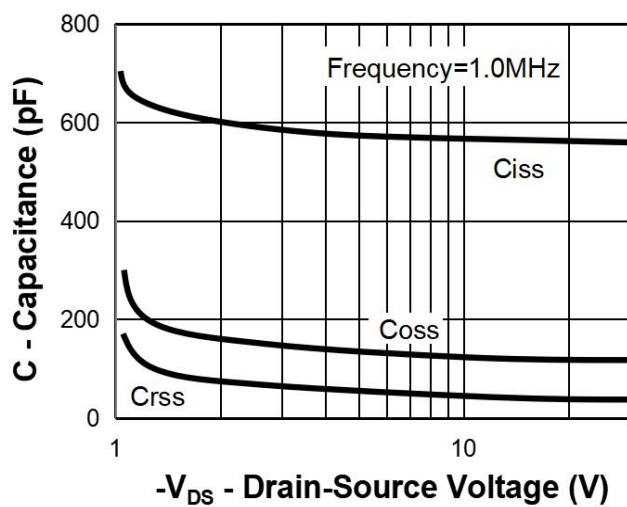


Fig 10. Capacitance Characteristics

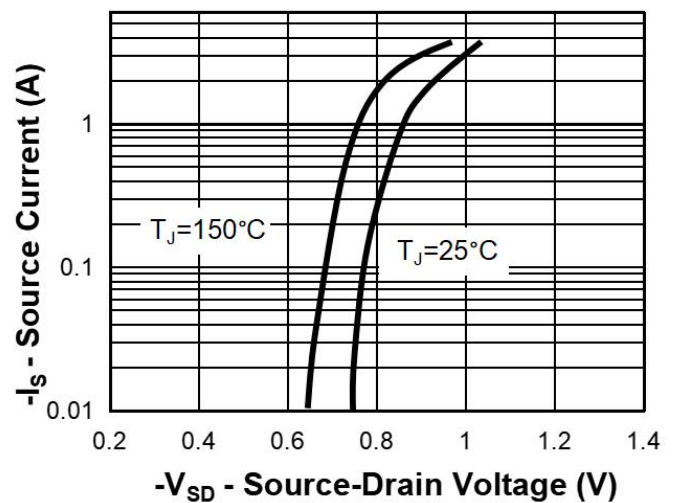
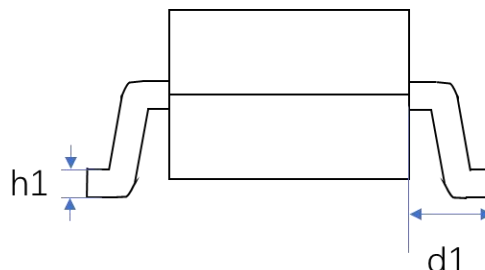
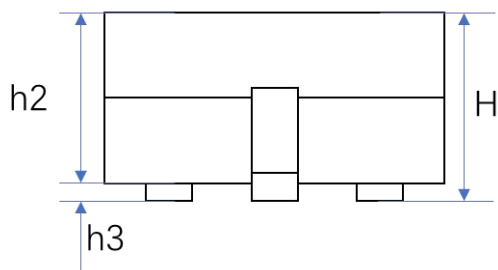


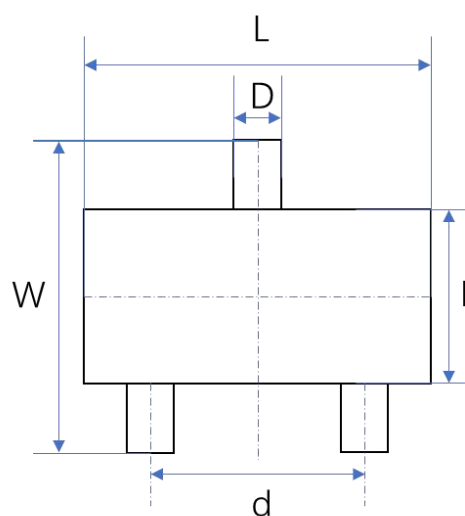
Fig 8. Source-Drain Diode Forward

9 Product dimension

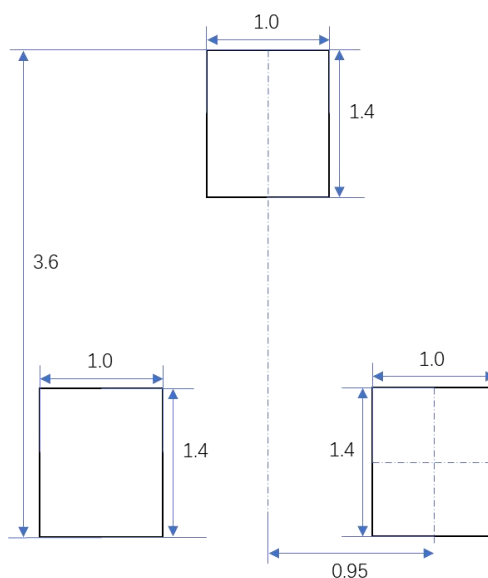
SOT-23



DIM	UNITS (mm)		
	MIN.	TYP.	MAX.
L	2.80	-	3.00
W	2.25	-	2.55
D	0.30	-	0.50
E	1.20	-	1.40
d	1.80	-	2.00
d1	-	0.55	-
H	0.90	-	1.15
h1	0.08	-	0.15
h2	0.9	-	1.05
h3	0.00	-	0.10



10 PCB Layout Footprints (Units: mm)



11 Ordering Information

Part Number	Packaging	Reel Size
SLMT320V3CP	3000/Tape & Reel	7 inch