

General Description

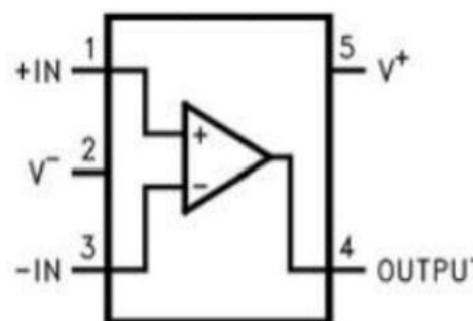
The LM321 brings performance and economy to low power systems. With a high unity gain frequency and a guaranteed $0.4\text{V}/\mu\text{s}$ slew rate, the quiescent current is only $100\mu\text{A}$ (5V). The input common mode range includes ground and therefore the device is able to operate in single supply applications as well as in dual supply applications. It is also capable of comfortably driving large capacitive loads. Overall the LM321 is a low power, wide supply range performance op amp that can be designed into a wide range of applications at an economical price without sacrificing valuable board space.

The LM321 is available in the SOT23-5L package.

Features

- Gain-Bandwidth product 1.0MHz
- Low supply current $100\mu\text{A}$
- Low input bias current 45nA
- Wide supply voltage range +3V to +36V

Pin Configurations



Ordering Information

Function Block

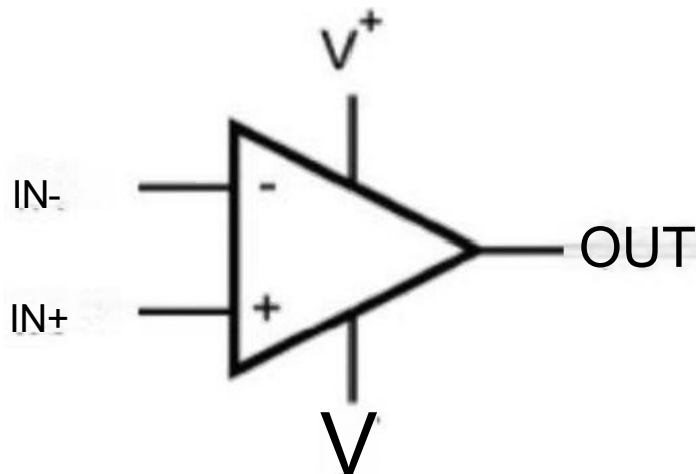


Figure1 Function Block Diagram of LM321

Absolute Maximum Ratings*1

Parameter Name	Value	Unit
Supply Voltage	36	V
Input Voltage	-0.3~+36	V
Output Short Circuit to GND $V_+ \leq 15V$ and $T_a = 25^\circ C$ *2	Continuous	
Junction Temperature *3	150	$^\circ C$
Thermal Resistance to Ambient (θ_{JA})	265	$^\circ C/W$
Operating Temperature Range	-40~+85	$^\circ C$
Storage Temperature Range	-65~+150	$^\circ C$

Electrical Characteristics

(TA=25°C, unless otherwise noted, V+=5V)

Parameter Name		Test Conditions		Min	Typ	Max	Unit
Input Offset Voltage		Ta=25. C*4			0.26	2.5	mV
Input Bias Current		Ta=25. C, IN(+) or IN(-), VCM=0V*5			45	150	nA
Input Offset Current		Ta=25. C, IIN(+)-IN(-), VCM=0V			3	30	nA
Input Common-Mode Voltage Range		Ta=25. C, V+=30V*6		0		V+-1.5	V
Supply Current		Over Full Temperature Range, RL=0 on all Op Amps	V+=30V		160	500	μA
			V+=5V		100	300	
Large Signal Voltage Gain		Ta=25. C, V+=15V, RL≥2k Ω (for Vo=1~11V)		50	100		V/mV
Common-Mode Rejection Ratio		DC, Ta=25. C, VCM=0~V+-1.5V		70	90		dB
Power Supply Rejection Ratio		DC, Ta=25. C, V+=5~30V		75	100		dB
Amplifier-to-Amplifier Coupling		0-25-. -v=-			120		dB
Output Current	Source	Vo=2V, Ta=25. C		20	40		mA
	Sink	VIN ()=1V, VIN(+)=0V, v+=15V, Vo=2V, Ta=25. C		10	15		mA
		VIN (-)=1V, VIN(+)=0V, v+=15V, Vo=200mV, Ta=25. C		12	50		μA
Short Circuit to Ground		Ta=25. C, V+=15V*2			40	85	mA
Output Voltage Swing	VOH	V+=27V	RL=2k Ω	21	25		V
			RL=10k Ω	22	24		V
	VoL	V+=5V, RL=10k Ω			5	20	mV

*1 Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics.

*2: Short circuits from the output to v can cause excessive heating and eventual destruction. When considering short circuits to ground, the maximum output current is approximately 40mA independent of the magnitude of V. At values of supply voltage in excess of +15V, continuous short-circuits can exceed the power dissipation ratings and cause eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifiers.

*3: The maximum power dissipation is a function of TMAx, θA, and Ta. The maximum allowable power dissipation at any ambient temperature is $P = (TMAx - Ta) / \theta A$. All numbers apply for packages soldered directly onto a PC board.

*4: $V_o=1.4V$, $R_s=0\Omega$ with v from 5V to 30V; and over the full input common-mode range (0V to $v^+ - 1.5V$) at 25°C

*5: The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the input lines.

*6: The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3V (at 25°C). The upper end of the common-mode voltage range is $V^+ - 1.5V$ (at 25°C), but either or both inputs can go to +32V without damage, independent of the magnitude of V

*7: Due to proximity of external components, insure that coupling is not originating via stray capacitance between these external parts. This typically can be detected as this type of capacitance increases at higher frequencies.

Simplified Schematic

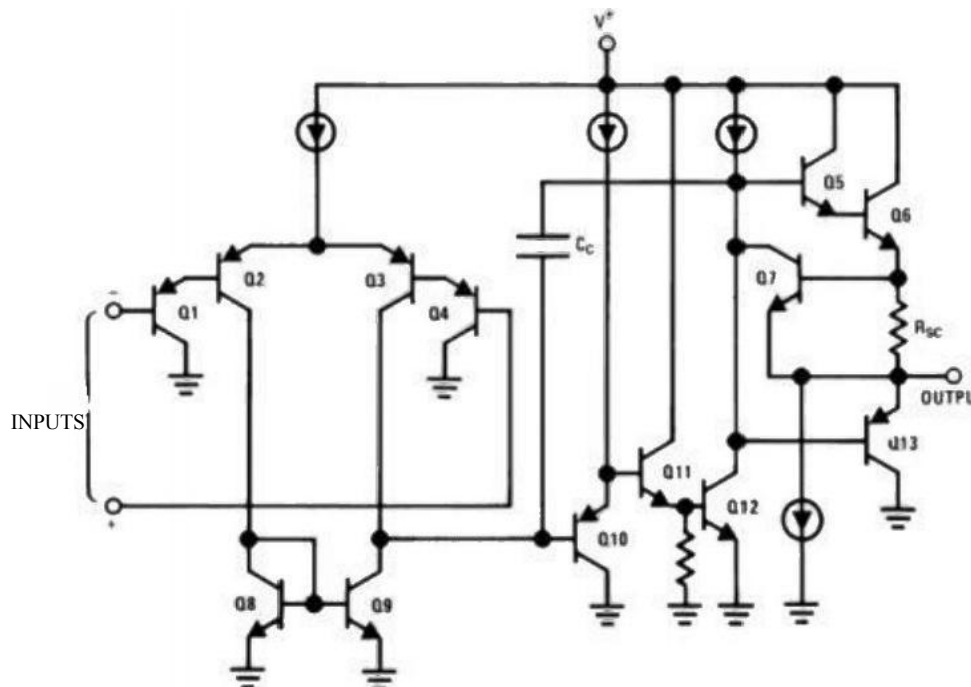
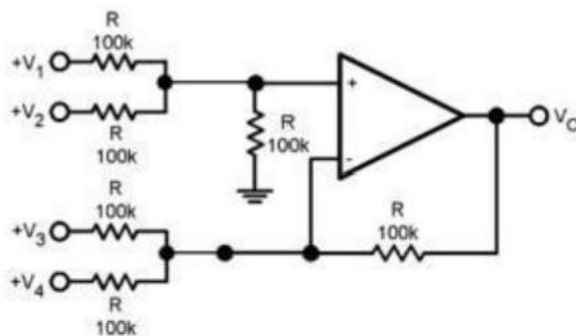


Figure2 Simplified Schematic of LM321

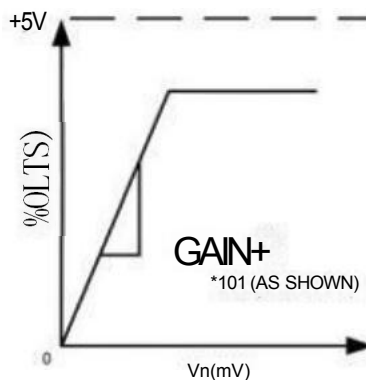
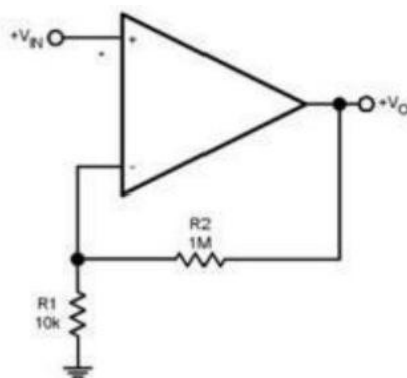
Typical Application Circuit



DC Summing Amplifier
($V_i's \geq 0V_{oc}$ and $V_o \geq V_{oc}$)

Where: $V_o = V_1 + V_2 - V_3 - V_4$. ($V_1 + V_2 \geq (V_3 + V_4)$) to keep $V_o > 0V_{Dc}$

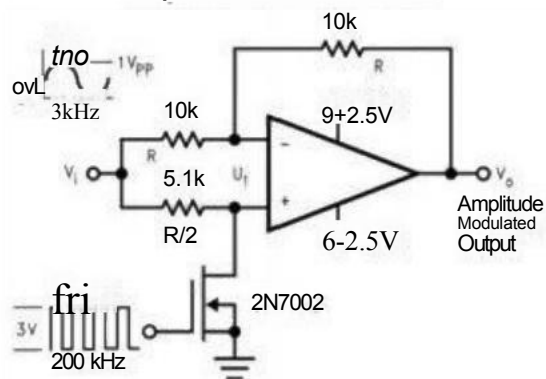
Non-Inverting DC Gain (0V input = 0V Output)



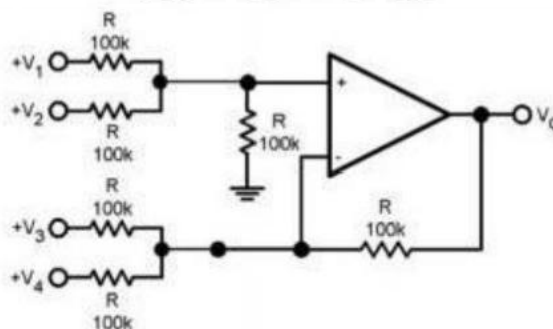
R NOT NEEDED DUE TO TEMPERATURE INDEPENDENT



Amplitude Modulator Circuit

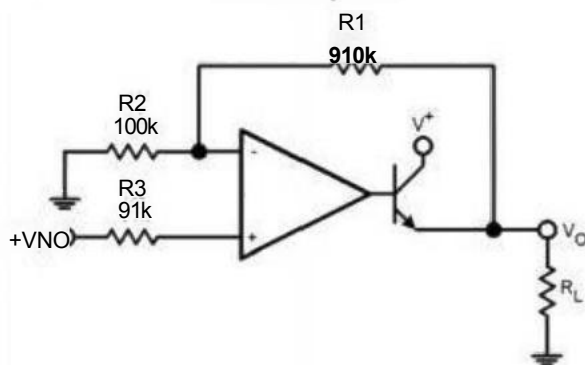


DC Summing Amplifier
($V_{IN's} \geq 0V_{oc}$ and $V_o \geq V_{pc}$)



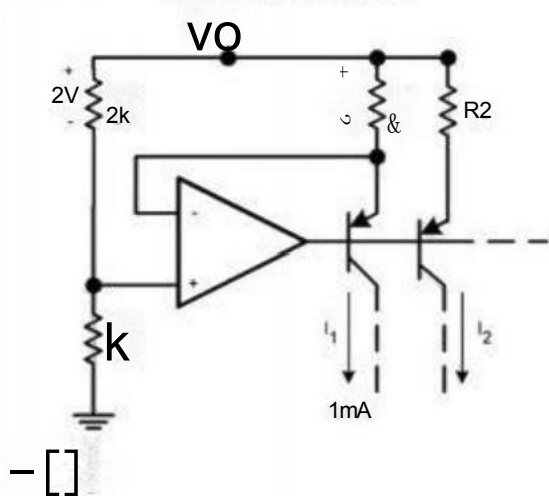
Where: $V_o = V_1 + V_2 - V_3 - V_4$. ($V_i + V_2 \geq V_3 + V_4$) to keep $V_o > 0V_{pe}$

Power Amplifier

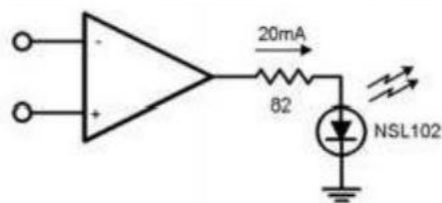


$V_o = 0V_{oc}$ for $V_n = 0V_{pc}$. $A_y = 10$

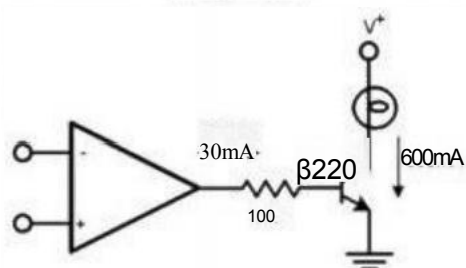
Fixed Current Sources



LED Driver



Lamp Driver



Application Summary

The LM321 op amp can operate with a single or dual power supply voltage, has true-differential inputs, and remain in the linear mode with an input common-mode voltage of 0 V_{DC} . This amplifier operates over a wide range of power supply voltages, with little change in performance characteristics. At 25°C amplifier operation is possible down to a minimum supply voltage of 3V .

Large differential input voltages can be easily accommodated and, as input differential voltage protection diodes are not needed, no large input currents result from large differential input voltages. The differential input voltage may be larger than V_{DC} without damaging the device. Protection should be provided to prevent the input voltages from going negative more than $-0.3\text{ V}_{\text{DC}}$ (at 25°C). An input clamp diode with a resistor to the IC input terminal can be used.

To reduce the power supply drain, the amplifier has a class A output stage for small signal levels which converts to class B in a large signal mode. This allows the amplifiers to both source and sink large output currents.

Therefore both NPN and PNP external current boost transistors can be used to extend the power capability of the basic amplifiers. The output voltage needs to raise approximately 1 diode drop above ground to bias the on-chip vertical PNP transistor for output current sinking applications.

For AC applications, where the load is capacitively coupled to the output of the amplifier, a resistor should be used, from the output of the amplifier to ground to increase the class A bias current and to reduce distortion.

Capacitive loads which are applied directly to the output of the amplifier reduce the loop stability margin. Values of 50pF can be accommodated using the worst-case non-inverting unity gain connection. Large closed loop gains or resistive isolation should be used if large load capacitance must be driven by the amplifier.

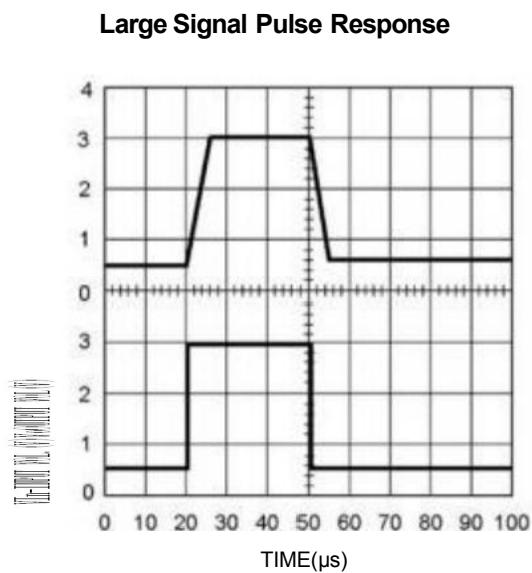
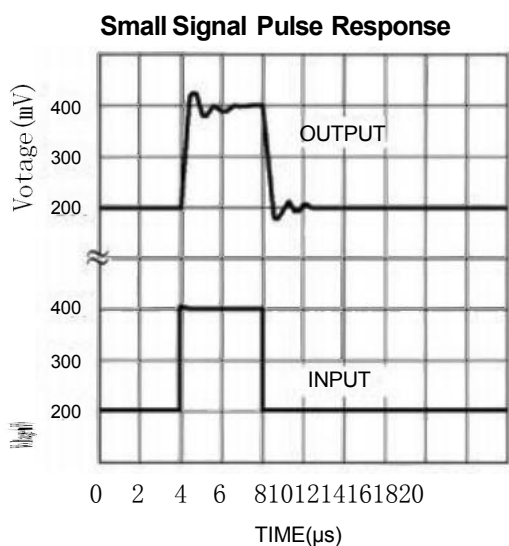
The bias network of the LM321 establishes a supply current which is independent of the magnitude of the power supply voltage over the range of from 3 V_{DC} to 30 V_{DC} .

Output short circuits either to ground or to the positive power supply should be of short time duration. Units can be destroyed, not as a result of the short circuit current causing metal fusing, but rather due to the large increase in IC chip dissipation which will cause eventual failure due to excessive junction temperatures. The larger value of output source current which is available at 25°C provides a larger output current capability at elevated temperatures than a standard IC op amp.

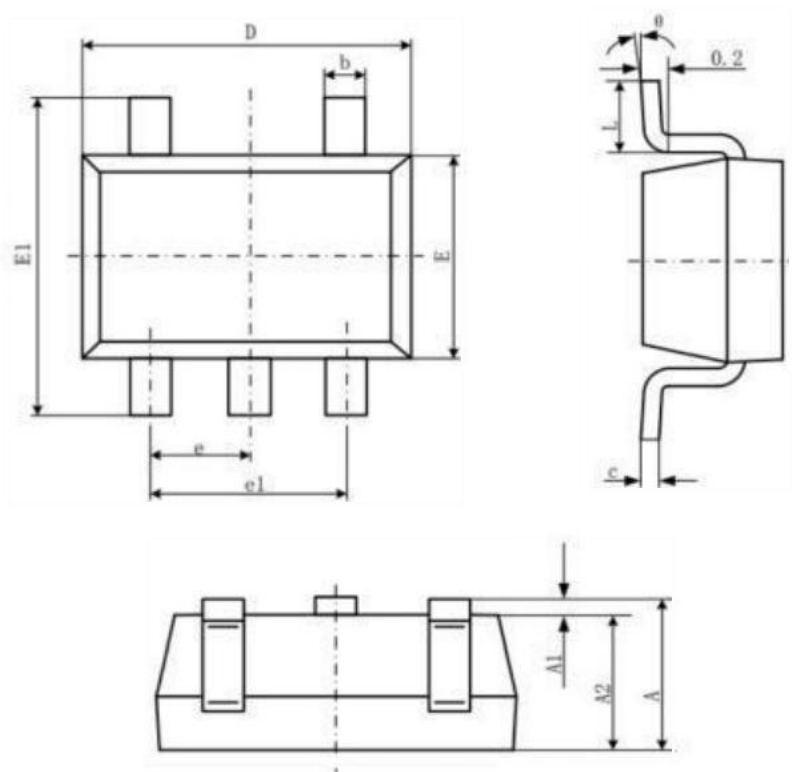
The circuits presented in the section on typical applications emphasize operation on only a single power supply voltage. If complementary power supplies are available, all of the standard op amp circuits can be used.

In general, introducing a pseudo-ground (a bias voltage reference of $V_{CC}/2$) will allow operation above and below this value in single power supply systems. Many application circuits are shown which take advantage of the wide input common-mode voltage range which includes ground. In most cases, input biasing is not required and input voltages which range to ground can easily be accommodated.

Characteristics Curves



SOT23-5 Package Outline Dimensions



编号	尺寸 (mm)	
	Min	Max
A	1.050	1.250
A1	0.000	0.100
A2	1.050	1.150
b	0.300	0.500
C	0.100	0.200
D	2.820	3.020
E	1.500	1.700
E1	2.650	2.950
e	0.950 (BSC)	
e1	1.800	2.000
L	0.300	0.600
θ	0°	8°