Dersonic®
德信立業 創新開拓

DOC NO.: DEC-SA-WI006
REV.: B/3
DATE: 2023/03/31

# 獨石電容器規格承認書
# APPROVAL SPECIFICATION FOR MONOLITHIC CAPACITORS

| | | | |
|---|---|---|---|
| 客戶 CUSTOMER | 立創商城 | | |
| 客戶料號 CUSTOMER P/N | | | |
| 客戶規格描述 CUST. SPEC | | | |
| 規格描述 DESCRIPTION | 50V/151/J/F5.08/斜尖/L24/环氧(蓝)/NP0/0805 | | |
| 產品編碼 PART NUMBER | CD1H151JC0IECHE000 | | |
| 日期 DATE | 2026年6月15日 | 文件編號 DOC. NO. | DEC-SA-WI006 |

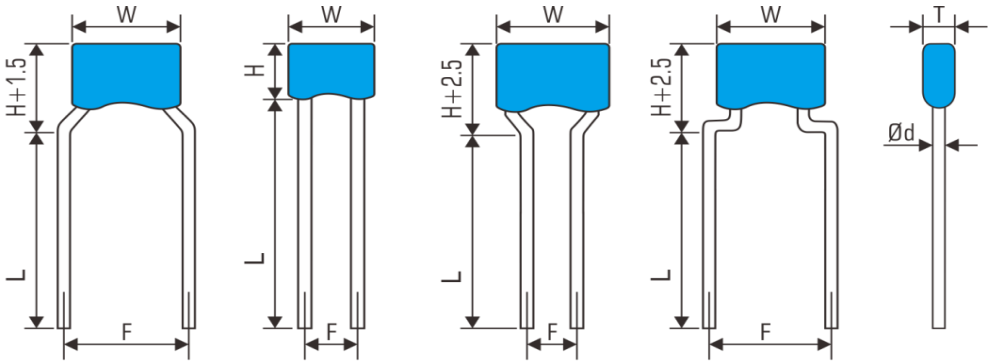
| 德爾創承認欄 APPROVED BY DERSONIC | | | 客戶承認欄 APPROVED BY CUSTOMER | |
|---|---|---|---|---|
| 批 準 APPROVED BY | 審核 CHECK BY | 制訂 FORMULATE BY | 批 準 APPROVED BY | 審核 CHECK BY |
| 彭少雄 | 吳成愛 | 胡明康 | | |

東莞市德爾創電子有限公司 承认专用章

# 東莞市德爾創電子有限公司
# DONGGUAN CITY DERSONIC ELECTRONIC CO., LTD.


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1. 規格表
DATA SHEET




脚型代码
Lead style code: 0 1 A 9

| | | |
|---|---|---|
| 產品編碼<br>Part number | | CD1H151JC0IECHE000 |
| 規格描述<br>Description | | 50V/151/J/F5.08/斜尖/L24/环氧(蓝)/NP0/0805 |
| 客戶料號<br>Customer P/N | | |
| 介質類別<br>Dielectric class | | Class 1 |
| 額定電壓<br>Rated voltage | | 50VDC |
| 電容量<br>Capacitance | | 150pF ±5% @ 1MHz 1V, 25ºC |
| 損耗角正切<br>Tangent of loss angle | | 0.001 max @ 1MHz 1V 25ºC |
| 耐電壓<br>Testing voltage | | 125V (Charge/discharge 50mA max), 3s, PASS |
| 絕緣電阻<br>Insulation resistance | | 10GΩ min @ 50V 60s, ≤RH70% |
| 温度特性<br>Temperature characteristics | | NP0<br>ΔC/C: 0±60PPM/ºC @ -55ºC～125ºC |
| 尺寸<br>DIMENSIONS | W (Weight) | 4.0 mm±1.0mm |
| | H (Height) | 5.00 mm max |
| | T (Thickness) | 2.6 mm±1.0mm |
| | F (Lead spacing) | 5.08 mm±0.8mm |
| | L (Lead length) | 24.0 mm±5.0mm |
| | ød (Lead diameter) | 0.45 mm±0.10mm |
| 標誌<br>Marking | | 151 |

## 2. 總則
GENERAL

1) 本規格書適用於電子設備用獨石電容器。
This specification applies to the leaded type MLCC for the electronic equipment.

2) 獨石電容器
Monolithic capacitor
獨石電容器是一種用積層陶瓷電容器焊接導線、並用環氧樹脂包封後的電容器。
Monolithic capacitor is a capacitor with a multilayer ceramic capacitor soldering lead wire and coating epoxy resin.

其具有以下特點：
It has the following characteristics：

■ 體積小，容量大，適合自動安裝的編帶包裝。
Miniature size, large capacitance, tape and reel packaging suitable for auto-placement.

■ 環氧樹脂封裝，從而具有優良的防潮性能、機械強度及耐熱性。
Epoxy resin coating creates excellent performance in humidity resistance, mechanical strength and heat resistance.

■ 工業生產標準尺寸及多種腳型產品。
Standard size, various lead configurations.

3) 符合RoHS 2.0
Complies with RoHS 2.0

## 3. 適用標準
APPLICABLE STANDARD

本產品符合下列標準，且本規格書的相關內容引用以下標準，當雙方對此存在爭議時，可依以下標準進行仲裁。
This product complies with the following standards, and the relevant content of this specification refers to the following standards, when the two sides in dispute, the following criteria for arbitration.

GB/T 2693-2001 電子設備用固定電容器 第1部分 總規範(IDT IEC 60384-1)
Fixed capacitors for use in electronic equipmen — Part 1: Generic specification (IDT IEC 60384-1)

GB/T 5966-2011 電子設備用固定電容器 第8部分 分規範 1類瓷介固定電容器(IDT IEC 60384-8)
Fixed capacitors for use in electronic equipmen — Part 8: Sectional specification: Fixed capacitors of ceramic dielectric, Class 1 (IDT IEC 60384-8)

GB/T 5968-2011 電子設備用固定電容器 第9部分 分規範 2類瓷介固定電容器(IDT IEC 60384-9)
Fixed capacitors for use in electronic equipmen — Part 9: Sectional specification: Fixed capacitors of ceramic dielectric, Class 2 (IDT IEC 60384-9)

## 4. 產品編碼
PART NUMBER

本公司產品編碼方式，舉例說明如下：
The product part number representation of the company, examples are as follows：

| CD | 1H | 151 | J | C0 | I | E | CH | E | 000 |
|---|---|---|---|---|---|---|---|---|---|
| 類別 Type | 額定電壓 Rated voltage | 標稱容量 Nominal capacitance | 容量偏差 Capacitance tolerance | 引線成型方式 Lead format | 編帶包裝或散裝腳長 Taping packing or Lead length (bulk) | 包封 Coating | 温度特性 Temperature characteristics | 芯片 Chip spec | 生產識別碼 Production identification code |

■ 類別 Type
CD: 獨石電容器 Leads type MLCC

■ 額定電壓 Rated voltage

| | | | |
|---|---|---|---|
| 1H: 50V | 3A: 1kV | 1E: 25V | 1J: 63V |
| 2A: 100V | 3D: 2kV | 2E: 250V | 2J: 630V |
| 2H: 500V | 3F: 3kV | | |

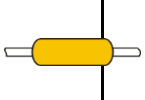
■ 標稱容量 Nominal capacitance
用3位數表示，單位pF。如下所示：
In 3 digits, unit is pF, as shown in below:
151: 150pF (1uF ＝ 1,000nF ＝ 1,000,000pF)

■ 容量偏差 Capacitance tolerance

| | | |
|---|---|---|
| C: ±0.25pF | J: ±5% | S: +50%/-20% |
| D: ±0.50pF | K: ±10% | Z: +80%/-20% |
| | M: ±20% | P: +100%/-0% |

■ 引線成型方式 Lead format

| 代碼 Code | 腳距 Lead spacing |
|---|---|
| A | 2.54mm |
| B | 3.5mm |
| C | 5.08mm |
| D | 7.50mm |
| E | 10.0mm |

| 代碼 Code | 1 | 0 | 9 | A |
|---|---|---|---|---|
| 腳型樣式 Lead style drawing |  |  |  |  |

■ 編帶包裝或散裝腳長 Taping packing or Lead length (bulk)

● Taping
T: Reel packing
P: Ammo packing

● Bulk (Leads length)

| | | |
|---|---|---|
| 4: 3.5mm | A: 8.0mm | V: 7.0mm |
| 6: 4.0mm | B: 10.0mm | Q: 9.0mm |
| 8: 5.0mm | I: 24.0mm | C: 12.0mm |
| 9: 6.0mm | | 2: 3.0mm |

■ 包封 Coating
E: 環氧（藍） Epoxy (Blue)
H: 環氧（黃） Epoxy (Yellow)

■ 温度特性 Temperature characteristics

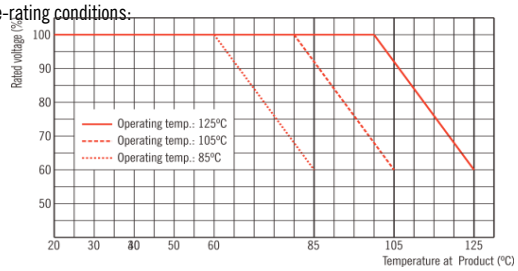
| | |
|---|---|
| CH: NP0 | F4: Y5V |
| R1: X7R | R2: X5R |

■ 芯片規格 Chip spec

| | | | |
|---|---|---|---|
| D: 0603 | G: 1210 | J: 1825 | N: 2220*2 |
| E: 0805 | H: 1808 | K: 2220 | |
| F: 1206 | I: 1812 | L: 2225 | |

■ 生產識別碼 Production identification code
內部控制碼，本規格書不作說明。
Inteer control code will not be described in this an approval specifications.

## 5. 測量和試驗
## MEASUREMENT AND TEST

| 序<br>No. | 項目<br>Item | 標準<br>Specifications | 試驗方法<br>Testing Method |
|---|---|---|---|
| 1 | 工作溫度範圍<br>Operating temp. range | -55ºC～125ºC | |
| 2 | 額定電壓<br>Rated voltage<br>( $U_R$ ) | 50V | 額定電壓是指在工作溫度範圍內，可連續施加在電容器上的最大直流電壓或最大交流電壓有效值或脈沖電壓的峰值。<br>當交流電壓附加於直流電壓時，Vp-p或Vo-p（以較大者爲準）應維持在額定電壓範圍內。<br>The rated voltage is defined as the maximum voltage which may be applied continuously to the capacitor within the operating temperature range.<br>When AC voltage is superimposed on DC voltage, Vp-p or Vo-p, whichever is larger, should be maintained within the rated voltage range.<br>※De-rating conditions:<br> |
| 3 | 外觀與尺寸<br>Appearance (APP) and Dimension | 外觀形狀沒有明顯的缺點，尺寸在標準範圍內。<br>No marked defect on appearance form and dimensions are within specified range. | 電容必須用目視檢查其明顯的缺點。<br>The capacitor should be visually inspected for evidence of defect.<br>尺寸用遊標卡尺測量。<br>Dimensions should be measured with slide calipers. |
| 4 | 標誌<br>Marking | 清晰易於識別。<br>To be easily legible. | 目視檢查。<br>The capacitor should be visually inspected. |
| 5 | 容量 ($C_R$)<br>Capacitance ($C_R$) | 142.5PF-157.5PF | 容量與tan δ在25±1℃下，使用下列條件進行測量。<br>The capacitance, tan δ should be measured at 25ºC±1ºC with the following conditions.<br>1MHz 1V, 25ºC |
| 6 | 損耗角正切(tan δ)<br>Tangent of loss angle (tan δ) | 0.001 max | |
| 7 | 絕緣電阻<br>Insulation Resistance (IR) | 10GΩ min | 在兩導線間施加下列電壓進行測量，時間不超過1分鐘。<br>The insulation resistance should be measured with a DC voltage not exceeding the following voltage at normal temperature and humidity and less than 1 minute of charging.<br>50V |
| 8 | 耐電壓<br>Testing Voltage (TV) | 沒有不合格<br>No failure. | 在電容器兩導線間施加下列測試電壓1到5s後不被破壞（充/放電流不大於2mA）。<br>The capacitor should not be damaged when test voltages of below are applied between the lead wires for 1 to 5 sec.(Charge/Discharge current ≤ 2mA)<br>125 |
| 9 | 導線抗張強度<br>Terminal Tensile Strength | 引線不應斷開，電容器不應破裂。<br>Lead wire should not be cut off capacitor should not be broken. | 固定住電容器，在引線上逐步施加徑嚮拉力直至10N，並保持10±1秒鐘。<br>Fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of the capacitor up to 10N and keep it for 10±1 sec. |
| 10 | 導線抗折強度<br>Terminal Bending Strengt | 引線不應斷開，電容器不應破裂。<br>Lead wire should not be cut off capacitor should not be broken. | 在引線出口處沿一個方嚮施加5N、90º的彎曲壓力，再恢復至初始狀態。之後，在2至3秒內再以相反方嚮施加一次90º的彎曲壓力。<br>Each lead wire should be subjected to 5N of weight and bent 90º at the point of egress, in one direction, then returned to its original position and bent 90º in the opposite direction at the rate of one bent in 2 to 3 sec. |

續上表
Continued on the table

| 序 No. | 項目 Item | | 標準 Specifications | 試驗方法 Testing Method |
|---|---|---|---|---|
| 11 | 可焊性 Solderability of Leads | | 導線必須有3/4以上的面積均勻附着焊錫<br>Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction. | 引線浸入焊料中3±0.5秒鐘，浸入深度離導線根部1.5-2.0mm。<br>The lead wire of a capacitor should be dipped into molten solder for 3±0.5 sec. The depth of immersion is up to about 1.5 to 2.0mm from the root of lead wires.<br>焊錫溫度： 245±5ºC<br>Temp. of solder: 245±5ºC |
| 12 | 焊錫耐熱性 Soldering Effect | APP | 沒有可見損傷<br>No marked defect | 導線浸入離導線根部1.5-2.0mm處、錫溫爲260±10ºC中3.5±0.5秒<br>The lead wires should be immersed in solder of 260±5ºC up to 1.5 to 2.0mm from the root of terminal for 3.5±0.5 sec.<br>試驗後處理：電容必須存放在室溫下12小時。<br>Post-treatment: Capacitor should be stored for 12 h at room condition. |
| | | Δ C/C | ±0.5% or ±0.5pF<br>(Whichever is larger) | |
| | | IR | 符合初始標準<br>Meets initial specified value. | |
| 13 | 振動 Vibration Resistance | APP | 沒有可見損傷<br>No marked defect | 將電容器導線焊穩和調整振動頻率範圍爲10-55Hz、總振幅爲1.5mm，振動從10Hz到55Hz，然後再回到10Hz，大約一分鐘。<br>The capacitor should be firmly soldered to the supporting lead wire and vibrated at a frequency range of 10 to 55Hz, 1.5mm in total amplitude, with about a 1 minute rate of vibration change from 10Hz to 55Hz and back to 10Hz.<br>總時間六個小時，每兩小時在相互垂直方嚮來回三次。<br>Apply for a total of 6 hours, 2 hours each in 3 mutually perpendicular directions. |
| | | $C_R$ | 如第5項進行試驗，沒有不合格<br>Per Item 5. | |
| | | tan δ | 如第6項進行試驗，沒有不合格<br>Per Item 6. | |
| 14 | 温度特性 | | ΔC/C: 0±60PPM/ºC | 電容器必須按照下列每一步驟進行測量。<br>The capacitance measurement should be made at each step specified in below.<br>Step Temperature (±1ºC)<br>1 +20<br>2 -55 (Y5V: -30)<br>3 +20<br>4 +125 (Y5V, X5R: +85)<br>5 +20 |
| 15 | 耐濕負荷 Humidity Loading | APP | 沒有可見損傷<br>No marked defect | 施加額定電壓的電容保持在溫度爲40±2ºC、相對濕度爲90-95%條件下500±12小時。<br>Apply the rated voltage for 500±12 hours at 40±2ºC in 90 to 95% relative humidity.<br>試驗後處理：<br>Post-treatment:<br>電容必須貯存在室溫條件下24小時。<br>Capacitor should be stored for 24 h at room condition. |
| | | Δ C/C | ±2% or ±1pF<br>(Whichever is larger) | |
| | | tan δ | 小於初始標準的2倍<br>Less than 200% initial specified value. | |
| | | IR | 大於初始標準的25%<br>More than 25% initial specified value. | |
| 16 | 高温負荷 High Temperature Load | APP | 沒有可見損傷<br>No marked defect | 在125±2ºC (X5R, Y5V: 85ºC)、相對溫度不大於50%條件下施加下列額定電壓1000+48/-0小時（充/放電流小於2mA）<br>Apply a DC voltage of 150% of the following voltage for 1000+48/-0 hours at 125±2ºC (X5R, Y5V: 85ºC) with a relative humidity of 50% max. (Charge/discharge current ≤ 2mA)<br>100VDC<br>試驗後處理: 電容器應在室溫下儲存24小時。<br>Post-treatment: Capacitor shall be stored for 24 h at room condition. |
| | | Δ C/C | ±3% or ±1pF<br>(Whichever is larger) | |
| | | tan δ | 小於初始標準的1.5倍<br>Less than 1.50% of initial specified value. | |
| | | IR | 大於初始標準的50%<br>More than 50% initial specified value. | |
| 17 | 溫度循環 Temperature and Immersion Cycle | APP | 沒有可見損傷<br>No marked defect | 溫度循環試驗按以下條件進行試驗和測量<br>Temperature cycling shall be measured in the following test.<br>Step Temperature Time<br>1 -55±2ºC (Y5V, -30ºC) 30min<br>2 +125±2ºC(X5R, Y5V: 85ºC) 30min<br>循環次數：5次<br>Cycle numbers: 5 cycles<br>試驗後處理: 電容器應在室溫下儲存12小時。<br>Post-treatment: Capacitor shall be stored for 12 h at room condition. |
| | | Δ C/C | ±0.5% or ±0.5pF<br>(Whichever is larger) | |
| | | tan δ | 小於初始標準的1.5倍<br>Less than 150% of initial specified value. | |
| | | IR | 符合初始標準<br>Meets initial specified value. | |

## 6. 包裝和儲存
## PACKAGING AND STORAGE

### 6.1. 包裝
PACKAGING

盒裝編帶品，每盒2000pcs（每箱20 000pcs）。
Taping of ammo packing, 2000 pcs/box (20 000 pcs/carton)

散包包裝，每包1000pcs（視瓷片大小不等，每箱20 000～100 000pcs）。
Bulk packing, 1000 pcs/bag (Depending on the disc size, each carton is 20 000～100 000pcs)

### 6.2. 貯存條件
STORAGE ENVIRONMENT

電容器絕緣包封層不是完美的密封形式，因此，請勿將電容器存放在腐蝕性氣體中，尤其是存在氯氣、硫氣、酸、城、鹽等場所，同時應防潮。電容器應存放在溫度及相對濕度分別不超出5～40ºC及15～70%範圍的場所。
The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Store the capacitors where the temperature and relative humidity do not exceed 5 to 40 degrees centigrade and 15 to 70%.

請在6個月內使用電容器。超過6個月，在使用前確認其可焊性和電容量。
Use capacitors within 6 months after delivered. for more than 6 months, confirm the solderability and capacitance before use.

## 7. 測量和使用注意事項
## MEASURING AND APPLICATION NOTICE

### 7.1. 測量注意事項
Measurement notice

請在以下條件下測量。
Please measure under the following conditions.

#### 7.1.1. 標準大氣條件
Standard atmospheric conditions

除非另有規定，所有試驗和測量應按在IEC 60068-1的5.3中規定的試驗用標準大氣條件下表進行。
Unless otherwise specified, all tests and measurements shall be made under standard atmospheric conditions for testing as given in 5.3 of IEC 60068-1.

| 溫度<br>Temperature | 相對濕度<br>Relative humidity | 氣壓<br>Air pressure |
|---|---|---|
| 15ºC～35ºC | 25%～75% | 86kPa～106kPa |

在進行測量之前，電容器應在測量溫度下存放足夠時間，以使整個電容器都達到這一溫度。爲此目的，規定與試驗後恢復時間同樣的時間，通常是足夠的。
Before the measurements are made, the capacitor shall be stored at the measuring temperature for a time sufficient to allow the entire capacitor to reach this temperature. The period as prescribed for recovery at the end of a test is normally sufficient for this purpose.

在標準大氣條件下進行測量，其測量結果存在爭議時應采用仲裁溫度（見7.1.3）重復測量。
Test and measurement shall be made under standard atmospheric conditions for testing, in the event of a dispute, the measurements shall be repeated using one of the referee temperatures (as given in 7.1.3).

當按某一順序進行試驗時，一個試驗的最後測量可以作爲下一試驗的初始測量。
When tests are conducted in a sequence, the final measurements of one test may be taken as the initial measurements for the succeeding test.

在測量期間，不應使電容器受到氣流、陽光直射或可能引起誤差的其他影響。
During measurements the capacitor shall not be exposed to draughts, direct sunlight or other influences likely to cause error.

#### 7.1.2. 恢復條件
Recovery conditions

除非另有規定，恢復應在試驗用標準大氣條件（見7.1.1）下進行。
Unless otherwise specified recovery shall take place under the standard atmospheric conditions for testing (7.1.1).

如果恢復必須在嚴格控制的條件下進行，應采用IEC 60068-1中5.4.1的控制條件。
If recovery under closely controlled conditions is necessary, the controlled recovery conditions of 5.4.1 of IEC 60068-1 shall be used.

除非有關規範另有規定，恢復時間應爲1h～2h。
Unless otherwise specified in the relevant specification, a duration of 1 h to 2 h shall be used.

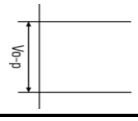
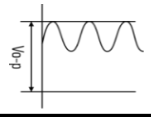
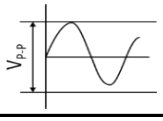
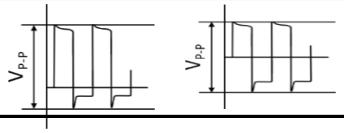
#### 7.1.3. 仲裁條件
Referee conditions

在仲裁情況下，應選用IEC 60068-1中5.2中規定的仲裁試驗用標準大氣條件。
For referee purposes, one of the standard atmospheric conditions for referee tests taken from 5.2 of IEC 60068-1, as given in table 1 below, shall be selected:

| 溫度<br>Temperature | 相對濕度<br>Relative humidity | 氣壓<br>Air pressure |
|---|---|---|
| 25ºC±1ºC | 48%～52% | 86kPa～106kPa |

### 7.2. 工作電壓
Operating voltage

嚮電容器施加的電壓切勿超過額定電壓。
The voltage applied to the capacitor must not exceed the rated voltage.

| 電壓<br>Voltage | 直流電壓<br>DC Voltage | 直流+交流電壓<br>DC+AC Voltage | 交流電壓<br>AC Voltage | 脈沖電壓<br>Pulse Voltage |
|---|---|---|---|---|
| 測量位置<br>Positional<br>Measurement |  |  |  |  |

在交流電路或紋波電流電路中使用直流額定電壓電容器時，請務必將外加電壓的Vp-p值或包含直流偏置電壓的Vo-p值維持在額定電壓範圍內。
When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range.
若嚮電路施加電壓，開始或停止時可能會因諧振或切換產生暫時的異常電壓。請務必使用額定電壓範圍包含這些異常電壓的電容器。
When the voltage is applied to the circuit, starting or stopping may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

7.3. 過電壓影響
Overvoltage effects
施加到電容器的過電壓可能會導致電容器內部介質層擊穿而引起電路短路。
The overvoltage applied to the capacitor may cause the dielectric layer of the capacitor to break down and cause a short circuit.
擊穿前的可持續時間取決於施加電壓和周圍溫度。
The duration before the breakdown depends on the applied voltage and the ambient temperature.

7.4. 焊錫
Soldering
當在PCB/PWB焊錫這個產品時，不要超過電容器的焊錫耐熱性標準。過度的熱量會使電容器內部焊錫熔化，可能導致熱沖擊而使陶瓷介質出現暗裂。
When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specifications of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

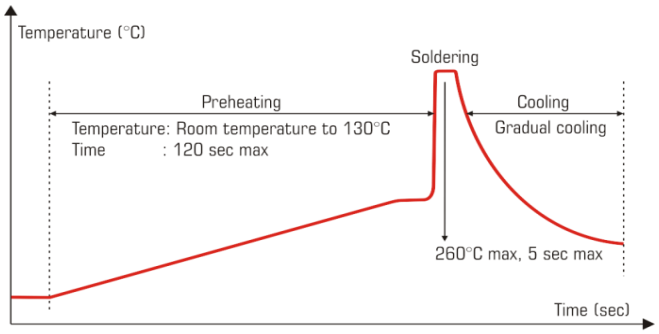
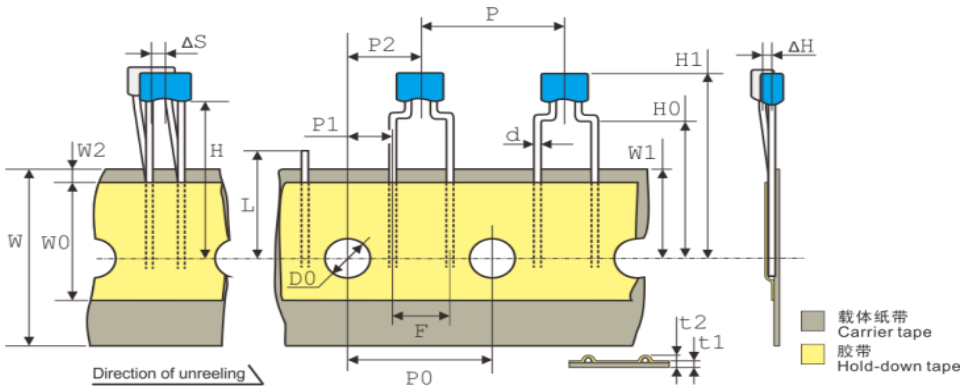


Fig.: Wave-soldering temperature-time profile to recommend

當使用烙鐵進行手工焊錫時，應該遵照下列條件：
When soldering capacitor with a soldering iron, it should be performed in the following conditions.
焊錫溫度：320ºC最大
Temperature of iron-tip: 320 degrees C. Max.
烙鐵頭：不超過40W
Soldering iron wattage: 40W max.
焊錫時間：不超過3.0秒
Soldering time: 3.0 sec. Max.

## 8. 編帶尺寸規格
TAPING SPECIFICATIONS




| 項目<br>Item | | 代碼<br>Symbol | 標準<br>Specification (mm) | 備注<br>Remarks |
|---|---|---|---|---|
| 導線直徑<br>Lead-wire diameter | | ød | 0.45±0.1 | |
| 元件間間距<br>Pitch of component | | P | | |
| 進料孔間距<br>Feed hole pitch | | P0 | | 间距累积误差：每20孔1.0mm<br>Cumulative pitch error: 1.0mm/20 pitch |
| 進料孔與導線垂直距離<br>Feed hole center to lead | | P1 | | |
| 進料孔與元件垂直距離<br>Hole center to component center | | P2 | | |
| 腳距<br>Lead-to-lead distance | | F | 5.08±0.8 | |
| 元件偏移<br>Component alignment | | Δh | ≤2.0 | |
| 元件沿編帶偏離，左或右<br>Deviation along tape, Left or right | | ΔS | ≤1.3 | |
| 紙帶寬<br>Tape width | | W | 18.0+1.0/-0.5 | |
| 膠帶寬<br>Hold-down tape width | | W0 | ≥7.0 | |
| 孔位<br>Hole position | | W1 | 9.0+0.75/-0.5 | |
| 膠帶位置<br>Hole-down tape position | | W2 | ≤3.0 | |
| 元件到紙帶的高度<br>Height of component from tape center | 直腳類型<br>Straight lead | H | 18.0+2/-0 | |
| | 彎腳類型<br>Kinked lead | H0 | 16.0±0.5 | |
| 元件高度<br>Component height | | H1 | ≤32.25 | |
| 進料孔直徑<br>Feed hole diameter | | D0 | 4.0±0.3 | |
| 編帶厚度<br>Total tape thickness | | t1 | ≤0.9 | 纸带厚度：0.5±0.1mm<br>Ground paper: 0.5±0.1mm |
| 編帶厚度（含導線）<br>Total thickness, tape and lead wire | | t2 | ≤1.5 | |
| 剪切长度<br>Length of snipped | | L | ≤11.0 | |