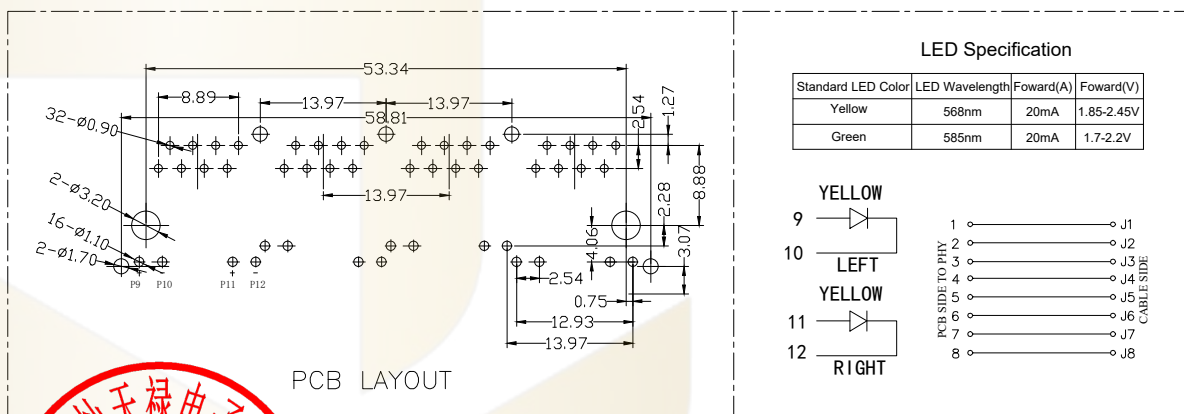




3. The mark with “*” is key inspection dimension for QC



PCB SIDE TO PHY

1	J1
2	J2
3	J3
4	J4
5	J5
6	J6
7	J7
8	J8

PCB LAYOUT

