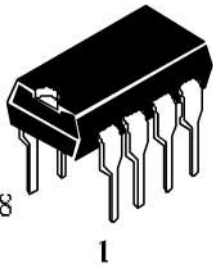


IL2418

Telephone Tone Ringer with Bridge Diode

The IL2418 is a monolithic integrated circuit telephone tone ringer with bridge diode, when coupled with an appropriate transducer, it replaces the electro-mechanical bell. The device is designed for use with either a piezo transducer or an inexpensive transformer coupled speaker to produce a pleasing tone composed of a high frequency (f_R) alternating with a low frequency (f_L) resulting in a warble frequency. The supply voltage is obtained from the AC ring signal and the circuit is designed so that noise on the line or variation of the ringing signal can not affect correct operation of the device.

- On chip high voltage full wave diode bridge rectifier
- Low current consumption, in order to allow the parallel operation of the 4 devices
- Low external component count
- Tone and switching frequencies adjustable by external components
- High noise immunity due to built-in voltage-current hysteresis
- Activation voltage adjustable
- Internal zener diodes to protect against over voltages
- Ringer impedance adjustable with external components

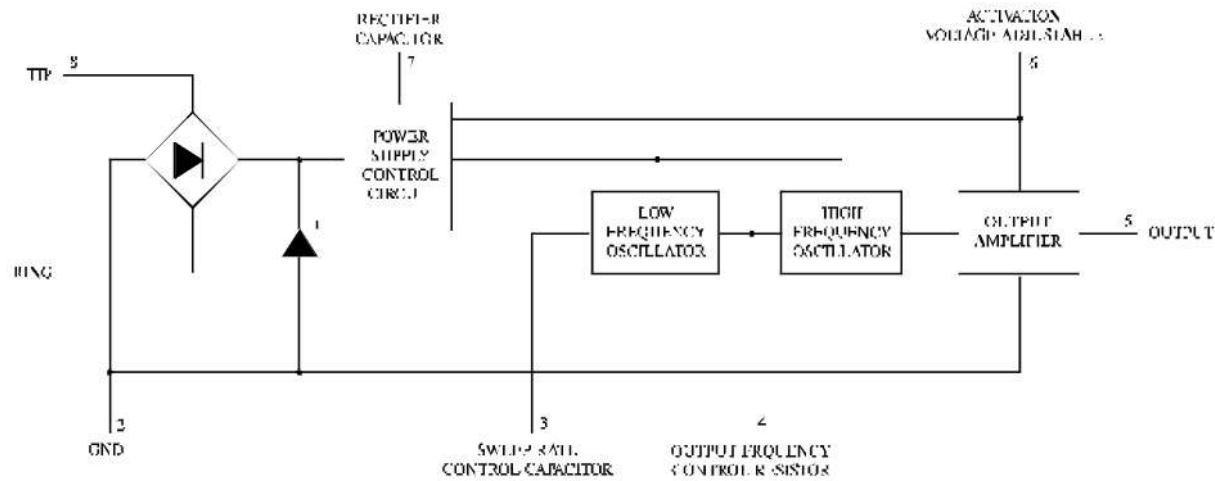


ORDERING INFORMATION
IL2418N Plastic
 $T_A = -20^{\circ}$ to 70° C
for package

PIN ASSIGNMENT

RING	1	8	TRP
GND	2	7	RC
SRC	3	6	AVA
OPR	4	5	OUT

BLOCK DIAGRAM



IL2418

MAXIMUM RATINGS* (TA = 25°C)

Symbol	Parameter	Value	Unit
VTP	Calling Voltage (f = 50 Hz) Continuous	120	Vrms
VTP	Calling Voltage (f = 50 Hz) 5 Sec ON/10 Sec OFF	200	Vrms
ICC	Supply Current	22	mA
Tstg	Storage and Junction Temperature	-65 to +150	°C

* Maximum Ratings are those values beyond which damage to the device may occur.
Functional operation should be restricted to the Recommended Operating Conditions.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
VCC	Supply Voltage		26	V
TA	Operating Temperature	-20	+70	°C

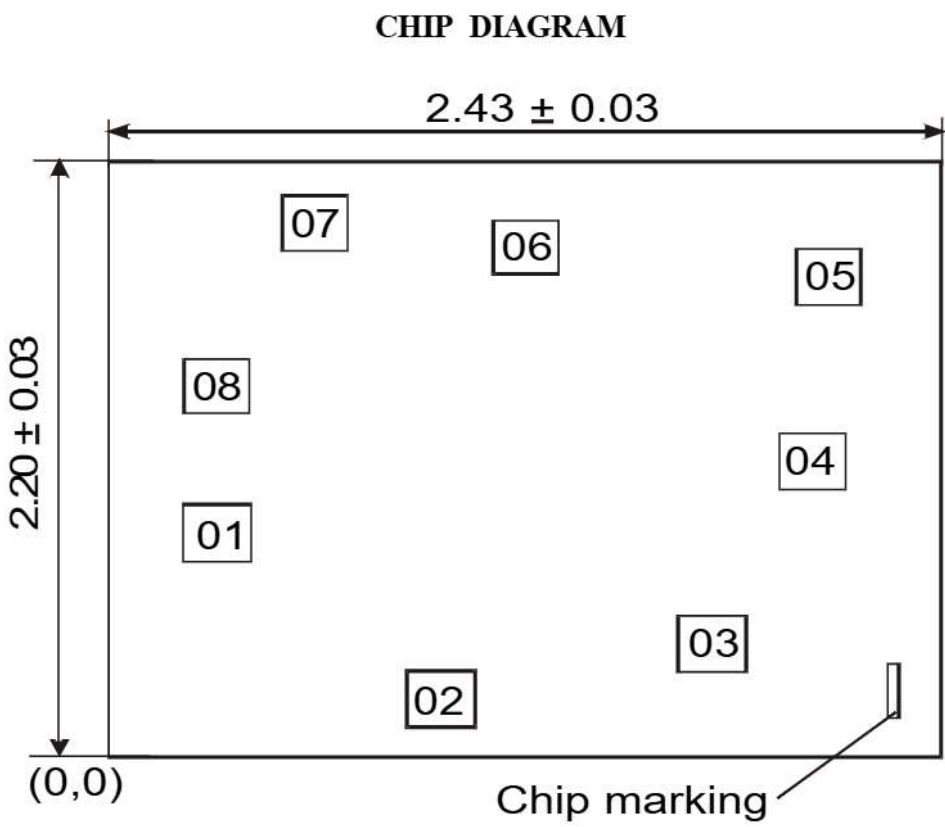
This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation, VIN and VOUT should be constrained to the range $GND \leq (V_{IN} \text{ or } V_{OUT}) \leq V_{CC}$.
Unused inputs must always be tied to an appropriate logic voltage level (e.g., either GND or VCC).
Unused outputs must be left open.

ELECTRICAL CHARACTERISTICS(TA = 25°C)

Symbol	Parameter	Test Conditions	Guaranteed Limits			Unit
			Min	Typ	Max	
ICC	Current Consumption without Load	VS=8.8 to 26 V			1.8	mA
VON	Activation Voltage		12.2		13	V
VONR	Activation Voltage Range	RA = 1 KΩ	8		10	V
VSUS	Sustaining Voltage		8		8.8	V
RD	Differential Resistance in Off Condition		6.4			KΩ
VOUT	Output Voltage Swing			VCC-3		V
IOUT	Short Circuit Current	VS = 26 V		35		mA

[illegible]

EE	1	:	1:	2:	3:	4	:	:	:	6	(4)	\		III	1	1	x	6	:
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Chip making (X=2,283, Y=0,165) 20
Size pad: 0.12 x 0.12 mm

PAD LOCATION

Pad No	Symbol	X	Y
01	RING	0.220	0.590
02	GND	0.920	0.100
03	SPC	1.630	0.230
04	OFR	2.075	0.810
05	OUT	2.090	1.620
06	AVA	1.150	1.820
07	RC	0.519	1.890
08	TIP	0.220	1.310

Pad size is given as per metallization layer

