

Power switching,
Built-in 32.768 kHz DTCXO, High Stability

S8900T3J

- Built-in frequency adjusted 32.768 kHz crystal unit and DTCXO
- Interface Type : I²C-Bus
- Interface voltage range : 1.8 V to 5.5 V
- Temp. compensated voltage range : 1.8 V to 5.5 V
- Timekeeping voltage range : 1.6 V to 5.5 V
- Auto power switching function : Automatically switches to backup power supply by monitoring the VDD voltage
- Interrupt output : Wake up every minute or every second
- Alarm interruption : Date, day, hour, minute

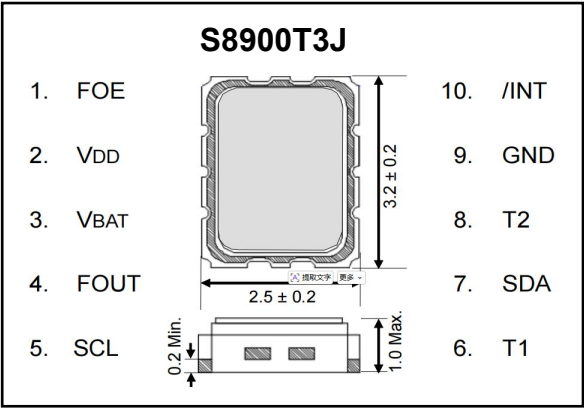


S8900T3J
3225-10

PIN Function

Pin	Pin Name	Description
#1	FOE	FOUT output enable control
#2	VDD	Power supply input
#3	VBAT	Backup battery power input; connect to VDD if battery switch function is not required
#4	FOUT	CMOS clock output controlled by FOE; default 32.768kHz, frequency configurable
#5	SCL	I ² C bus serial clock input
#6	T1	Factory test pin. Do not connect externally.
#7	SDA	I ² C address & data transmission; open-drain, external pull-up resistor required
#8	T2	No connection (NC)
#9	GND	Ground
#10	/INT	Alarm, timer & time update interrupt output; open-drain, external pull-up resistor required

Terminal connection (Unit: mm)



Electrical Characteristics

No.	Item	Condition	MIN	TYP	MAX	UNIT
1	Nominal Frequency	—	32.768			KHz
2	Operating Temperature	—	-40	—	85	°C
3	Storage Temperature	—	-55	—	125	°C
4	Frequency Stability	-40°C~85°C	-3.8	—	3.8	ppm
5	Supply Voltage (VDD)	—	1.8	3	5.5	V
6	Current Consumption	VDD=5V	—	0.85	—	μ A
		VDD=3.3V	—	0.8	—	μ A
7	Timer Supply Voltage (VCLK)	—	1.6	3	5.5	V
8	Temperature Compensation Voltage (VTEM)	—	1.8	3	5.5	V

Block diagram

