

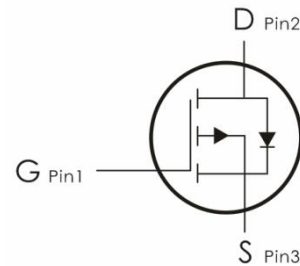
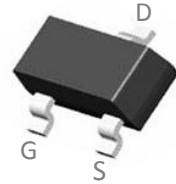
Description:

This P-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=-20V, I_D=-7A, R_{DS(on)} < 18m\Omega @ V_{GS}=-4.5V$ (Typ: $14m\Omega$)
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.
- 6) MSL3



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DO2021AA	2021A	SOT-23-3	3000 pcs/Reel

Absolute Maximum Ratings: ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D	Continuous Drain Current- $T_A=25^\circ\text{C}^1$	-7	A
	Continuous Drain Current- $T_A=100^\circ\text{C}^1$	-4.9	
I_{DM}	Pulsed Drain Current ²	-28	
P_D	Power Dissipation	2	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55-+150	$^\circ\text{C}$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62.5	$^\circ\text{C}/\text{W}$

Electrical Characteristics: ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	-20	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =-20V	---	---	-1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 12V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	Gate-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	-0.4	-0.65	-1	V
R _{DS(ON)}	Drain-Source On Resistance ³	V _{GS} =-4.5V, I _D =-7A	---	14	18	m Ω
		V _{GS} =-2.5V, I _D =-5A	---	18	23	m Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =-10V, V _{GS} =0V, f=1MHz	---	1730	---	pF
C _{oss}	Output Capacitance		---	242	---	
C _{rss}	Reverse Transfer Capacitance		---	231	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =-10V, I _D =-7A, R _{ENG} =2.5 Ω ,V _{GS} =-4.5V	---	10	---	ns
t _r	Rise Time		---	31	---	ns
t _{d(off)}	Turn-Off Delay Time		---	28	---	ns
t _f	Fall Time		---	8	---	ns
Q _g	Total Gate Charge	V _{GS} =-4.5V, V _{DS} =-10V, I _D =-3A	---	16	---	nC
Q _{gs}	Gate-Source Charge		---	2.2	---	nC
Q _{gd}	Gate-Drain “Miller” Charge		---	4.4	---	nC
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _{SD} =-7A	---	---	-1.2	V
I _S	Continuous Drain Curren	V _D =V _G =0V	---	---	-5.8	A
I _{SM}	Pulsed Drain Current		---	---	-23.3	A

Notes:

1. Computed continuous current assumes the condition of $T_{j,Max}$ while the actual continuous current depends on the thermal & electro-mechanical application board design
2. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Characteristics: ($T_A=25^\circ C$ unless otherwise noted)

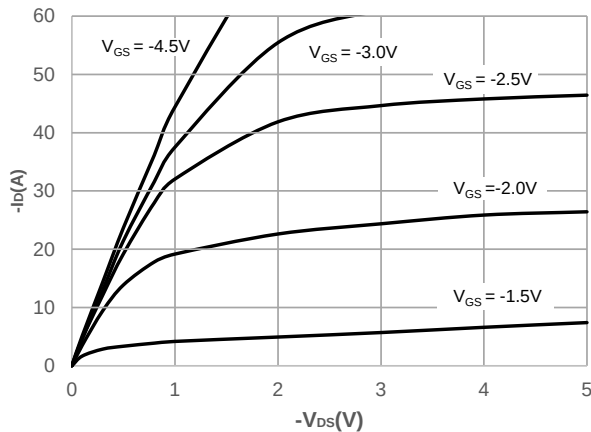


Figure 1: Output Characteristics

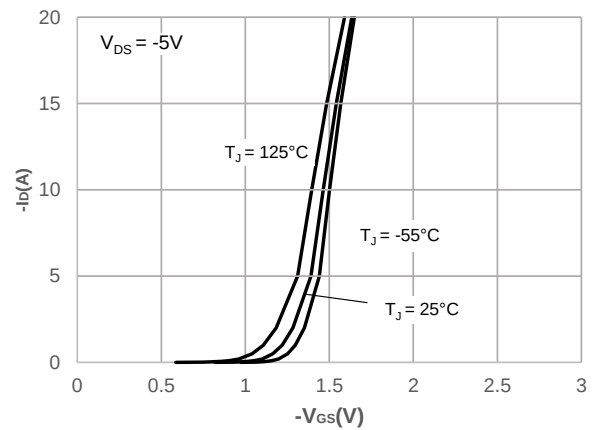


Figure 2: Typical Transfer Characteristics

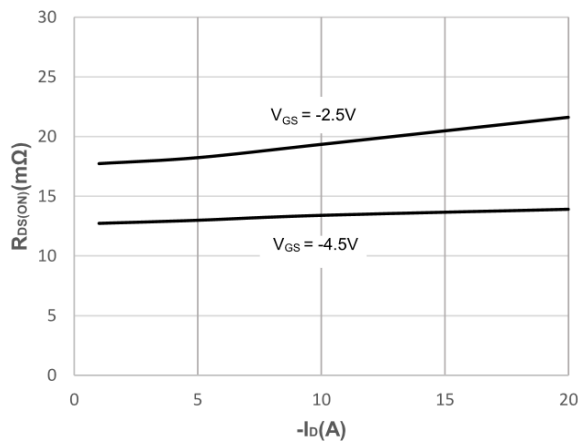


Figure 3: On-resistance vs. Drain Current

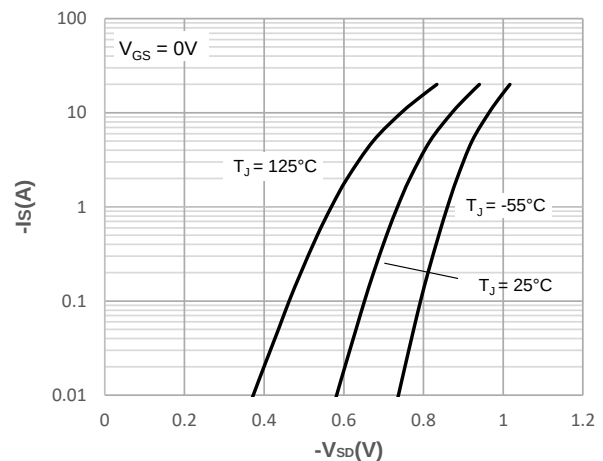


Figure 4: Body Diode Characteristics

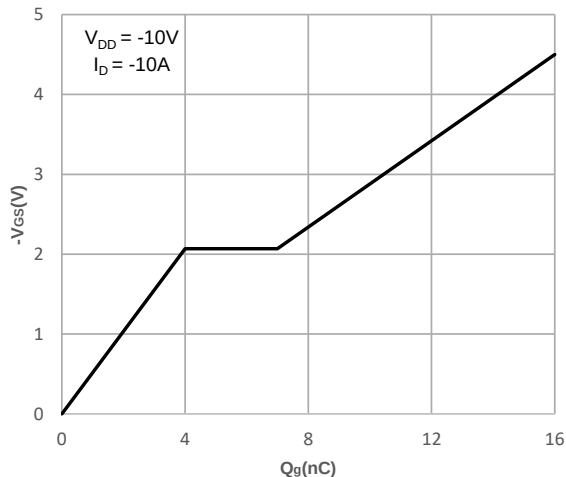


Figure 5: Gate Charge Characteristics

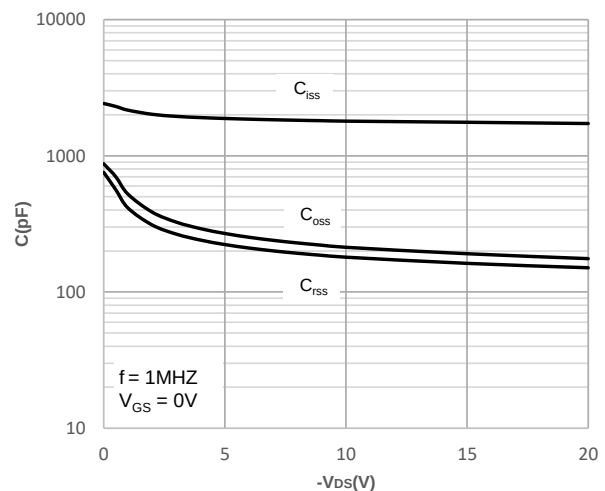


Figure 6: Capacitance Characteristics

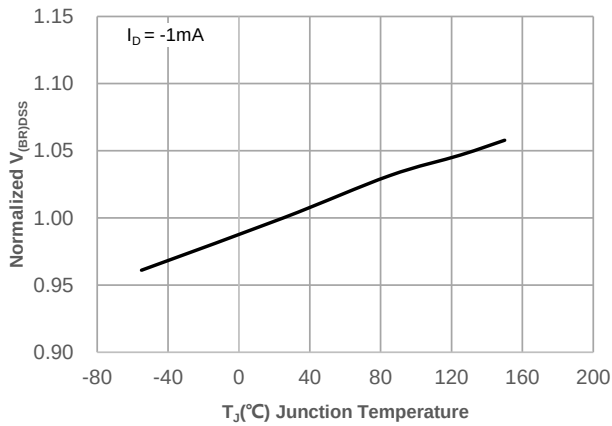


Figure 7: Normalized Breakdown voltage vs. Junction Temperature

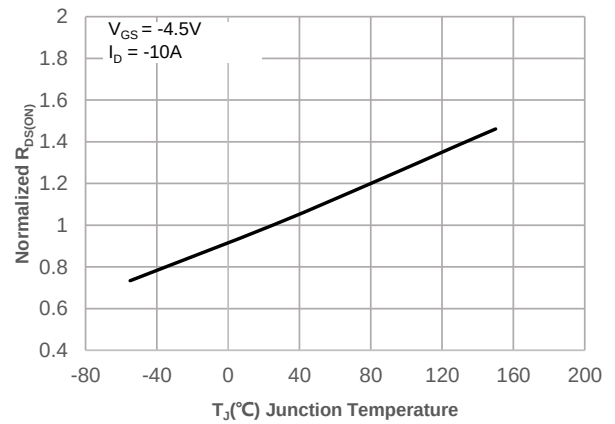


Figure 8: Normalized on Resistance vs. Junction Temperature

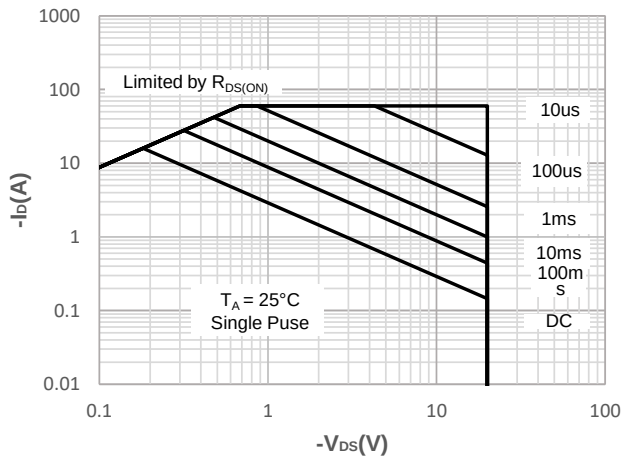


Figure 9: Maximum Safe Operating Area

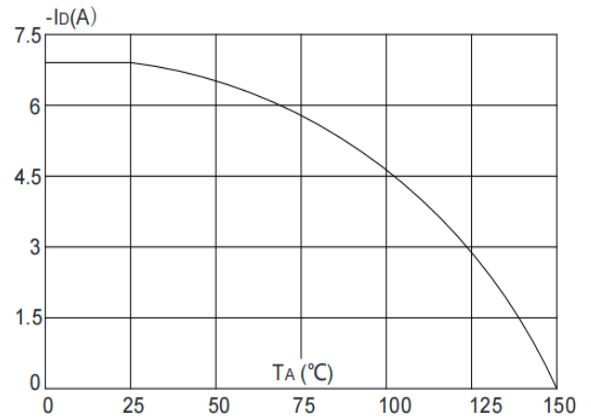


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

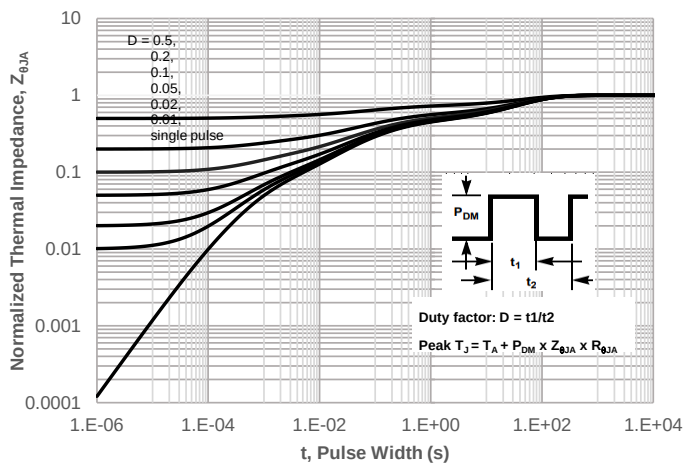


Figure 11: Normalized Maximum Transient Thermal Impedance

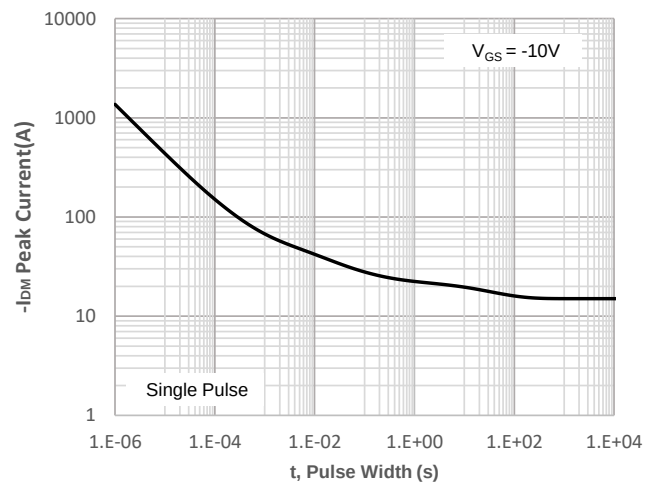
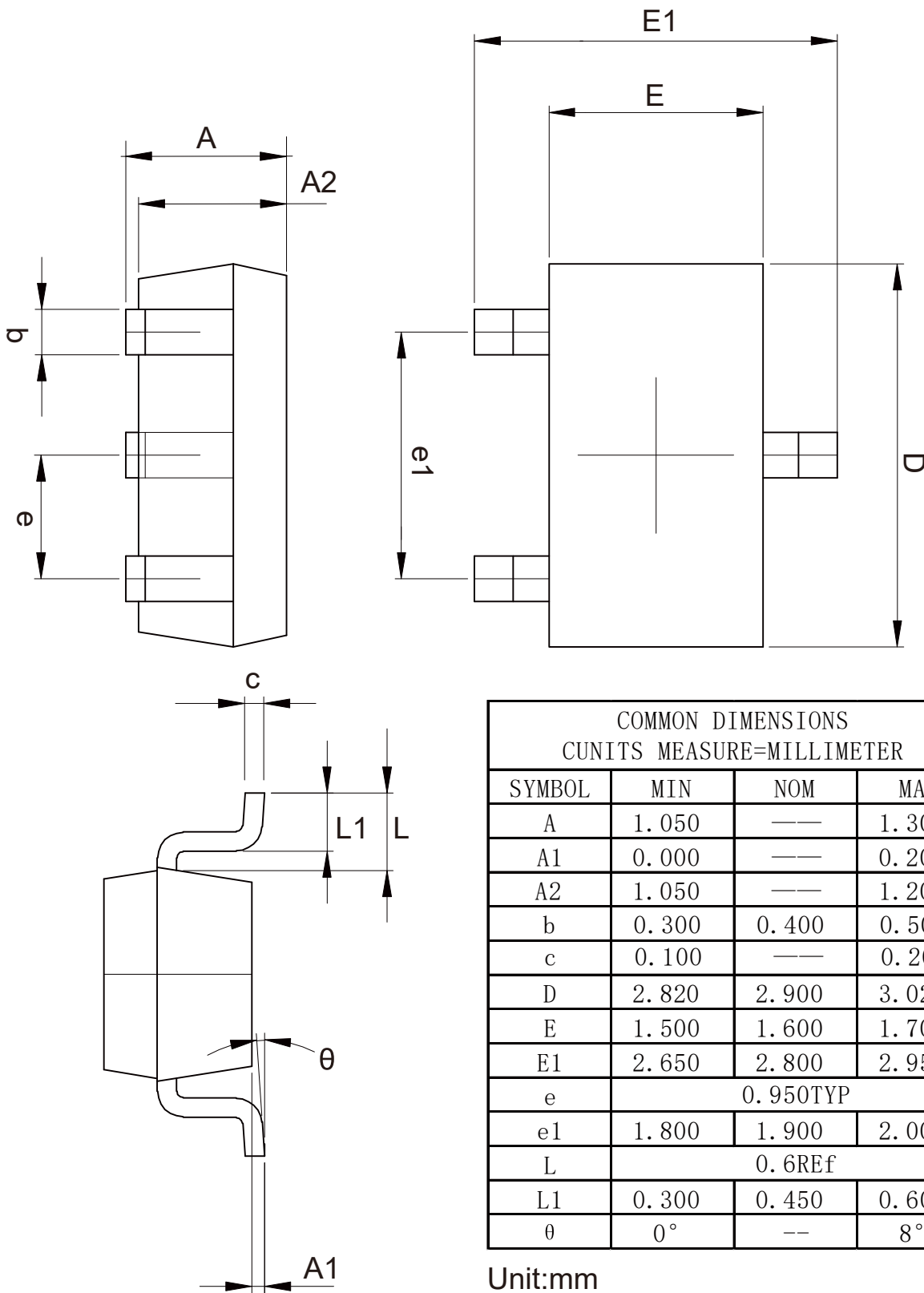


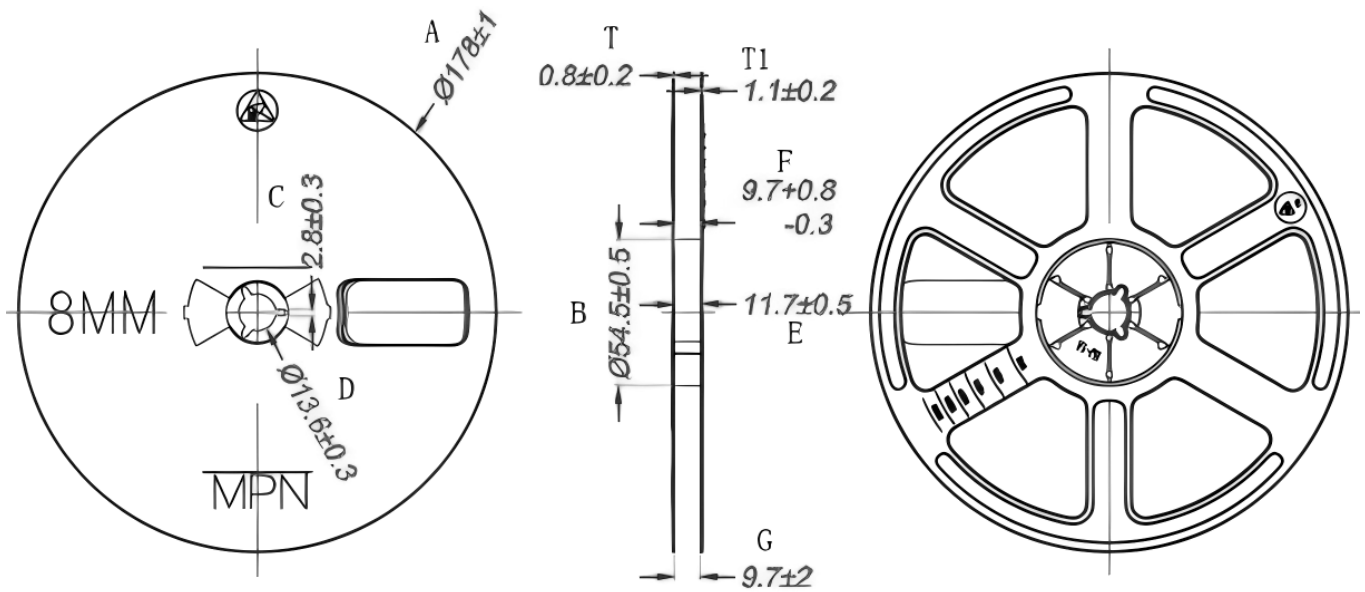
Figure 12: Peak Current Capacity

SOT-23-3 Package Outline Data

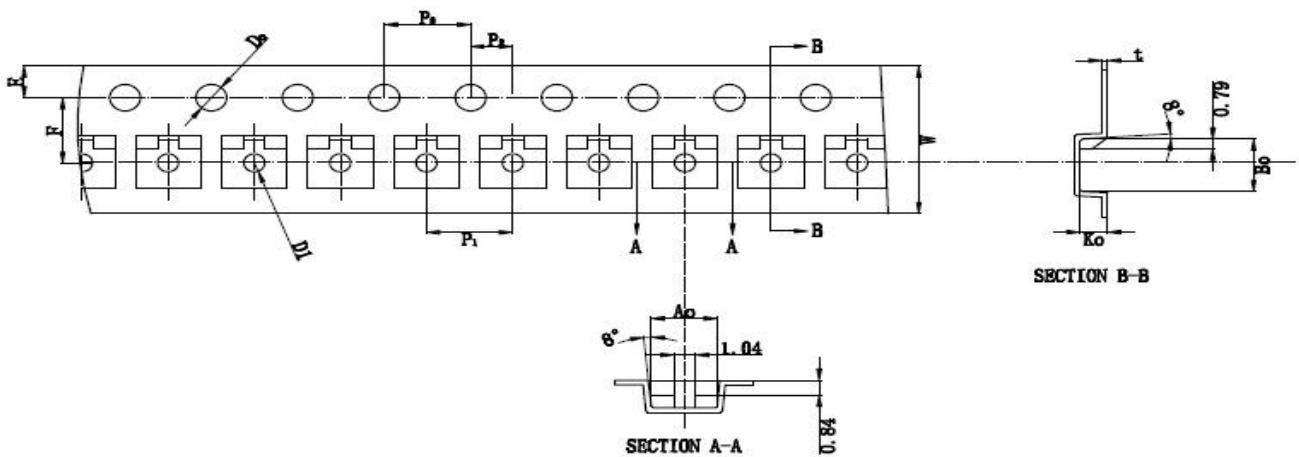


Tape & Reel Information

Dimensions in mm

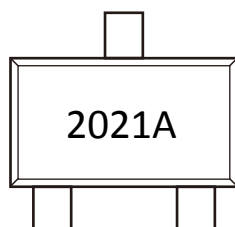


PKG TYPE	W	P	E	F	D	D1	Po	Po10	P2	A0	B0	K0	T
SOT23-3	8.00	4.00	1.75	3.50	1.50	1.00	4.00	40.00	2.00	3.13	3.22	1.30	0.20
Tolerance	± 0.2	± 0.1	± 0.1	± 0.1	± 0.1	± 0.1	± 0.1	± 0.2	± 0.1	± 0.1	± 0.1	± 0.1	± 0.03



Pulling direction 

Marking Information:



Previous Version

Version	Date	Subjects (major changes since last revision)
1.0	2025-09-08	Release of final version

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