

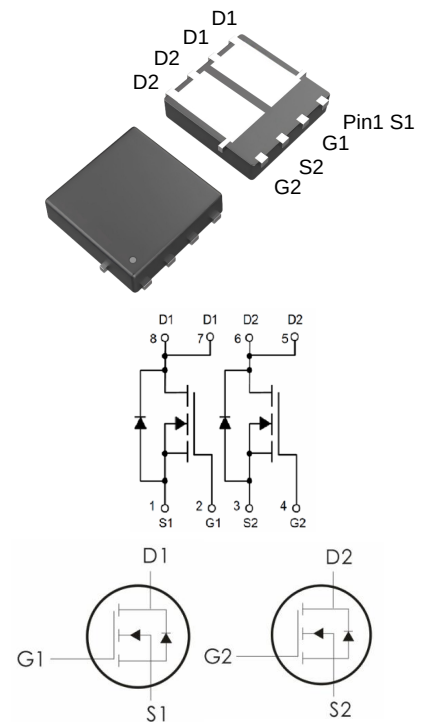
Description:

This Dual N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=30V, I_D=35A, R_{DS(on)} < 10m\Omega @ V_{GS}=10V$ (Typ: $8.5m\Omega$)
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.
- 6) MSL



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DOZ35DN03	35DN03	DFN3*3-8D	5000 pcs/Reel

Absolute Maximum Ratings: ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current ¹	35	A
	Continuous Drain Current- $T_c=100^\circ\text{C}$ ¹	24.5	
I_{DM}	Pulsed Drain Current ²	140	
P_D	Power Dissipation	22	W
E_{AS}	Single pulse avalanche energy ³	17.8	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55-+150	$^\circ\text{C}$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	5.68	$^\circ\text{C}/\text{W}$

Electrical Characteristics: ($T_c=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Sourctce Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	30	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =30V	---	---	1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	Gate-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1	1.5	2.5	V
R _{DS(ON)}	Drain-Source On Resistance ⁴	V _{GS} =10V, I _D =10A	---	8.5	10	m Ω
		V _{GS} =4.5V, I _D =5A	---	12	16	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	940	---	pF
C _{oss}	Output Capacitance		---	140	--	
C _{rss}	Reverse Transfer Capacitance		---	116	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =15V, I _D =20A, R _G =3 Ω ,V _{GS} =10V	---	6	---	ns
t _r	Rise Time		---	5	---	ns
t _{d(off)}	Turn-Off Delay Time		---	25	---	ns
t _f	Fall Time		---	7	---	ns
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =15V, I _D =10A	---	20	---	nC
Q _{gs}	Gate-Source Charge		---	6.3	---	nC
Q _{gd}	Gate-Drain “Miller” Charge		---	4.5	---	nC
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _{SD} =22A	---	---	1.2	V
I _S	Continuous Drain Curren	V _D =V _G =0V	---	---	29	A
I _{SM}	Pulsed Drain Current		---	---	116.6	A
T _{rr}	Reverse Recovery Time	I _F =10A, T _J =25 °C	---	7	---	ns
Q _{rr}	Reverse Recovery Charge	dl/dt=100A/us	---	6.3	---	nC

Notes:

1. Computed continuous current assumes the condition of $T_{j,Max}$ while the actual continuous current depends on the thermal & electro-mechanical application board design
2. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
3. EAS condition : $T_J=25^{\circ}C, V_{DD}=15V, V_G=10V, L=0.1mH$
4. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Characteristics: ($T_c=25^{\circ}C$ unless otherwise noted)

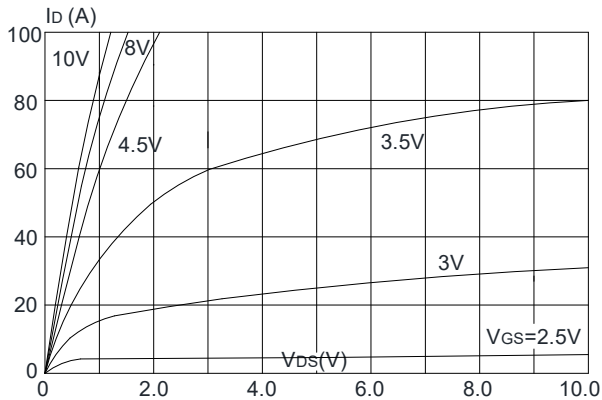


Figure 1: Output Characteristics

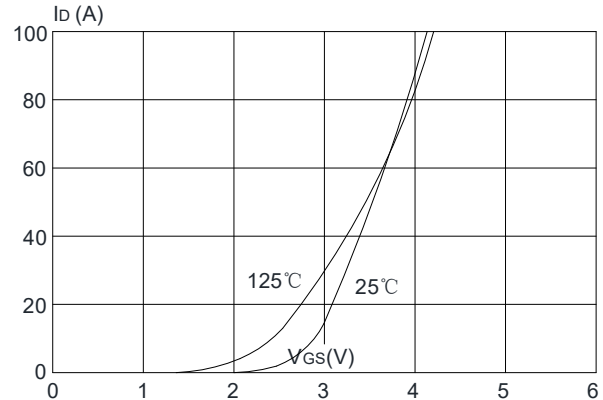


Figure 2: Typical Transfer Characteristics

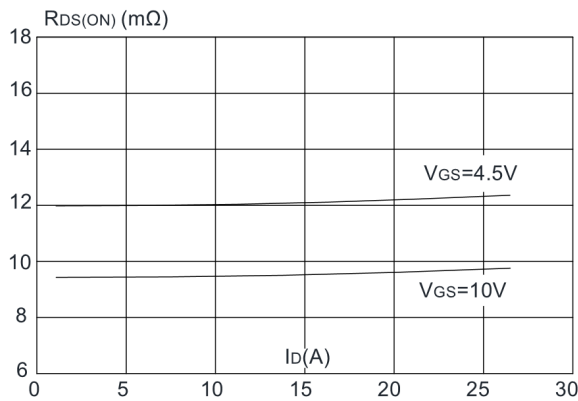


Figure 3: On-resistance vs. Drain Current

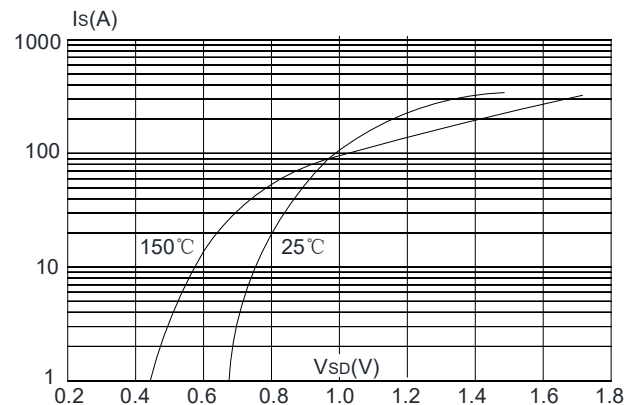


Figure 4: Body Diode Characteristics

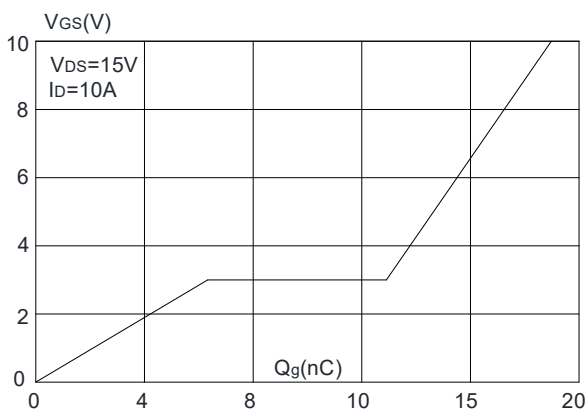


Figure 5: Gate Charge Characteristics

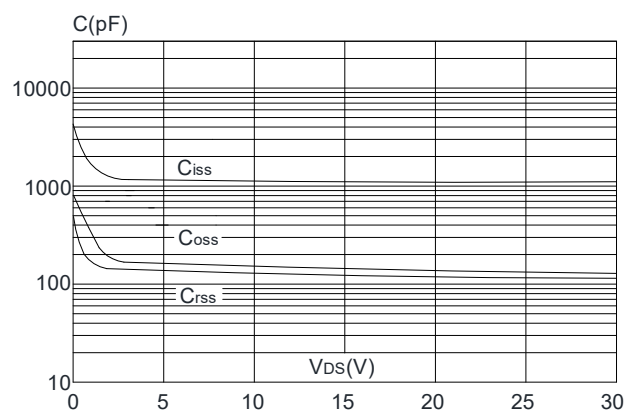


Figure 6: Capacitance Characteristics

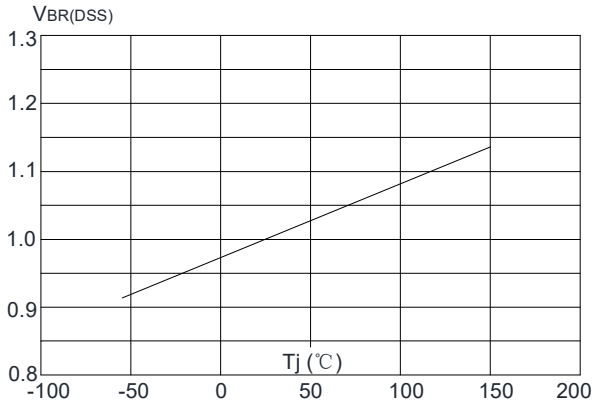


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

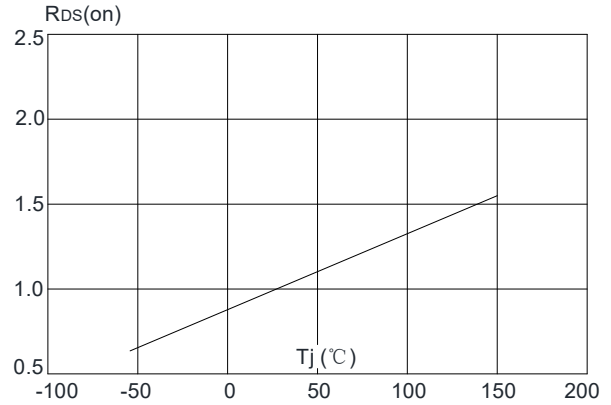


Figure 8: Normalized on Resistance vs. Junction Temperature

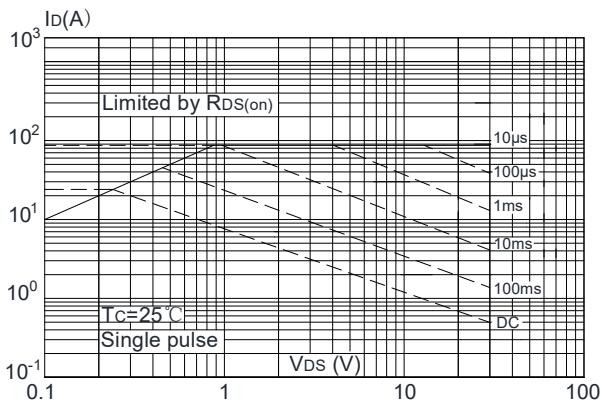


Figure 9: Maximum Safe Operating Area

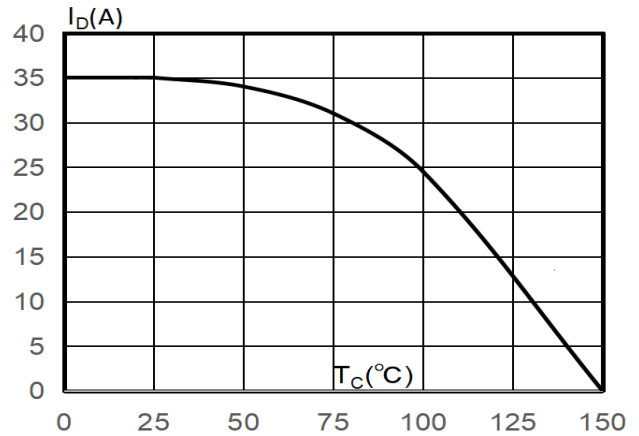


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

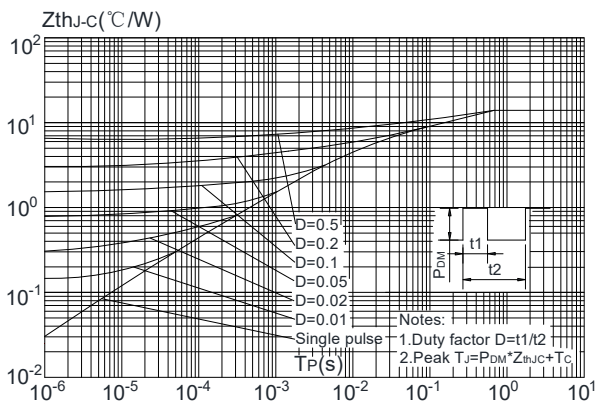
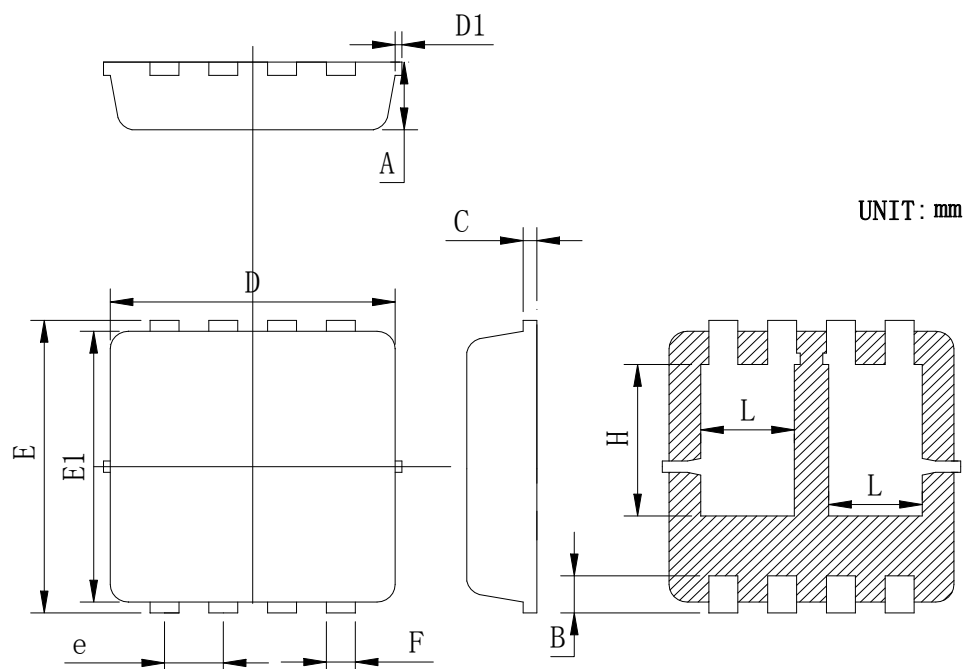


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

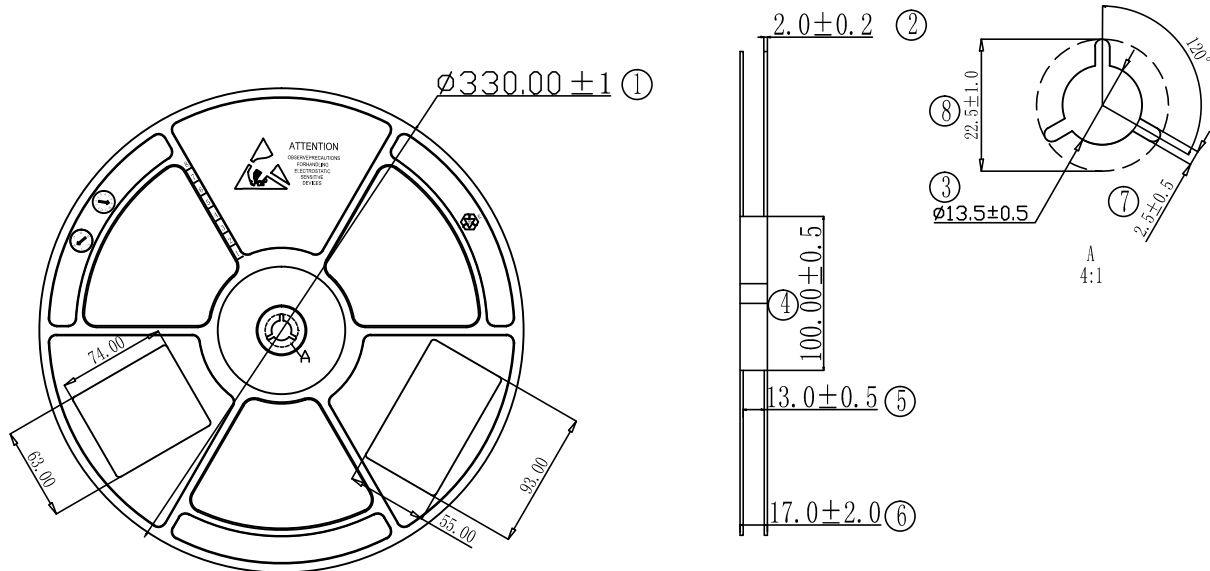
DFN3X3-8D Package Outline Data



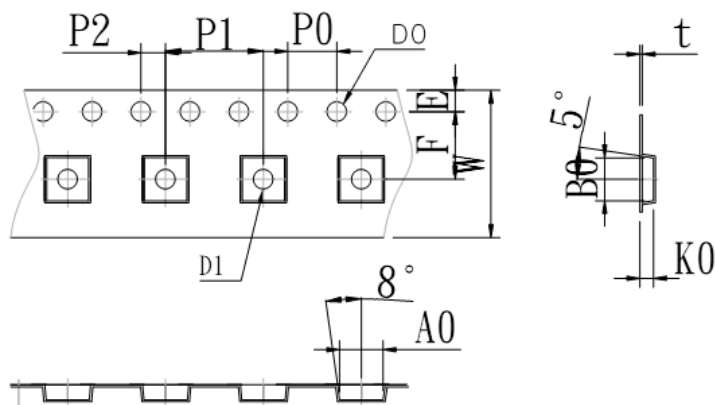
Symbol	Min	Typ	Max
A	0.725	0.775	0.825
B	0.28	0.38	0.48
C	0.13	0.15	0.20
D	3.05	3.15	3.25
D1			0.10
E	3.25	3.35	3.45
E1	3.0	3.1	3.2
e	0.60	0.65	0.70
F	0.27	0.32	0.37
H	1.63	1.73	1.83
L	0.93	1.03	1.13

Tape & Reel Information

Dimensions in mm



Symbol	A0	B0	K0	D0	D1	P0	P1	10*P0
Spec	3.55 $\pm$ 0.10	3.45 $\pm$ 0.10	1.13 $\pm$ 0.10	1.55 $\pm$ 0.10	1.55 $\pm$ 0.10	4.00 $\pm$ 0.10	8.00 $\pm$ 0.10	40.0 $\pm$ 0.10
Symbol	W	E	F	P2	t			
Spec	12.00 $\pm$ 0.10	1.75 $\pm$ 0.10	5.50 $\pm$ 0.10	2.00 $\pm$ 0.10	0.20 $\pm$ 0.05			



Pulling direction →

Marking Information:

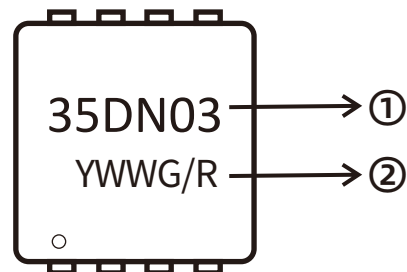
① . Part NO.

② . Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



Previous Version

Version	Date	Subjects (major changes since last revision)
1.1	2026-04-22	Release of final version

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