



产品承认书 SPECIFICATION FOR APPROVAL

客户名称:

CUSTOMER

我司料号:

OUR PART NO.

XRTLVR506050S75NMHDBC

我司品名:

OUR PART NAME

Mini Molding Power Inductors

送样日期:

DATE SAMPLES

数量:

QUANTITY

制造确认 MANUFACTURER APPROVE

拟制 DRAWN	审核 CHECKED	确认 APPROVED
HuFangting	RaoPin	Zhongcuilan

客户确认 CUSTOMER APPROVE

客户名称 CUSTOMER NAME:

客户料号 CUSTOMER P/N:

规格型号 DESCRIPTION:

Dimensions: 506050 Inductance: 0.075 uH

检查结果: ☐ 合格 ☐ 不合格

签名及盖章:

INSPECT RESULT ACCEPT REJECT

SIGNATURE AND STAMP

说明 REMARK:

如对本承认书内容有异议请提出或标记发送至我司, 本承认书在未收到异议回复时于本承认书提供一周后生效。

If you have any objection to the contents of this acknowledgement, please put forward or mark it and send it to our company. This acknowledgement will take effect one week after it is provided if you do not receive an objection reply.

东莞市祥如电子有限公司

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修订记录 Revision record

[illegible]

1. Features

- 1.1 Metal material for large current and low loss.
- 1.2 High performance (Isat) realized by metal dust core.
- 1.3 Low loss realized with low Rdc.
- 1.4 Closed magnetic circuit design reduces leakage flux.
- 1.5 Vinyl thermal spray, better surface compactness.
- 1.6 Environmental requirements must comply with the QESP-44 document
- 1.7 100% lead (Pb) free meet RoHS2.0 and Halogen , Reach and other legal and regulatory requirements standard.

2. Application

- 2.1 DC/DC converters.
- 2.2 Pad,Smart phone.
- 2.3 Portable gaming devices, Smart wear, Wi-Fi module.
- 2.4 Notebooks, VR, AR.
- 2.5 LCD displays, HDDs, DVCs, DSCs, etc.
- 2.6 Baseband power supply, Amplifier, Power management, Module power supply, Camera power manageme

3. Ordering Procedure

XRTLVR	5060	50	S	75N	M	H	D	B	-C
①	②	③	④	⑤	⑥	⑦	⑧	⑨	⑩

①Series Name: Molding Power Inductors

②External Dimensions(L×W):5060=5.0*6.0 mm

③External Dimensions(H):50=5.0 mm

④Size Tolerance:S=±0.2mm

⑤Inductance value:75N=0.075uH

⑥Tolerance: M=±20%

⑦Coating color: H=

⑧Product type:D=

⑨Special define: B~Z=Special

⑩special requirements:C=

For special characteristics, please refer to the specific values in Item 5 "Specifications".

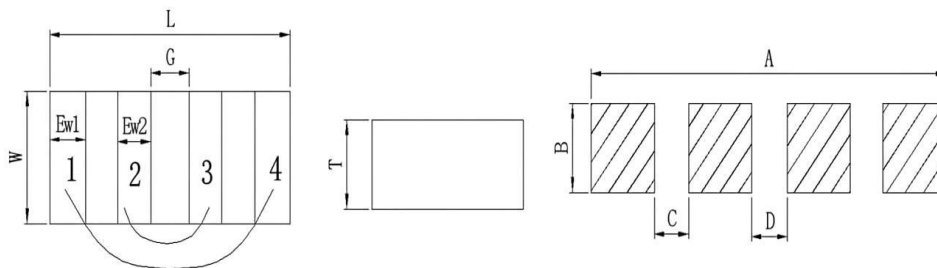
4. Product Category

① .SHAPE AND DIMENSIONS

VR :

Outline Dimensions

Recommend Land Pattern Dimensions



Units:mm

Series	L	G	W	Ew1	Ew2	T	A	B	C	D
XRTLVR506050S	5.8±0.2	0.7±0.2	5.0 Max	0.8±0.2	0.8±0.2	4.8±0.2	6.1	5.1	0.75	0.5

5. Marking

②. Marking(Black)

Note 1.: The inductor is marked with a 3-digit code Example -- 1.0μH → 1R0

Note 2.: The point on the top surface represents winding direction of choke

③.Product characteristic description

Note 1.: All test data is referenced to 25 °C ambient.

Note 2.: Test Condition:1MHz, 1.0Vrms.

Note 3.: Irms:DC current (A) that will cause an approximate ΔT of 40 °C.

Note 4.: Isat:DC current (A) that will cause L0 to drop approximately 30%.

Note 5.: Operating Temperature Range -55°C to + 125°C.

Note 6.: The part temperature (ambient + temp rise) should not exceed 125 under °C the worst case operating conditions. Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the end application.

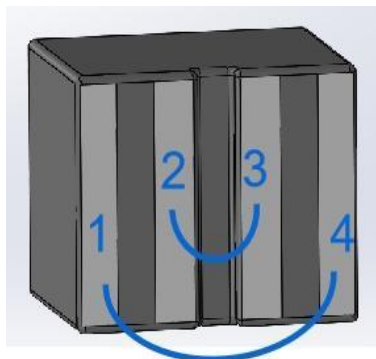
Note 7.: The rated current as listed is either the saturation current or the heating current depending on which value is lower

6.Specifications

单绕组-Ono Coil

P/N	L1~4 / L2~3 (μH)@(0A) 1MHz	Rdc1~4(mΩ)		Rdc2~3(mΩ)		Heat rating current Irms(A)		Saturation current Isat(A)	
		Typical	Max	Typical	Max	Typical	Max	Typical	Max
XRTLVR506050S75NMHDBC	0.075	0.250	0.3	0.58	0.65	25	20	55	50

7.Structure

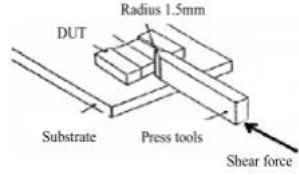
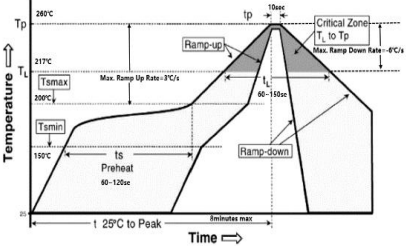
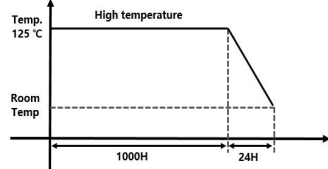
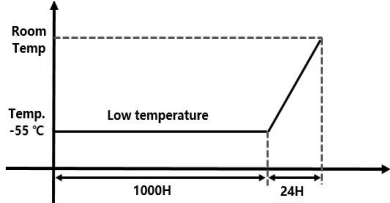


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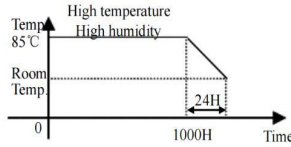
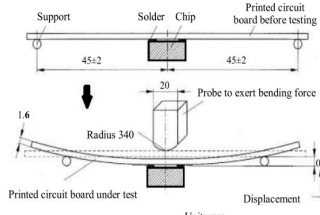
9. Product test

9.1 Reliability

No.	Item	Requirements	Test Methods and Remarks	Reference	Sample Size
1	Solderability	(1) No case deformation or change in appearance. (2) Terminal area must have 95% min. Solder coverage.	①Temperature: $245 \pm 5^{\circ}\text{C}$. ②Solder Composition: Sn/Ag3.0/Cu0.5(Pb Free). ③Sample immersion tin furnace $5 \pm 0.5\text{s}$.	AEC-Q200 (J-STD 002)	32
2	Adhesion of teral electrode	(1) Strong bond between the pad and the core, without come off PCB.	①Preconditioning: 245°C Reflow 3 times ②Inductors shall be subjected to $(260 \pm 0/-5^{\circ}\text{C})^{\circ}\text{C}$ for $(10 \pm 5)\text{s}$ Soldering in the base whit 0.3mm solder. ③Aplombelectrode way plus tax X N for (10 ± 1) seconds. 	AEC-Q200 (AEC-Q200-006)	32
3	Reflow test	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① The peak temperature: $260 \pm 0/-5^{\circ}\text{C}$. ② Reflow: 3 times. ③ Temperature curve is as below 	AEC-Q200 (MIL-STD-202 Method 210)	32
4	High temperature	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: Bake at $125 \pm 5^{\circ}\text{C}$ for $24 \pm 0.5\text{H}$, 245°C Reflow 3 times ②Temperature: $125 \pm 2^{\circ}\text{C}$. ③ Time : 1000 hours. ④Measurement at 24 ± 4 hours after test conclusion 	AEC-Q200 (MIL-STD -202 Method 108)	77
5	Low temperature	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	①Preconditioning: Bake at $125 \pm 5^{\circ}\text{C}$ for $24 \pm 0.5\text{H}$, 245°C Reflow 3 times ②Temperature: $-55 \pm 2^{\circ}\text{C}$. ③Time : 1000 hours. ④ Measurement at 24 ± 4 hours after test conclusion 	JESD22-A119A	77

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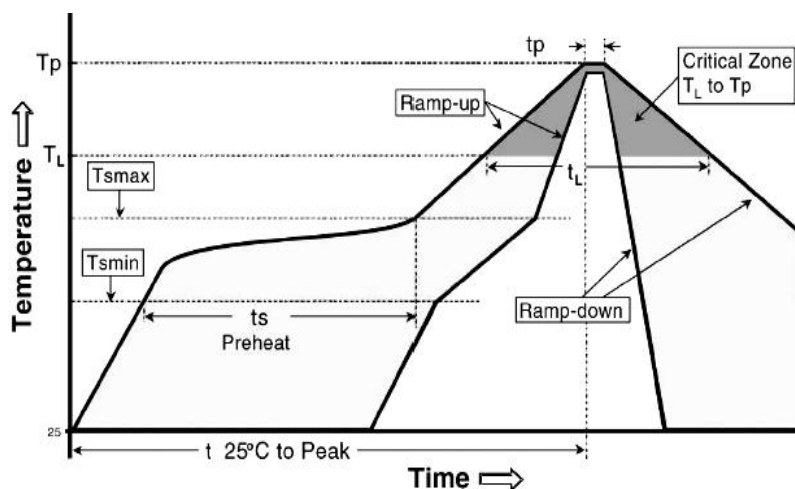
SPECIFICATION FOR APPROVAL

6	Thermal shock	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	①Preconditioning: Bake at $125\pm 5^\circ\text{C}$ for $24\pm 0.5\text{H}$, 245°C Reflow 3 times ②Repeat 500 cycle as follow : ($-55\pm 2^\circ\text{C}$, $30\pm 3\text{minutes}$)、(Room temperature, 5 minutes)、($+125\pm 2^\circ\text{C}$, $30\pm 3\text{minutes}$)、(Room temperature, 5 minutes) ③Measurement at 24 ± 4 hours after test conclusion	MIL-STD-202 Method 107	77
7	Resistance to SolderingHeat	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	①Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free). ②Solder Temperature: $260\pm 5^\circ\text{C}$. ③Immersion Time: $10\pm 1\text{sec}$.	AEC-Q200 (MIL-STD-202 Method 210)	32
8	Static Humidity	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	①Preconditioning: Bake at $125\pm 5^\circ\text{C}$ for $24\pm 0.5\text{H}$, 245°C Reflow 3 times ②1000 hours, $85^\circ\text{C}/85\%\text{RH}$. ③ Unpowered. ④Measurement at 24 ± 4 hours after test conclusion 	AEC-Q200 (MIL-STD-202 Method 103)	77
9	Board Flex	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	①Preconditioning: 245°C Reflow 3 times ②Part mounted on a $100\text{mm}\times 40\text{mm}$ FR4 PCB board, which is $1.6\pm 0.2\text{ mm}$ thick and as a Layer-thickness $35\text{ }\mu\text{m} \pm 10\text{ }\mu\text{m}$. ③ Bending speed is 1mm/s . ④ Keeping the P.C Board 2 mm minimum for 60 seconds. 	AEC-Q200 (AEC-Q200-005)	30
10	Vibration	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	①Preconditioning: 245°C Reflow 3 times ②Frequency range : 10~2000Hz. ③Amplitude: 1.5mm or 20g. ④ Sweep time and duration: 10~2000~10Hz for 20 minutes. ⑤ Each four hours in X,Y,Z direction, 12hours in total.	AEC-Q200 (MIL-STD-202 Method 204)	32
11	Mechanical Shock	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: 245°C Reflow 3 times ②Peak acceleration: 100G/S ③Duration of pulse: 6ms ④3times in each of 6($\pm X, \pm Y, \pm Z$) axes.	AEC-Q200 (MIL-STD-202 Method 213)	32
12	Loading at High Temperature	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	①Preconditioning: Bake at $125\pm 5^\circ\text{C}$ for $24\pm 0.5\text{H}$, 245°C Reflow 3 times ②Temperature: $85\pm 2^\circ\text{C}$. ③ Time : 1000 hours. ④ Applied Current : Rated current. ⑤Measurement at 24 ± 4 hours after test conclusion	AEC-Q200 (MIL-STD-202 Method 108)	77

9.2 Soldering Condition

(This is for recommendation, please customer perform adjustment according to actual application)

Recommend Reflow Soldering Profile: (solder : Sn96.5 / Ag3 / Cu0.5)



Profile Feature	Lead (Pb)-Free solder
Preheat: Temperature Min (T _{smin}) Temperature Max (T _{smax}) Time (T _{smin} to T _{smax}) (ts)	150°C 200°C 60 -120 seconds
Average ramp-up rate: (T _s max to T _p)	3°C / second max.
Time maintained above : Temperature (T _L) Time (t _L)	217°C 60-150 seconds
Peak Temperature (T_p)	260°C
Time within +10 /-5°C of actual peak Temperature (t_p)²	10 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8minutes max

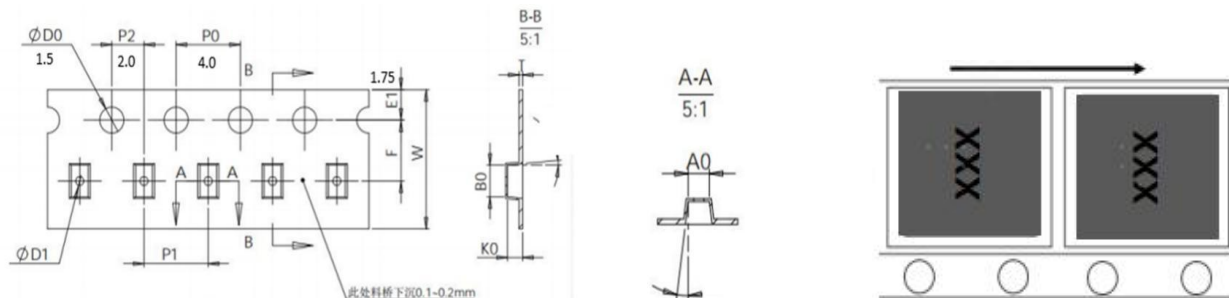
Allowed Re-flow times : 2 times

Remark : To avoid discoloration phenomena of chip on terminal electrodes, please use N2 Re-flow furnace .

10. Packing

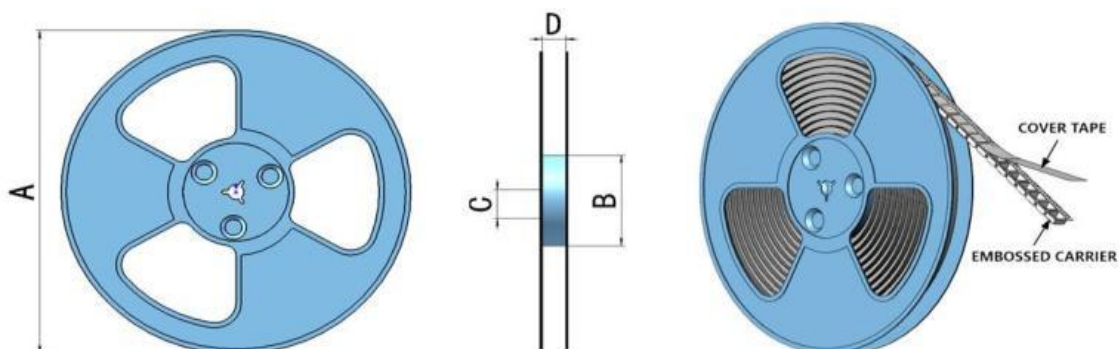
10.1 Dimension of plastic taping: (Unit: mm)

The following dimensions are related to the actual fit of the machine, for reference only



Series/mm	W	A0	B0	D1	F	K0	P1	T
公差	/	/	/	±0.20	±0.10	/	±0.10	±0.05
506050	16.0±0.30	6.20±0.10	5.20±0.10	1.5	7.5	5.20±0.10	12.0	0.5

10.2 Dimension of Reel: (Unit: mm)



Series/mm	A	B	C	D	QTY/Reel
公差	±2.0	±1.0	±1.0	±1.0	/
506050	330	100	13.5	17.5	800pcs

11. Note

11.1 recommend products store in warehouse with temperature between 15 to 35℃ under humidity between 25 to 75%RH. Even under storage conditions recommended above, solder ability of products will be degraded stored over 1 year old.

11.2 Cartons must be placed in correct direction which indicated on carton, otherwise the reel or wire will be deformed.

11.3 Storage conditions as below are inappropriate:

- Stored in high electrostatic environment
- Stored in direct sunshine, rain, snow or condensation.
- Exposed to sea wind or corrosive gases, such as Cl₂, H₂S, NH₃, SO₂, NO₂, etc.

11.4 The products are used in circuit board thickness greater than 1.6mm. If customers use less than the thickness of the circuit board that you should confirm with the company, in order to recommend a more suitable product.