

Features

- Wide supply voltage range from 0.8V to 2.7V
- Inputs accept voltages up to 2.7V
- I_{OFF} supports partial-power-down mode
- Low static power consumption; I_{CC}=10μA (Max.)
- ESD Protection Exceeds JESD 22
 - 2000-V Human-Body Model (A114-A)
 - 200-V Machine Model (A115-A)
 - 1000-V Charged-Device Model (C101)

General Description

The SN74AUC1G02DBVR-TP is a low-power single 2-input positive-NOR gate which provides the Function $Y = \overline{A+B}$ or $Y = \overline{A} \cdot \overline{B}$ in positive logic. This device ensures a very low static and dynamic power consumption across the entire VCC range from 0.9V to 3.6V.

Applications

- AV Receiver
- Audio Dock:Portable
- Blu-ray Player and Home Theater
- Embedded PC
- Personal Digital Assistant(PDA)
- Power:Telecom/Server AC/DC Supply:Single Controller:Analog and Digital
- Solid State Drive(SSD):Client and Enterprise
- Wireless Headset,Keyboard,and Mouse

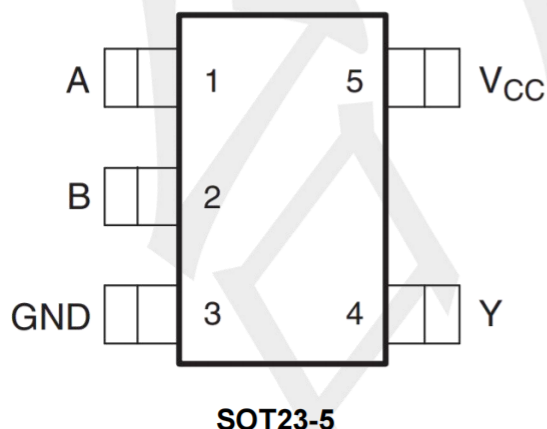
Logic Diagram



Ordering Information

ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION
SN74AUC1G02DBVR-TP	SOT23-5	Tape and Reel,3000

Pin Configuratio (TOP VIEW)



Function Table (each gate)

Input		Output
A	B	Y
L	L	H
L	H	L
H	L	L
H	H	L

Note:H: HIGH voltage level;L: LOW voltage level.

Absolute Maximum Ratings

PARAMETER	SYMBOL	CONDITIONS	RATINGS	UNIT
Supply Voltage	V _{CC}		-0.5 ~ +3.6	V
Input Voltage	V _{IN}		-0.5 ~ +3.6	V
Output Voltage	V _{OUT}	Output in the high or low state	-0.5 ~ +V _{CC} +0.5V	V
		Output in the power-off state	-0.5 ~ +3.6	V
VCC or GND Current	I _{CC}		±50	mA
Continuous Output Current	I _{OUT}	V _{OUT} =0~V _{CC}	±20	mA
Input Clamp Current	I _{IK}	V _{IN} <0	-50	mA
Output Clamp Current	I _{OK}	V _{OUT} <0	-50	mA
Storage Temperature Range	T _{STG}		-65 ~ +150	°C
Junction to Ambient	θ _{JA}	SOT-23-5	230	°C/W

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Recommended Operating Conditions

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V _{CC}	Operating	0.8	--	2.7	V
Input Voltage	V _{IN}		0	--	2.7	V
Output Voltage	V _{OUT}	High or low state	0	--	V _{CC}	V
Input Transition Rise or Fall Rate	Δt/Δv	V _{CC} =0.8V ~ 2.7V	--	--	200	ns/V
Operating Temperature	T _A		-40	--	+125	°C

Electrical Characteristics (T_A=25°C ,unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-level Input Voltage	V _{IH}	V _{CC} =0.8V	0.7×V _{CC}	--	--	V
		V _{CC} =1.1V ~ 1.95V	0.65×V _{CC}	--	--	V
		V _{CC} =2.3V ~ 2.7V	1.6	--	--	V
Low-level Input Voltage	V _{IL}	V _{CC} =0.8V	--	--	0.3×V _{CC}	V
		V _{CC} =1.1V ~ 1.95V	--	--	0.35×V _{CC}	V
		V _{CC} =2.3V ~ 2.7V	--	--	0.7	V
High-Level Output Voltage	V _{OH}	V _{CC} =0.8 ~ 2.7V, I _{OH} =-20μA	V _{CC} -0.1	--	--	V
		V _{CC} =1.1V, I _{OH} =-1.1mA	0.75×V _{CC}	--	--	V
		V _{CC} =1.4V, I _{OH} =-1.7mA	1.11	--	--	V
		V _{CC} =1.65V, I _{OH} =-1.9mA	1.32	--	--	V
		V _{CC} =2.3V	I _{OH} =-2.3mA	2.05	--	V
			I _{OH} =-3.1mA	1.9	--	V
		V _{CC} =2.7V	I _{OH} =-2.7mA	2.42	--	V
			I _{OH} =-4mA	2.3	--	V
Low-Level Output Voltage	V _{OL}	V _{CC} =0.8 ~ 2.7V, I _{OL} =20μA	--	--	0.1	V
		V _{CC} =1.1V, I _{OL} =1.1mA	--	--	0.3×V _{CC}	V
		V _{CC} =1.4V, I _{OL} =1.7mA	--	--	0.31	V
		V _{CC} =1.65V, I _{OL} =1.9mA	--	--	0.31	V
		V _{CC} =2.3V	I _{OL} =2.3mA	--	0.31	V
			I _{OL} =3.1mA	--	0.44	V
		V _{CC} =2.7V	I _{OL} =2.7mA	--	0.31	V
			I _{OL} =4mA	--	0.44	V
Input Leakage Current	I _{I(LEAK)}	V _{CC} =0 ~ 2.7V, V _{IN} =GND ~ 2.7V	--	--	±0.1	μA
Power OFF Leakage Current	I _{off}	V _{CC} =0 V, V _{IN} or V _{OUT} =0 ~ 2.7V	--	--	±0.2	μA
Additional Power OFF Leakage Current	ΔI _{off}	V _{CC} =0 V~0.2V, V _{IN} or V _{OUT} =0 ~ 2.7V	--	--	1	μA
Quiescent Supply Current	I _{CC}	V _{CC} =0.8 ~ 2.7V, V _{IN} =V _{CC} or GND, I _{OUT} =0	--	--	10	μA
Additional Quiescent Supply Current Per Input Pin	ΔI _{CC}	V _{CC} =2.7 V, V _{IN} =V _{CC} -0.6V, I _{OUT} =0	--	--	40	μA
Input Capacitance	C _I	V _{CC} =0V-2.7V, V _{IN} =V _{CC} or GND	--	1.5	--	pF
Output Capacitance	C _{OUT}	V _{CC} =0V, V _{OUT} =GND	--	3	--	pF

SN74AUC1G02DBVR-TP

SINGLE 2-INPUT POSITIVE-NOR GATE

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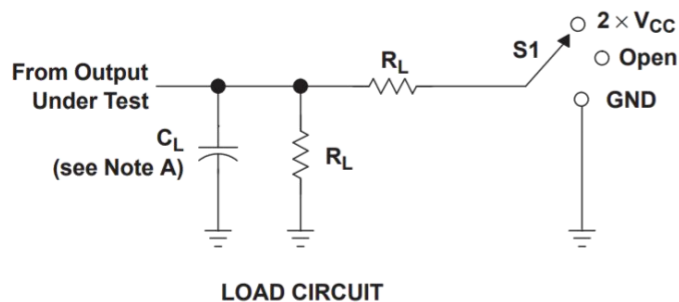
OPERATING CHARACTERISTICS (f=10MHz, TA =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	C _{PD}	V _{CC} =0.8V	--	4.1	--	pF
		V _{CC} =1.2±0.1V	--	4.1	--	pF
		V _{CC} =1.5±0.1V	--	4.1	--	pF
		V _{CC} =1.8±0.15V	--	4.1	--	pF
		V _{CC} =2.5±0.2V	--	4.2	--	pF

SWITCHING CHARACTERISTICS (TA =25°C, unless otherwise specified)

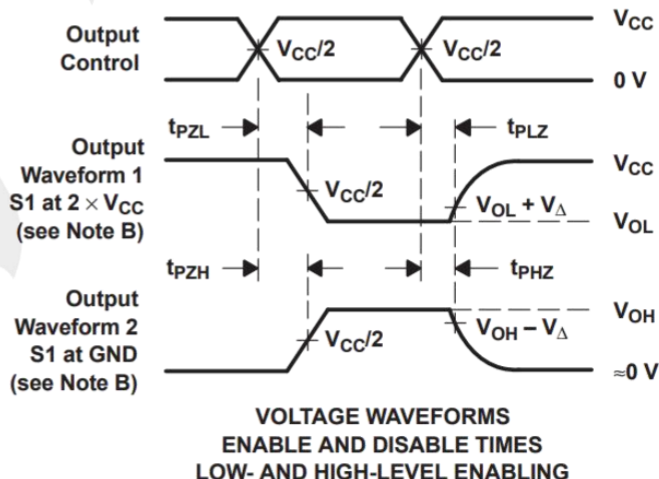
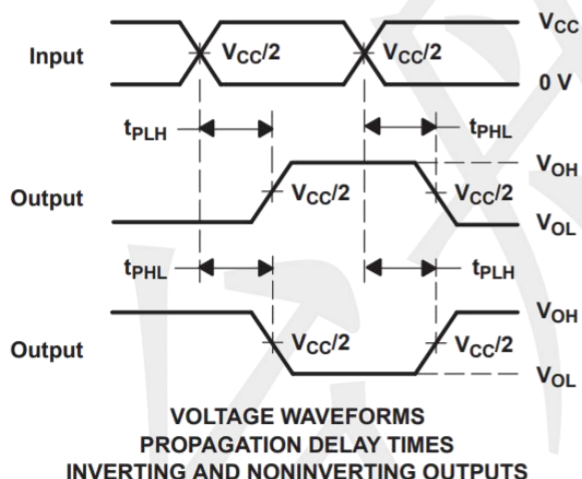
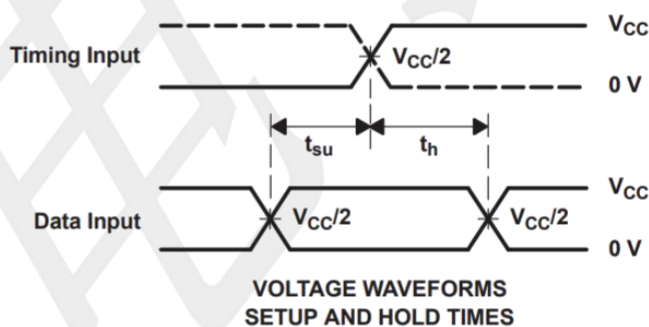
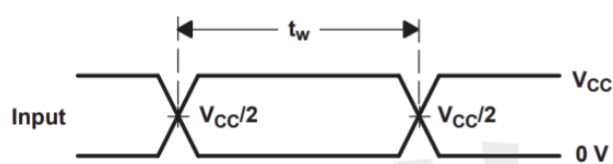
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation delay from inputs (A or B) to output(Y)	t _{PD}	CL=5pF, RL=1MΩ	V _{CC} =0.8V	--	19.3	ns
			V _{CC} =1.2±0.1V	2.6	7.3	ns
			V _{CC} =1.5±0.1V	1.4	5.2	ns
			V _{CC} =1.8±0.15V	1	4.2	ns
			V _{CC} =2.5±0.2V	1	3	ns
		CL=10pF, RL=1MΩ	V _{CC} =0.8V	--	22.3	ns
			V _{CC} =1.2±0.1V	1.5	8.5	ns
			V _{CC} =1.5±0.1V	1	6.2	ns
			V _{CC} =1.8±0.15V	1	5	ns
			V _{CC} =2.5±0.2V	1	3.6	ns
		CL=15pF, RL=1MΩ	V _{CC} =0.8V	--	25	ns
			V _{CC} =1.2±0.1V	3.6	9.9	ns
			V _{CC} =1.5±0.1V	2.3	7.2	ns
			V _{CC} =1.8±0.15V	1.6	5.8	ns
			V _{CC} =2.5±0.2V	1	4.3	ns
		CL=30pF, RL=1MΩ	V _{CC} =0.8V	--	34.6	ns
			V _{CC} =1.2±0.1V	4.9	13.1	ns
			V _{CC} =1.5±0.1V	3.4	9.5	ns
			V _{CC} =1.8±0.15V	2.5	7.7	ns
			V _{CC} =2.5±0.2V	1.8	5.7	ns

TEST CIRCUIT AND WAVEFORMS



TEST	S1
t_{PLH}/t_{PHL}	Open
t_{PLZ}/t_{PZL}	$2 \times V_{CC}$
t_{PHZ}/t_{PZH}	GND

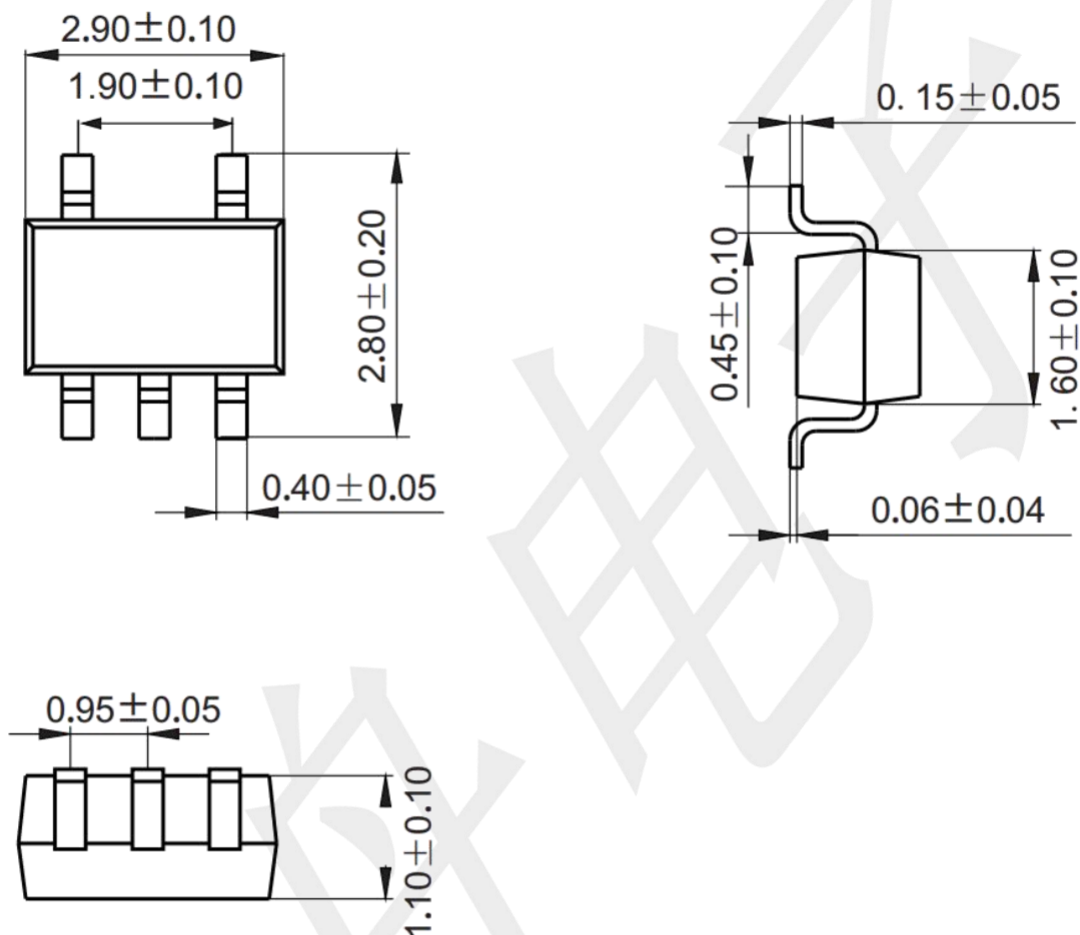
V_{CC}	C_L	R_L	V_{Δ}
0.8 V	15 pF	2 k Ω	0.1 V
1.2 V $\pm$ 0.1 V	15 pF	2 k Ω	0.1 V
1.5 V $\pm$ 0.1 V	15 pF	2 k Ω	0.1 V
1.8 V $\pm$ 0.15 V	15 pF	2 k Ω	0.15 V
2.5 V $\pm$ 0.2 V	15 pF	2 k Ω	0.15 V
1.8 V $\pm$ 0.15 V	30 pF	1 k Ω	0.15 V
2.5 V $\pm$ 0.2 V	30 pF	500 Ω	0.15 V



NOTES: A. C_L includes probe and jig capacitance. B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control. C. All input pulses are supplied by generators having the following characteristics: $PRR \leq 10$ MHz, $Z_O = 50 \Omega$, slew rate ≥ 1 V/ns. D. The outputs are measured one at a time, with one transition per measurement. E. t_{PLZ} and t_{PHZ} are the same as t_{dis} . F. t_{PZL} and t_{PZH} are the same as t_{en} . G. t_{PLH} and t_{PHL} are the same as t_{pd} . H. All parameters and waveforms are not applicable to all devices.

Package information (Unit: mm)

SOT23-5



Mounting Pad Layout (unit: mm)

