

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
900V	950mΩ@10V	8A



合肥矽普半导体

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Feature

- Fast Switching
- Low Gate Charge and Rdson
- **100% UIS TESTED**

Applications

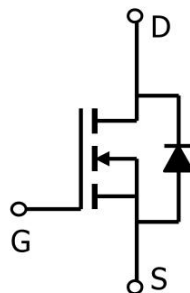
- DC-DC Converter
- Ideal for high-frequency switching and synchronous rectification

Package

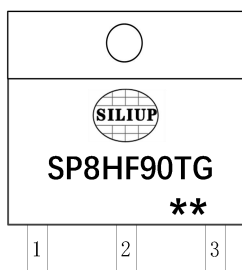


TO-220F(1:G 2:D 3:S)

Circuit diagram



Marking



SP8HF90TG :Product code
****** :Week code

Order Information

Device	Package	Unit/Tube
SP8HF90TG	TO-220F	50

Absolute maximum ratings (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	900	V
Gate-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current (Tc=25°C)	I_D	8	A
Continuous Drain Current (Tc=100°C)	I_D	5.3	A
Pulsed Drain Current	I_{DM}	32	A
Single Pulse Avalanche Energy ¹	EAS	75	mJ
Power Dissipation (Tc=25°C)	P_D	34	W
Power Dissipation (Tc=100°C)	P_D	13.6	W
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	3.7	°C/W
Recovery diode dv/dt	dv/dt	50	V/ns
Storage Temperature Range	T_{STG}	-55 to 150	°C
Operating Junction Temperature Range	T_J	-55 to 150	°C

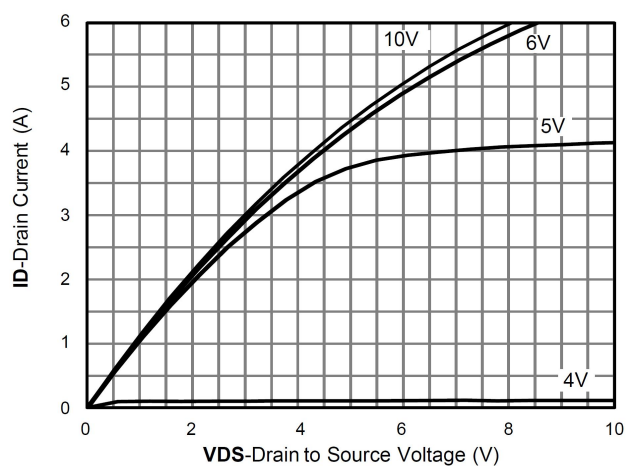
Electrical characteristics (Ta=25°C, unless otherwise noted)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	ID=250μA , VGS=0V	900	-	-	V
Drain Cut-Off Current	IDSS	VDS=720V , VGS=0V	-	-	1	μA
Gate Leakage Current	IGSS	VGS=±30V , VDS=0V	-	-	±100	nA
Gate Threshold Voltage	VGS(th)	VDS=VGS , ID=250μA	2.0	3.0	4.0	V
Drain-Source ON Resistance	RDS(ON)	VGS=10V , ID=4A	-	950	1200	mΩ
Gate Resistance	RG	VDS=100V , VGS=0V , f=1MHz	-	3.9	-	Ω
Dynamic Characteristics						
Input Capacitance	Ciss	VDS=100V , VGS=0V , f=1MHz	-	703	-	pF
Output Capacitance	Coss		-	26	-	
Reverse Transfer Capacitance	Crss		-	1.4	-	
Total Gate Charge	Qg	VDS=400V , VGS=10V , ID=5A	-	16	-	nC
Gate-Source Charge	Qgs		-	3.6	-	
Gate-Drain Charge	Qgd		-	7.1	-	
Gate Plateau Voltage	Vplateau		-	5	-	V
Switching Characteristics						
Turn-On Delay Time	td(on)	VDD=400V , VGS=10V , ID=5A , RG=10Ω	-	38	-	nS
Rise Time	tr		-	52	-	
Turn-Off Delay Time	td(off)		-	28	-	
Fall Time	tr		-	10	-	
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	VSD	IS=1A , VGS=0V , TJ=25℃	-	-	1.2	V
Maximum Body-Diode Continuous Current	IS		-	-	8	A
Body Diode Reverse Recovery Time	Trr	IS=5A , di/dt=100A/us , TJ=25℃	-	194	-	nS
Body Diode Reverse Recovery Charge	Qrr		-	2.5	-	uC

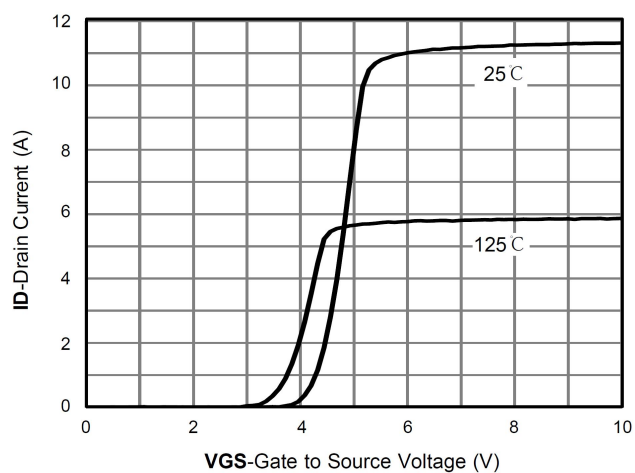
Note :

1. The test condition is $V_{DD}=50V$, $V_{GS}=10V$, $L=10mH$, $R_G=30\Omega$;

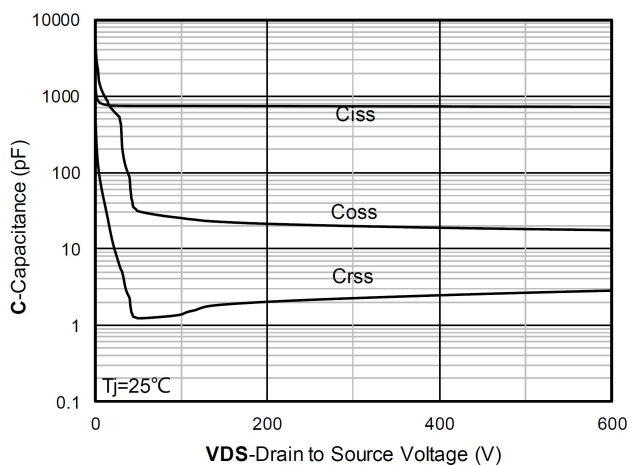
Typical Characteristics



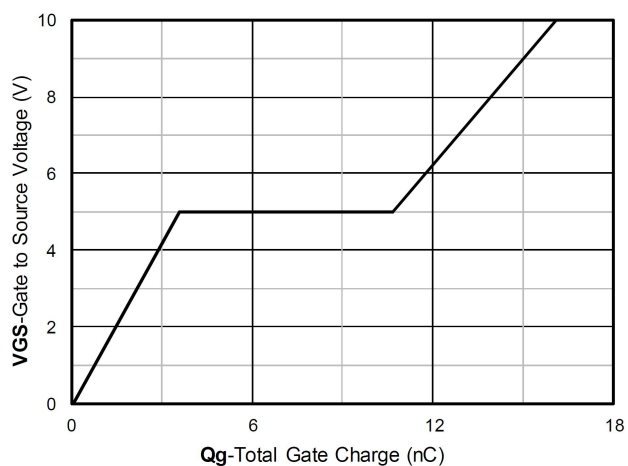
Output Characteristics



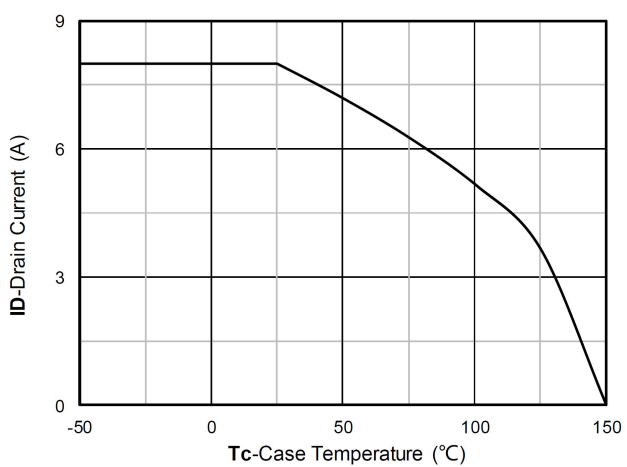
Transfer Characteristics



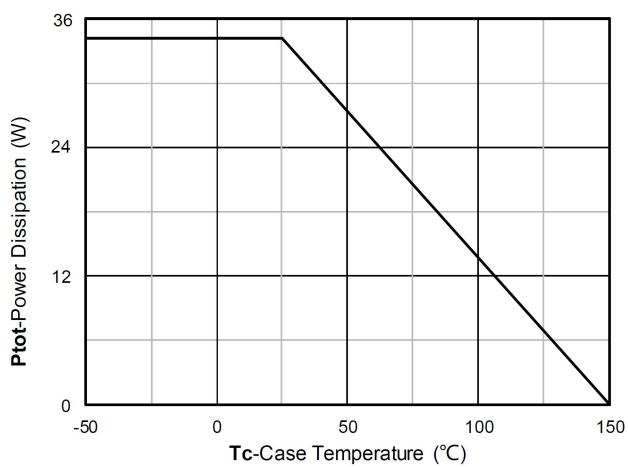
Capacitance Characteristics



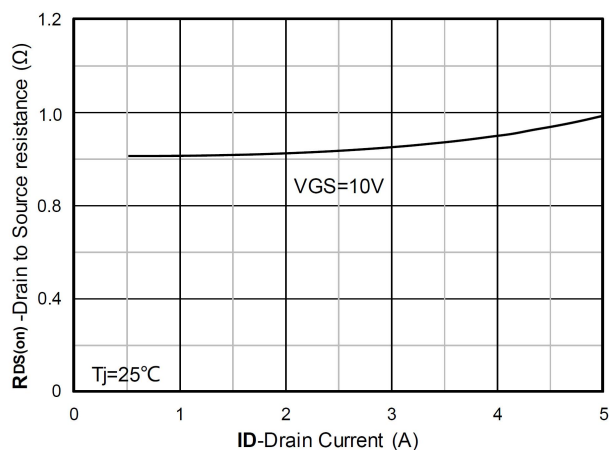
Gate Charge



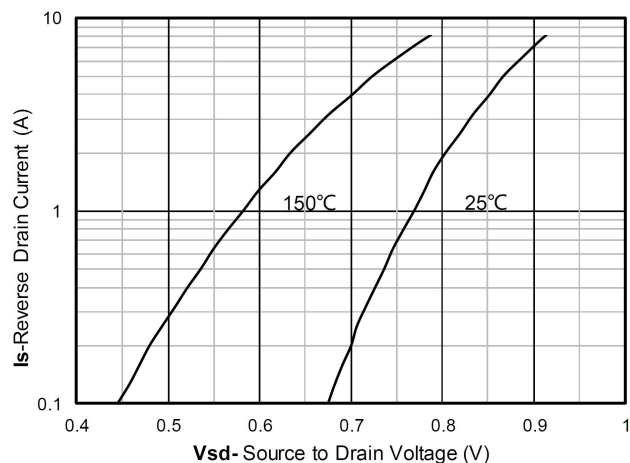
Current dissipation



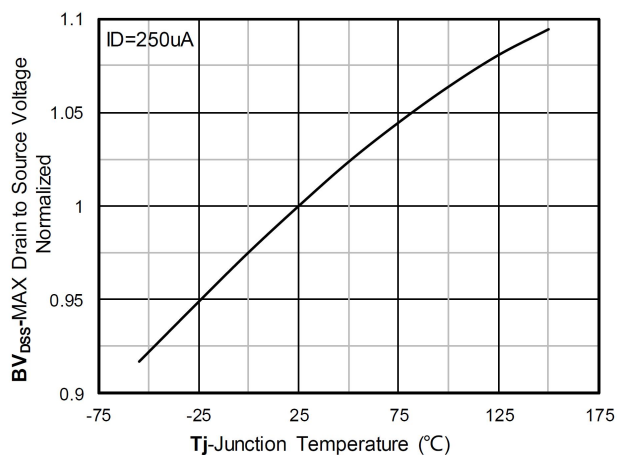
Power dissipation



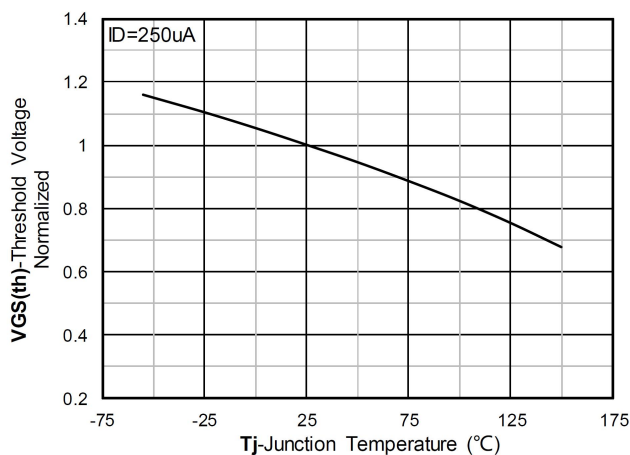
RDS(on) VS Drain Current



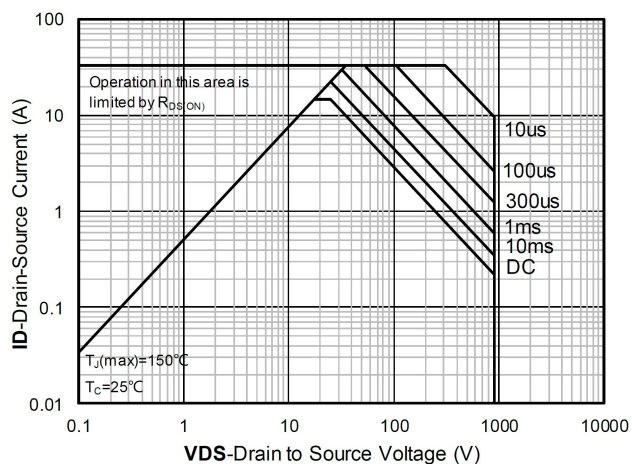
Forward characteristics of reverse diode



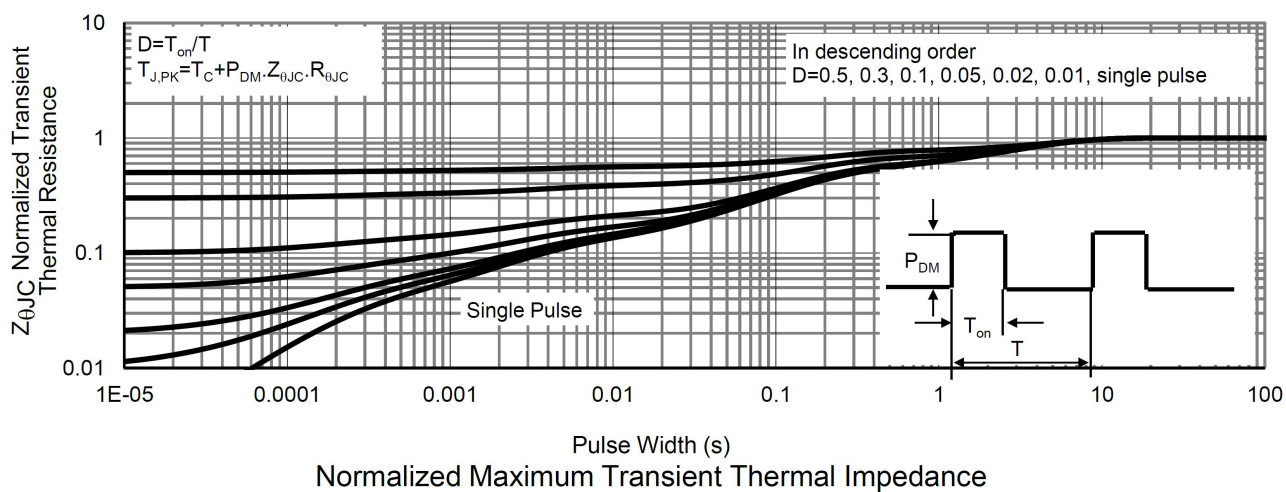
Normalized breakdown voltage



Normalized Threshold voltage

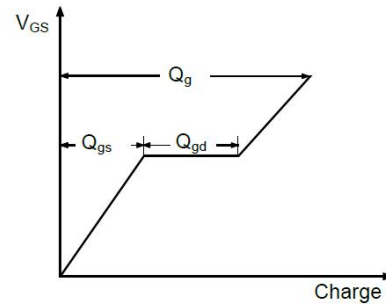
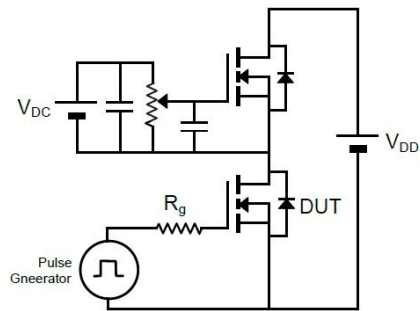


Safe Operation Area

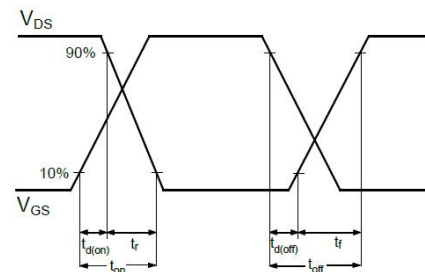
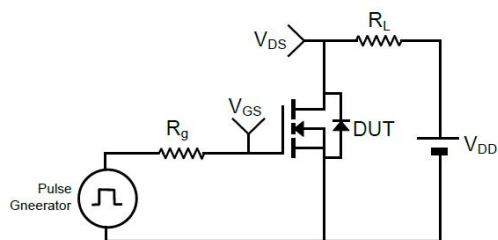


Test Circuit

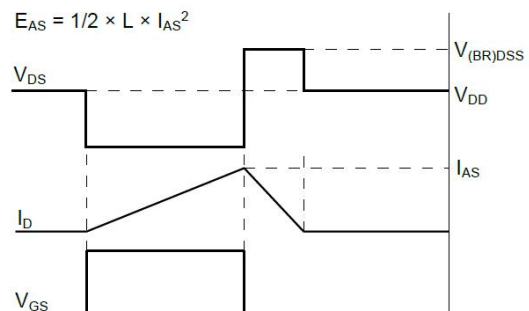
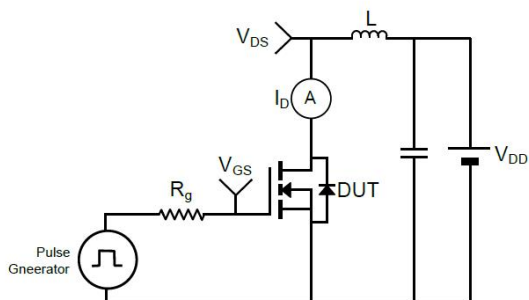
Gate Charge



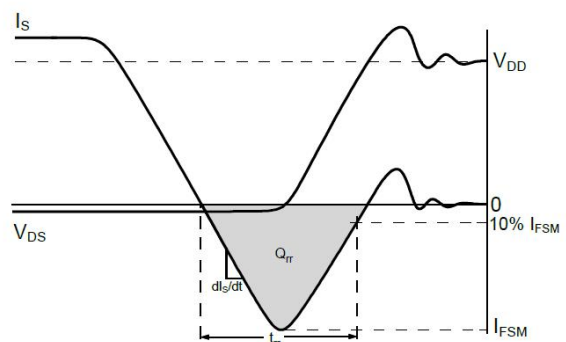
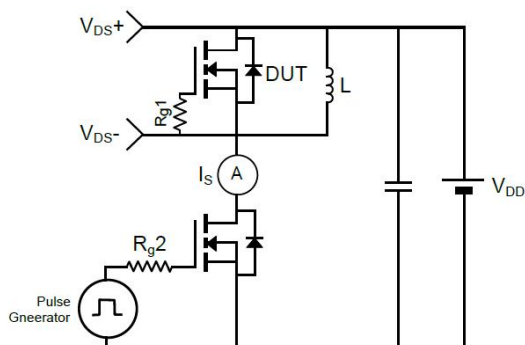
Resistive Load Switching Time

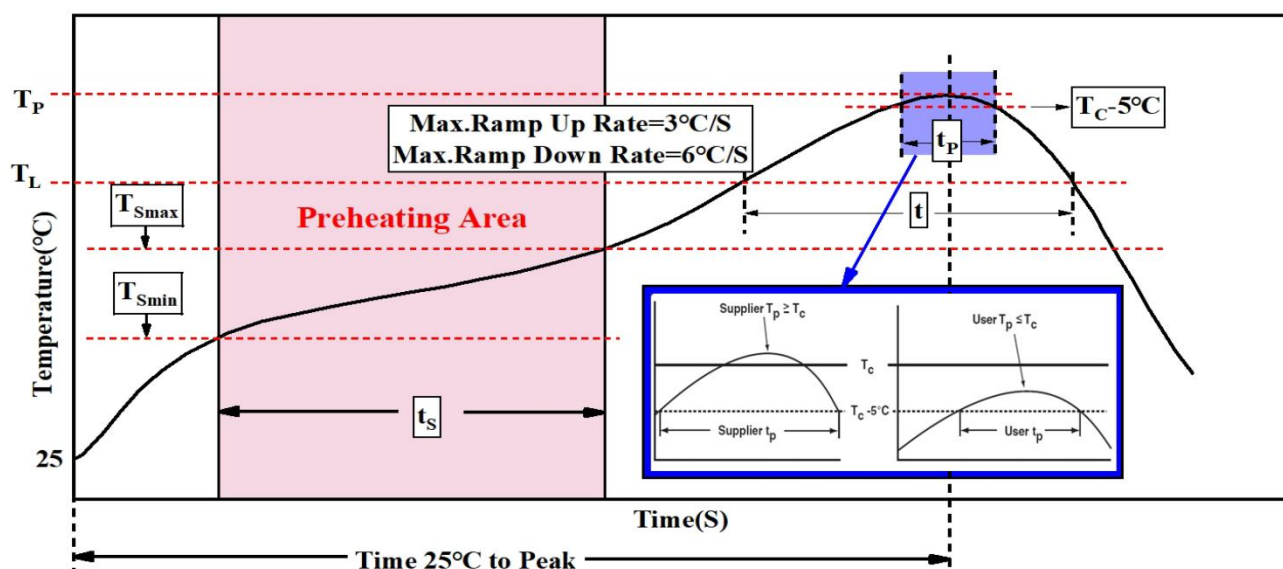


Un-clamped Inductive Load Switching



Drain-Source Body Diode Reverse Recovery



Temperature Profile for IR Reflow Soldering


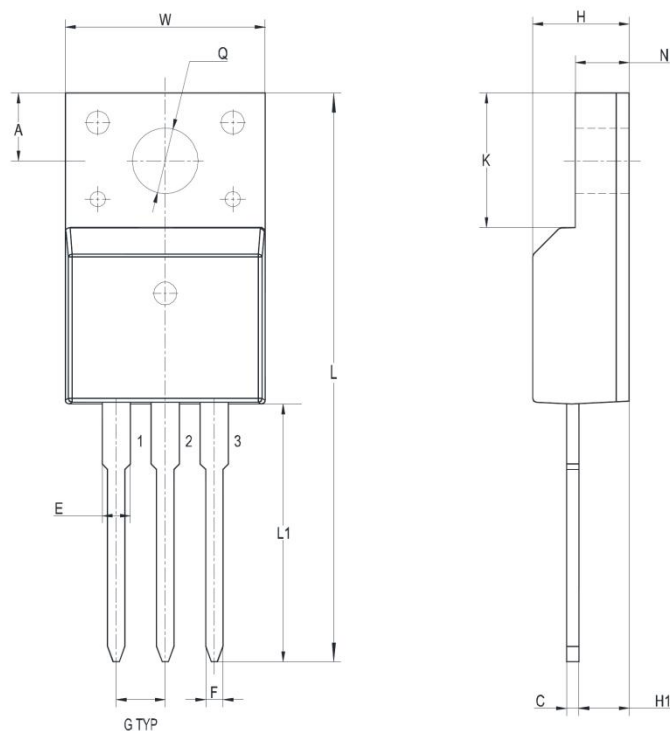
Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{Smin})	100°C	150°C
Temperature max (T_{Smax})	150°C	200°C
Time (T_{Smin} to T_{Smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{Smax} to T_P)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217°C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature e (T_P)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_P)** within 5°C of the specified classification temperature (T_C)	20** seconds	30** seconds
Average ramp-down rate (T_P to T_{Smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_P) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_P) is defined as a supplier minimum and a user maximum		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_C)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_C)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

TO-220F Package Information


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	2.80	3.50
C	0.40	0.60
E	1.15	1.45
F	0.70	0.90
G	2.54 TYP.	
W	9.50	10.50
H	4.50	4.90
H1	2.50	2.95
Q	2.90	3.55
L	28.00	29.50
L1	12.50	13.50
K	6.20	6.77
N	2.30	2.80