

N-Channel MOSFET

RCD35N10

■ Description

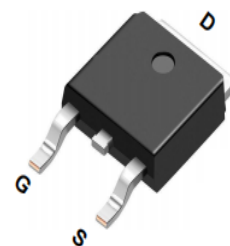
The RCD35N10 is an N-channel power MOSFET adopting advanced high cell density Trench technology, rated at 100V with ultra-low on-resistance and high drain current capability. It delivers excellent gate charge performance and Cdv/dt effect decline, with optimized switching and thermal characteristics for synchronous buck converter applications, complying with RoHS and Green Product requirements, and 100% EAS guaranteed with full function reliability approved.

■ Features

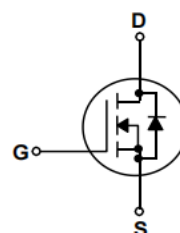
- Advanced high cell density Trench MOSFET technology
- Super low gate charge for high switching efficiency
- Excellent Cdv/dt effect decline
- Ultra-low RDS(ON) with high current handling capability
- 100% EAS guaranteed, full function reliability approved
- RoHS & Green Product compliant

■ Application

- Synchronous buck converters
- DC-DC power conversion
- High-efficiency power management systems



TO-252 top view



Schematic diagram



Marking and pin assignment

■ Absolute Maximum Ratings

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		100	V
V_{GSS}	Gate-Source Voltage		± 20	V
I_D	Continuous Drain Current	$T_C = 25^\circ\text{C}$	40	A
		$T_C = 100^\circ\text{C}$	21	A
I_{DM}	Pulsed Drain Current ^{note1}		120	A
EAS	Single Pulsed Avalanche Energy ^{note2}		30	mJ
P_D	Power Dissipation	$T_C = 25^\circ\text{C}$	42	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		3.6	$^\circ\text{C/W}$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +175	$^\circ\text{C}$

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■ Electrical Characteristics(T_J=25℃ unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =100V, V _{GS} =0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	1.5	2.2	V
R _{DS(on)}	Static Drain-Source on-Resistance note3	V _{GS} =10V, I _D =10A	-	25	32.5	mΩ
		V _{GS} =4.5V, I _D =6A	-	26	36	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	1964	-	pF
C _{oss}	Output Capacitance		-	90	-	pF
C _{rss}	Reverse Transfer Capacitance		-	74	-	pF
Q _g	Total Gate Charge	V _{DS} =80V, I _D =20A, V _{GS} =4.5V	-	20	-	nC
Q _{gs}	Gate-Source Charge		-	3.1	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	14	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} =80V, I _D =20A, R _G =3.1Ω, V _{GS} =4.5V	-	11	-	ns
t _r	Turn-on Rise Time		-	91	-	ns
t _{d(off)}	Turn-off Delay Time		-	40	-	ns
t _f	Turn-off Fall Time		-	71	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	40	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	120	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =20A	-	-	1.2	V
t _{rr}	Body Diode Reverse Recovery Time	I _F =20A, dI/dt=100A/μs	-	64	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge		-	152	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : T_J=25℃, V_{DD}=50V, V_G=10V, L=0.5mH, R_G=25Ω, I_{AS}= 11A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

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Typical Performance Characteristics

Figure 1: Output Characteristics

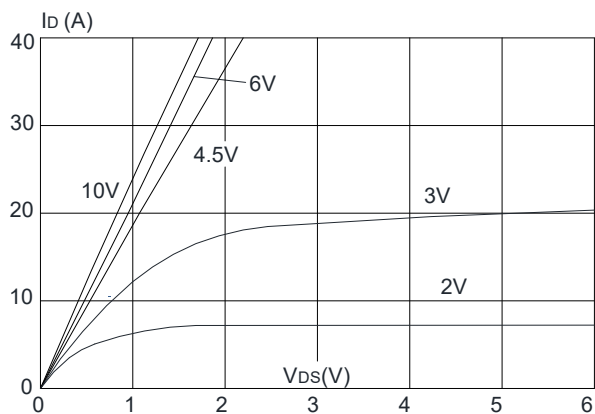


Figure 2: Typical Transfer Characteristics

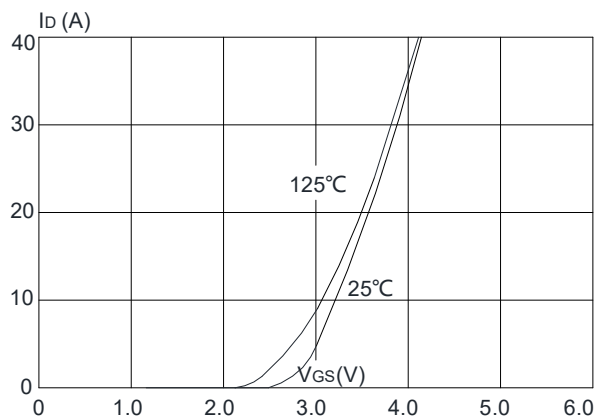


Figure 3: On-resistance vs. Drain Current

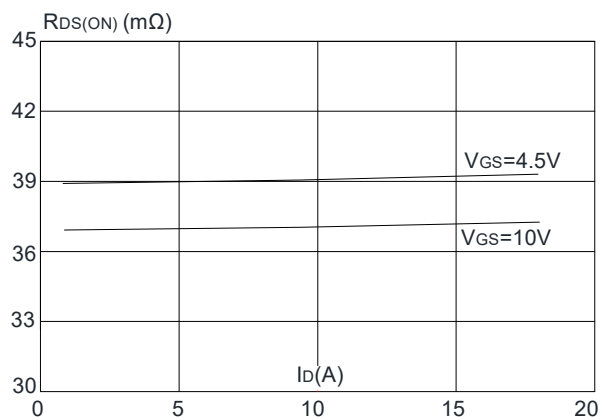


Figure 4: Body Diode Characteristics

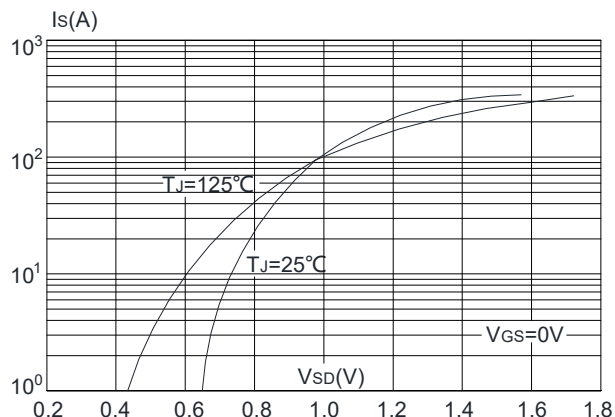


Figure 5: Gate Charge Characteristics

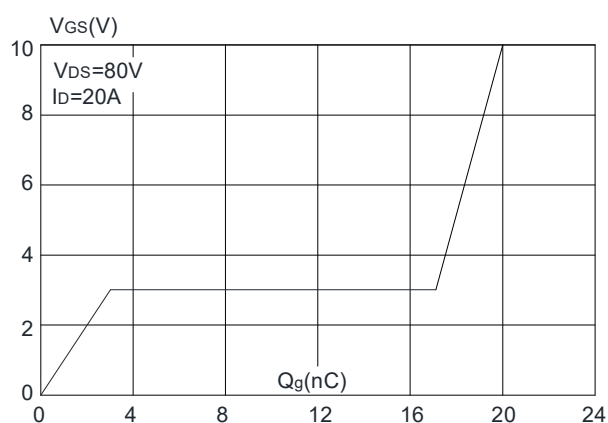
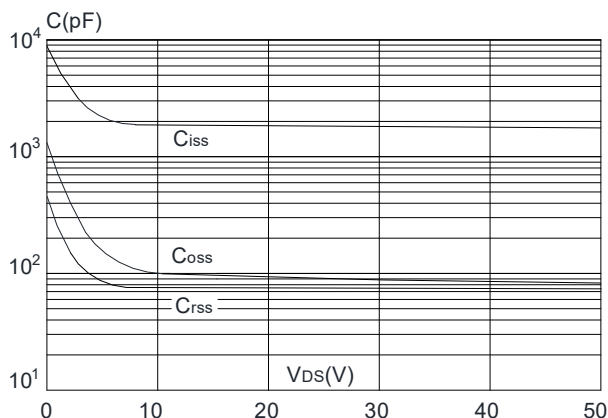


Figure 6: Capacitance Characteristics



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Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

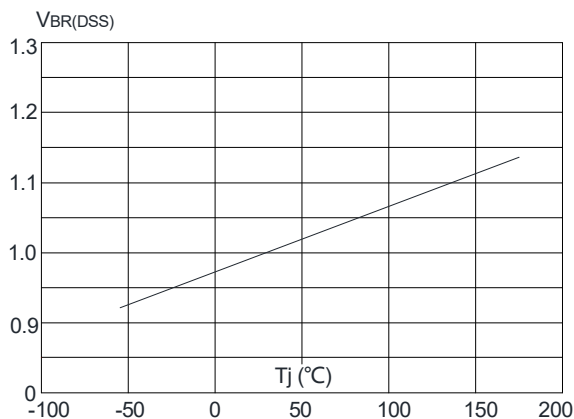


Figure 8: Normalized on Resistance vs. Junction Temperature

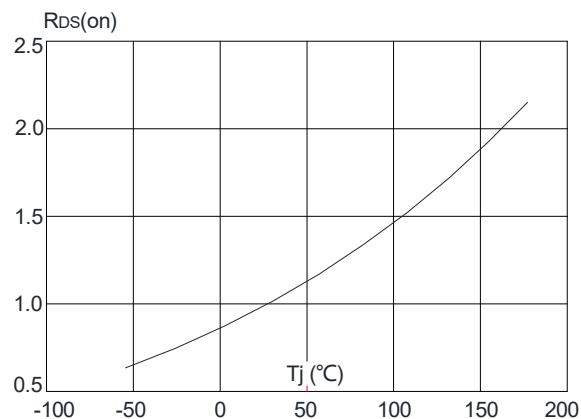


Figure 9: Maximum Safe Operating Area

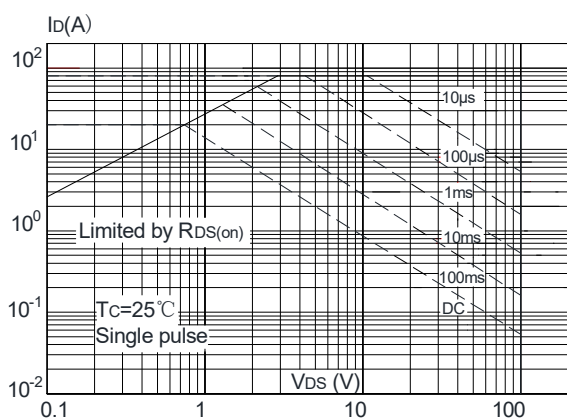


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

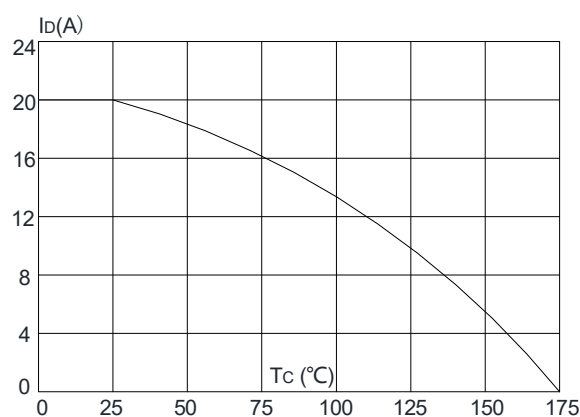
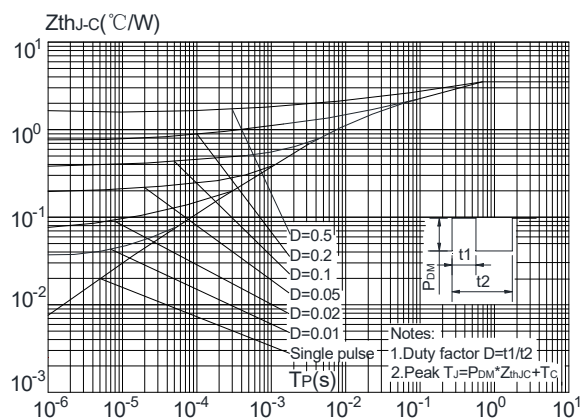


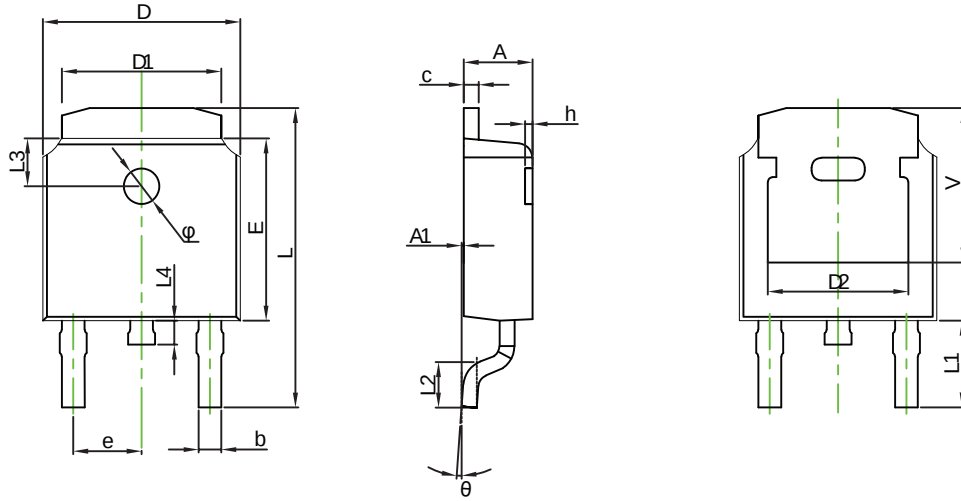
Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



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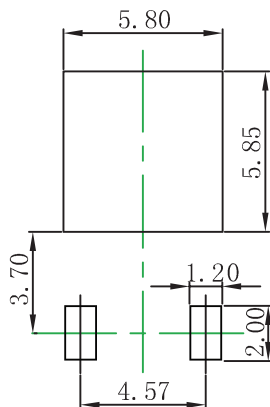
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TO-252 Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 REF.		0.190 REF.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900 REF.		0.114 REF.	
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063 REF.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250 REF.		0.207 REF.	

TO-252 Suggested Pad Layout



Note:
1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.