BORN SEMICONDUCTOR

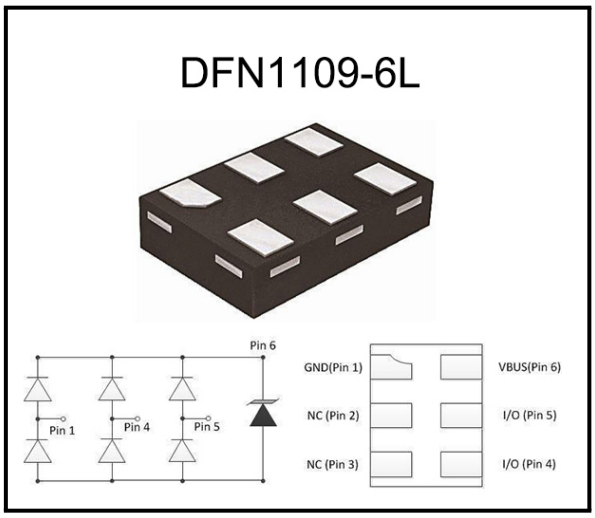
Rclamp0502N-N

ESD Protection Diode Array

Features

- 70Watts peak pulse power (tp = 8/20μs)
- Protect up to 2-lines
- Solid-state silicon-avalanche technology
- Low clamping Voltage
- Low leakage current
- Low capacitance (C_j=0.28pF typ.I/O to I/O)
- Protection one data/power line
- IEC 61000-4-2 ±15kV contact ; ±15kV air
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 3.5A (8/20μs)

Package



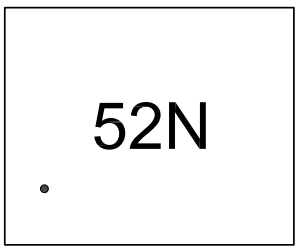
Applications

- USB 2.0
- MMC Port
- Video Port
- Digital visual interface

Mechanical Data

- DFN1109-6L package
- Molding compound flammability rating: UL 94V-0
- Packaging: Tape and Reel
- RoHS/WEEE Compliant

Marking



Ordering information

Order code	Package	Base qty	Delivery mode
Rclamp0502N-N	DFN1109-6L	3k	Tape and reel

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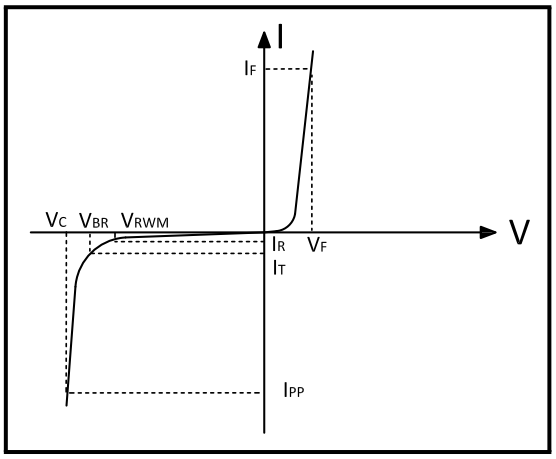
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Rclamp0502N-N

ESD Protection Diode Array

Electrical Parameters (T_A = 25°C unless otherwise noted)

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current



Note:8/20us pulse Waveform.

Absolute Maximum Rating

Rating	Symber	Value	Units
Peak Pulse Power (tp =8/20μs)	P_{PP}	70	Watts
Peak Pulse Current (tp =8/20μs)	I_{PP}	3.5	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	15 15	KV
Lead Soldering Temperature	T_L	260(10seconds)	°C
Junction Temperature	T_J	-55 to + 150	°C
Storage Temperature	T_{stg}	-55 to + 150	°C

Electrical Characteristics

Parameter	Symber	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	V_{RWM}	–	–	–	5.0	V
Reverse Breakdown Voltage	V_{BR}	I_T=1mA	6.0	7.5	9.5	V
Reverse Leakage Current	I_R	V_{RWM}=5V,T=25°C	–	–	0.5	uA
Peak Pulse Current	I_{PP}	tp =8/20us	–	–	3.5	A
Clamping Voltage	V_C	I_{PP}=3.5A, tp =8/20us	–	16	20	V
Junction Capacitance	C_j	V_R=0V,f=1MHZ,I/O to I/O	–	0.28	0.4	pF
		V_R=0V,f=1MHZ,I/O to GND	–	0.28	0.4	

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Rclamp0502N-N

ESD Protection Diode Array

Typical Characteristics

Figure 1: Peak Pulse Power vs. Pulse Time

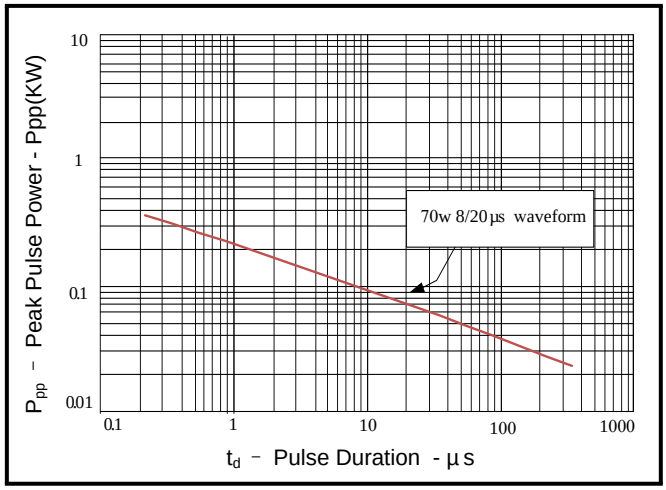


Figure 2: Power Derating Curve

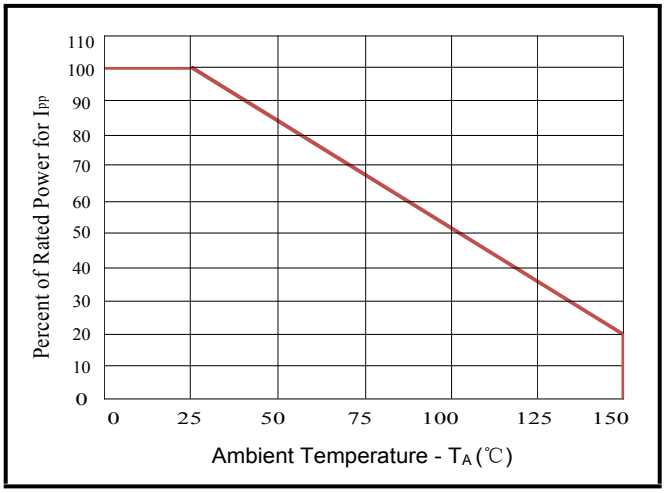


Figure 3: Pulse Waveform

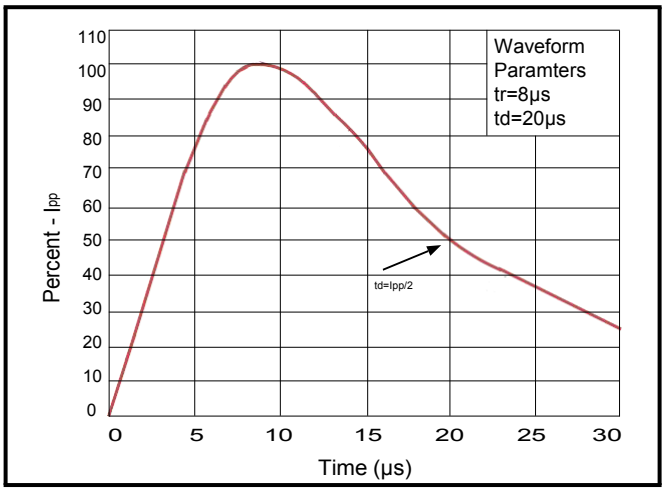
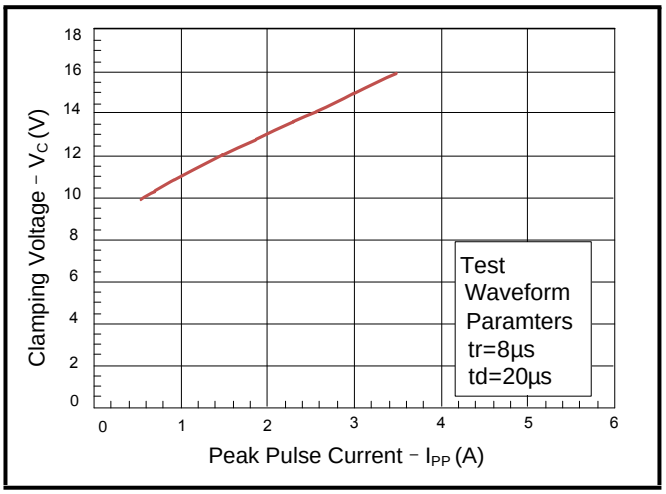


Figure 4: Clamping Voltage vs. I_{PP}

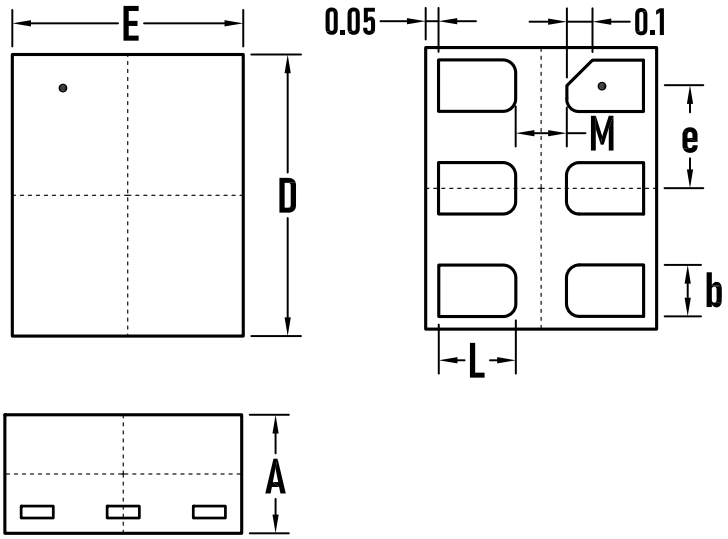


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Rclamp0502N-N

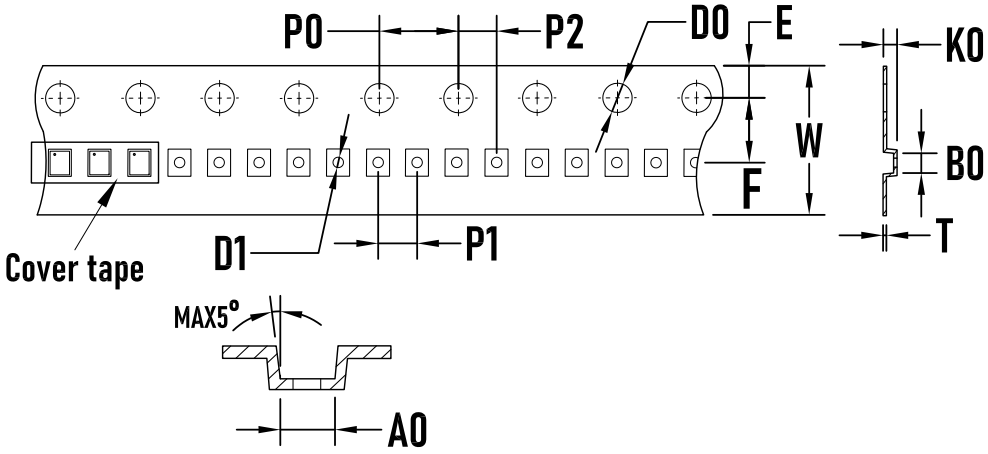
ESD Protection Diode Array

Outline Drawing – DFN1109-6L



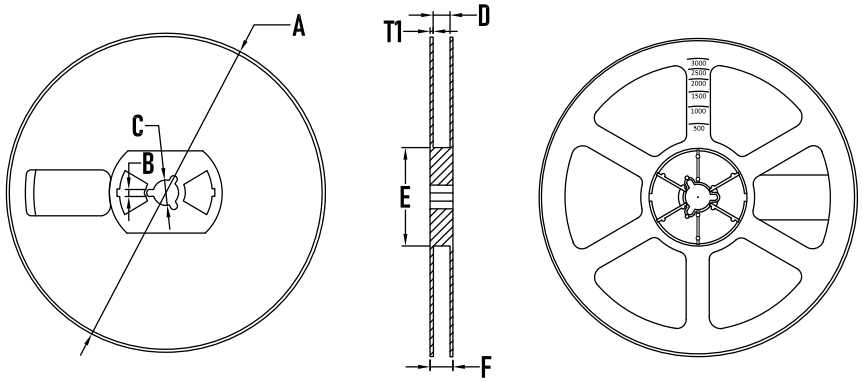
SYMBOL	MILLIMETER		
	MIN.	Typ.	MAX.
A	0.40	0.45	0.50
E	0.85	0.90	0.95
D	1.05	1.10	1.15
b	0.15	0.20	0.25
L	0.25	0.30	0.35
e	0.35	0.40	0.45
M	0.15	0.20	0.25

Packaging Tape - DFN1109-6L



SYMBOL	MILLIMETER
A0	1.03±0.05
B0	1.25±0.05
K0	0.60±0.05
P0	4.00±0.10
P1	2.00±0.05
P2	2.00±0.05
T	0.20±0.03
E	1.75±0.10
F	3.50±0.05
D0	1.55±0.05
D1	0.50±0.05
W	$8.00^{+0.03}_{-0.01}$

Packaging Reel



SYMBOL	MILLIMETER
A	178±1
B	3.5±0.2
C	14.3±0.2
D	9.8^{+2}_{-1}
E	54.5±0.5
F	12.4±0.5
T1	1.0±0.2
Quantity	3000PCS

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