GSR2303S

2.4 GHz Front End Module

Product Description

The GSR2303S is a highly integrated front end module (FEM) designed for 2.4GHz 802.11b/g/n/ac/ax systems. The device provides all the functionality of a fully matched power amplifier (PA), power detector, a 2.4G low-noise amplifier (LNA), and a single-pole, double-throw (SPDT) switches.

The GSR2303S provides a complete 2.4 GHz WLAN RF solution from the output of the transceiver to the antenna, and from the antenna to the input of the transceiver. The LNA increases the receive sensitivity of embedded solutions to improve range or to overcome the insertion loss of cellular filters (often included for mobile applications).

The GSR2303S also includes a transmitter power detector with 20 dB dynamic range, and a digital enable control for transmitter power ramp on/off control.

The device is provided in a compact, 16-pin2.5 x2.5 mm Quad Flat No-Lead (QFN) package. Pin map is shown in Figure 1. A functional block diagram is shown in Figure 2.

Pin Map

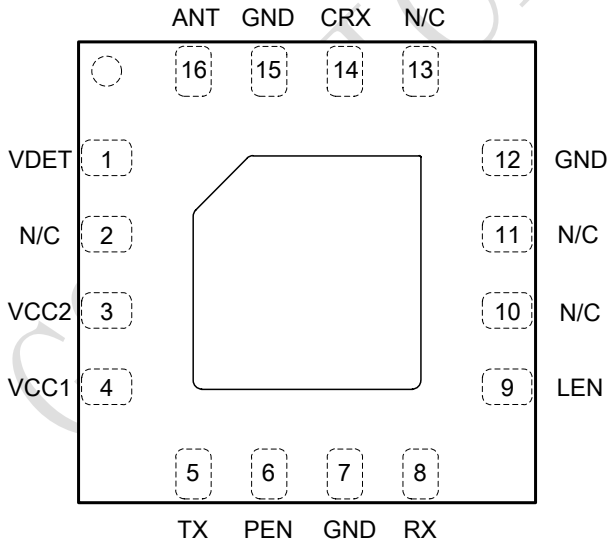


Figure 1 GSR2303S Pin Map (Top View)

Functional Block Diagram

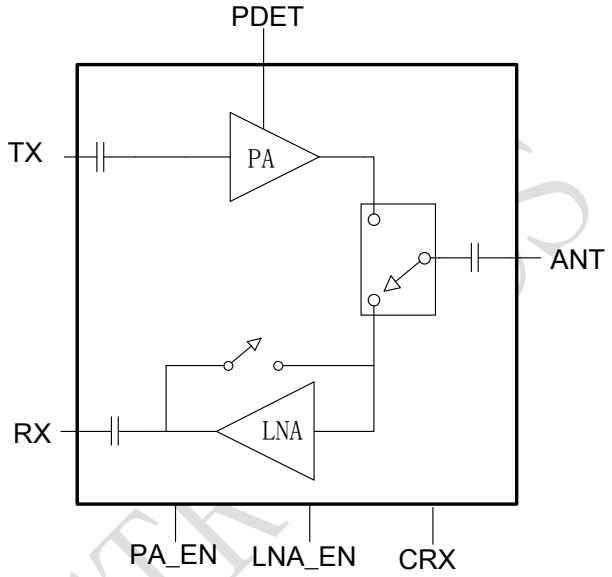


Figure 2 Block Diagram

Key Features

- Pout = +21 dBm HE40 MCS11 -43dB DEVM
- Pout = +22 dBm HE40 MCS11 -40dB DEVM
- Pout = +23 dBm VTH40 MCS9 -35dB DEVM
- Pout = +24 dBm HT40/20 MCS7 -30dB DEVM
- Integrated positive slope power detector
- Receive gain: 15 dB
- 1.6 dB LNA Noise Figure

Applications

- Mobile Access Points
- Mobile Phones
- Consumer Electronics
- Customer Premise Equipment
- Internet of Things

Products and Product information are Subject to Change Without Notice
GSR Microelectronics Proprietary Information

Pin Description

Pin	Name	Description
1	VDET	Power detector output voltage
2	N/C	No connection
3	VCC2	PA supply voltage
4	VCC1	PA and LNA supply voltage
5	TX	RF transmit input
6	PEN	PA enable
7	GND	Ground
8	RX	RF receive output
9	LEN	LNA enable
10	N/C	No connection
11	N/C	No connection
12	GND	Ground
13	N/C	No connection
14	CRX	Switch control voltage
15	GND	Ground
16	ANT	Antenna

Absolute Maximum Ratings

Parameter	Conditions	Rating
DC Supply Voltage		-0.3 to +5.25 V
Control Voltage (PEN,LN,CRX)		-0.3 to +3.6 V
Storage Temperature		-40 to 150 ℃
Junction Temperature		Max 150 ℃
RF Input Power at TX	ANT connect 50 Ω Load	+10 dBm
RF Input Power at ANT	RX connect 50 Ω Load, LNA active	+10 dBm
RF Input Power at ANT	RX connect 50 Ω Load, LNA off	+30 dBm

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability.

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Recommended Operating Conditions

Parameter	Min	Typ	Max	Units
Operating Frequency	2400	-	2500	MHz
DC Supply Voltage	3	5	5.25	V
Control Voltage – High	1.8	-	3.6	V
Control Voltage – Low	0	-	0.4	V
Operating temperature	-40	25	85	℃

Logic Truth Table

Mode	PEN	LEN	CRX
All off	0	0	0
WLAN receive LNA	0	1	1
WLAN receive bypass	0	0	1
WLAN transmit	1	0	0

Electrical Specifications

Parameter	Conditions	Min	Typ	Max	Units
Transmit Mode	Unless otherwise noted: VCC1, VCC2 =5 V, Temp=+25ºC, PEN =3.3 V, LEN=CRX=0 V				
11ax Output Power	HE40 MCS11 1024QAM		21		dBm
Dynamic EVM			-43		dBm
11ax Output Power			22		dB
Dynamic EVM			-40		dB
11ac Output Power	VHT40 MCS9 256QAM		23		dBm
Dynamic EVM			-35		dB
11n HT20 Output Power	MCS7 64QAM		24		dBm
Dynamic EVM			-30		dB
mask compliant power	Ht20 mcs0		27		dBm
Gain			28		dB
Gain Flatness	Across any 40 MHz Channel	-0.5		+0.5	dB
TX Port Return Loss			12		dB
ANT Port Return Loss			13		dB
Quiescent Current	RF OFF		160		mA
Operating Current	Pout=21 dBm, HE40 MCS11		240		mA
	Pout=24 dBm, HT20 MCS7		290		mA
Operating Current	Pout=27 dBm, HT20 MCS0		390		mA
PA_EN Current	PA_EN =High		5		uA

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2nd Harmonics	Pout=25 dBm, 11b 1Mbps		-20		dBm/MHz
3rd Harmonics	Pout=25 dBm, 11b 1Mbps		-30		dBm/MHz
VDET Voltage	RF OFF		0.19		V
	Pout=18 dBm, CW		0.69		V
	Pout=24 dBm, CW		0.88		V
Switching Time	LNA On or Bypass to Transmit Mode		400		nS
Roughness	Rated power = 25 dBm, load VSWR 10:1	No damage			
Receive LNA Mode	Unless otherwise noted: VCC1, VCC2 =5 V, Temp=+25ºC, PEN=0 V, LEN= CRX=3.3 V				
Noise Figure			1.6		dB
Gain			15		dB
Gain Flatness	Across any 40 MHz Channel	-0.5		+0.5	dB
Out of Band Gain	f = 5000 ~ 6000 MHz		3		dB
RX Port Return Loss			8		dB
ANT Port Return Loss			10		dB
Quiescent Current			21		mA
LNA_EN Current	LNA_EN =High		8		uA
Input P1dB			-4		dBm
Switching Time	Transmit to Receive LNA Mode		400		nS
	Bypass to LNA Mode		400		nS
Receive Bypass Mode	Unless otherwise noted: VCC1, VCC2 =5 V, Temp=+25ºC, PEN=LEN=0,CRX=3.3V				
Gain			-2.5		dB
Gain Flatness	Across any 40 MHz Channel	-0.25		+0.25	dB
Out of Band Gain	f = 5000 ~ 6000 MHz		-2.5		dB
RX Port Return Loss			12		dB
ANT Port Return Loss			12		dB
Quiescent Current	RF OFF		150		uA
Input P1dB			25		dBm
Switching Time	Transmit to Receive Bypass Mode		400		nS
	LNA to Bypass Mode		400		nS

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Evaluation Board Schematic

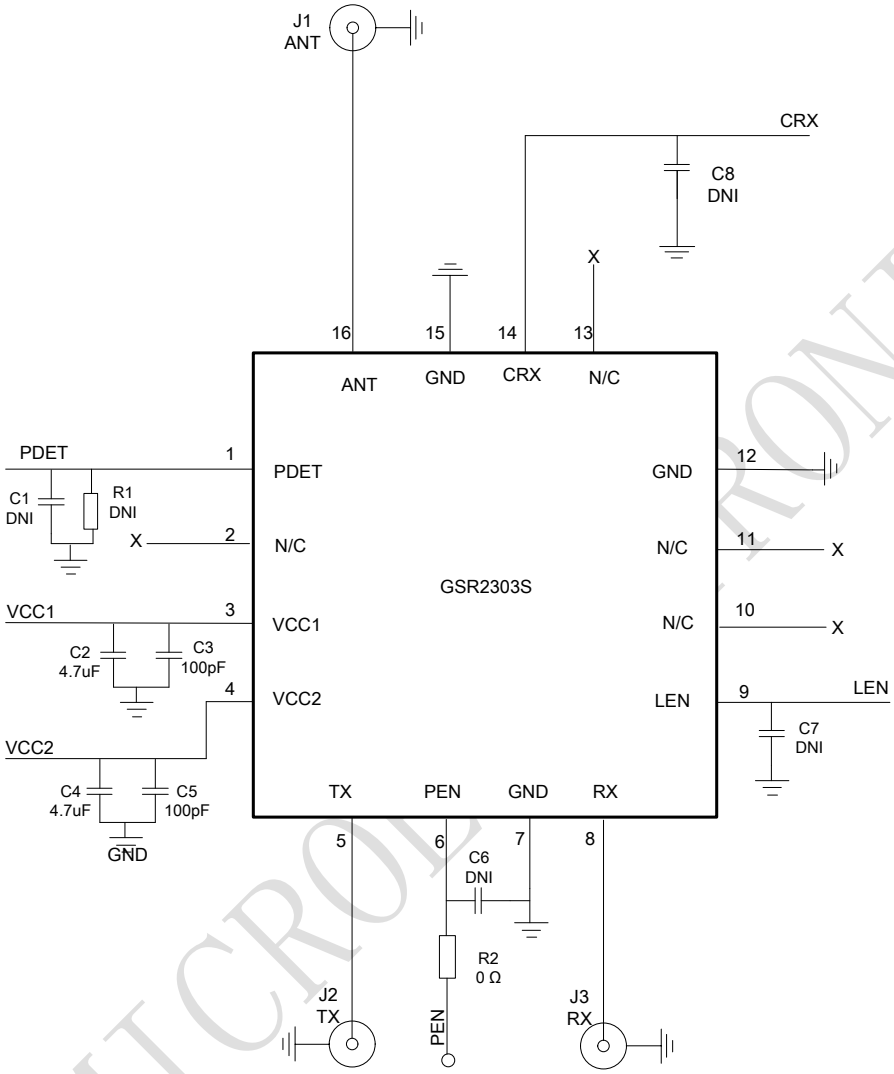
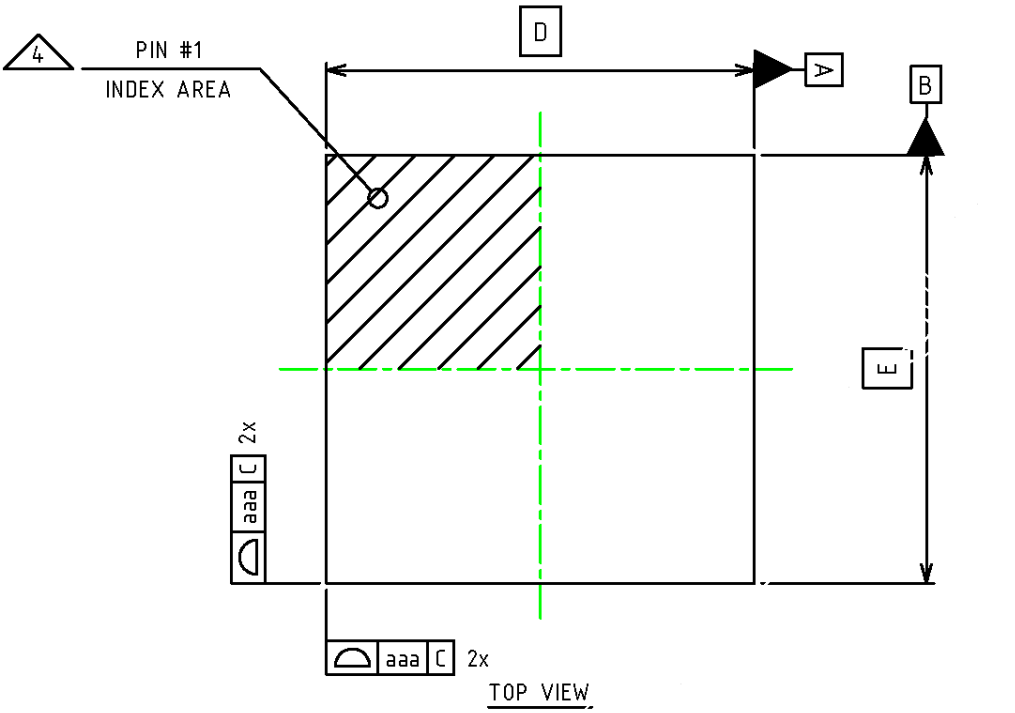
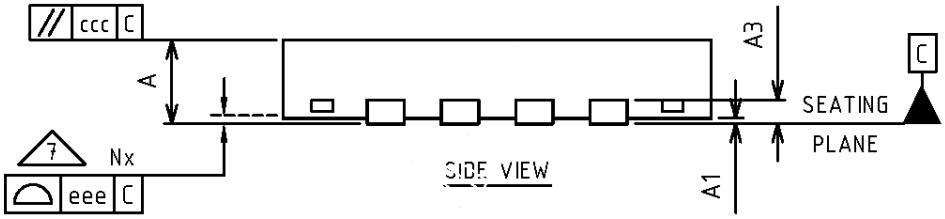


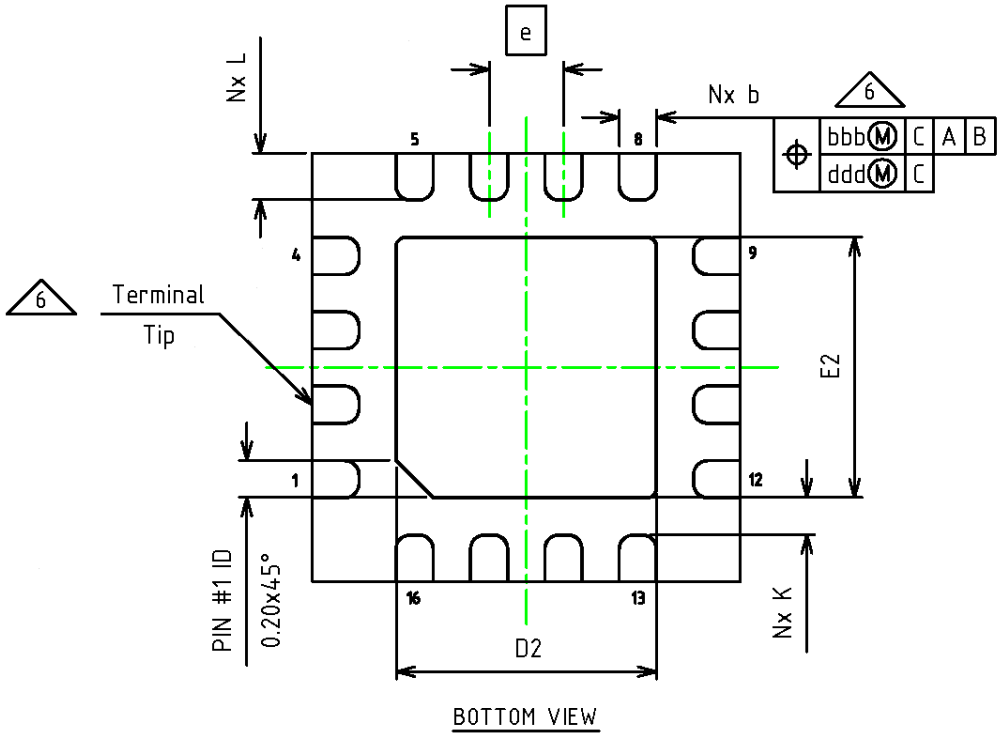
Figure 3 GSR2303S Application Schematic

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Package Outline







Dimension Table				
	MINIMUM	NOMINAL	MAXIMUM	NOTE
A	0.45	0.5	0.55	
b	0.15	0.20	0.25	
D		2.5		
E		2.5		
e		0.5		
D2	1．3	1.35	1.4	
E2	1.3	1.35	1.4	
K	0.2			
L	0.125	0.225	0.325	
N		16		

Notes: All dimensions are in millimeters.

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Contact Information

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