TPESD3V3L4UW

Low Capacitance TVS Diode Array

WWW.TECHPUBLIC.COM

Features

- Low operating voltage: 3.3V
- Ultra low capacitance: 0.3pF (I/O to I/O)
- Ultra low leakage: nA level
- Low clamping voltage
- -IEC 61000-4-2 (ESD) immunity test

Air discharge: ±20kV

Contact discharge: ±15kV

-IEC61000-4-4 (EFT) 40A (5/50ns)

-IEC61000-4-5(Lightning):4.0A(8/20μs)

- These are Pb−Free Devices
- Response Time is Typically < 1 ns

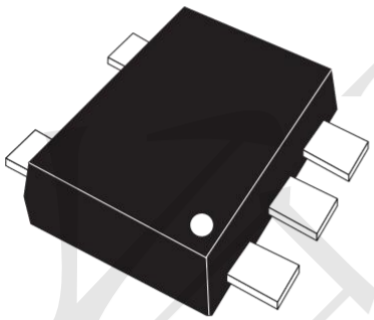
Mechanical Characteristics

- Package: SOT-553
- Lead Finish: Matte Tin
- Case Material: “Green” Molding Compound
- Terminal Connections: See Diagram Below
- - IEC 61000-4-2 (ESD) immunity test

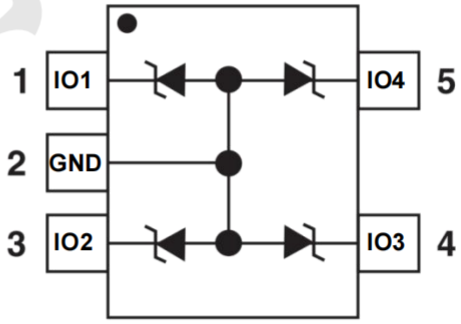
Applications

- USB OTG
- Consumer products
- Digital video interface(DVI)
- PCI Express and Serial SATA Ports

Dimensions SOT-553



Dimensions and Pin Configuration



Circuit and Pin Schematic

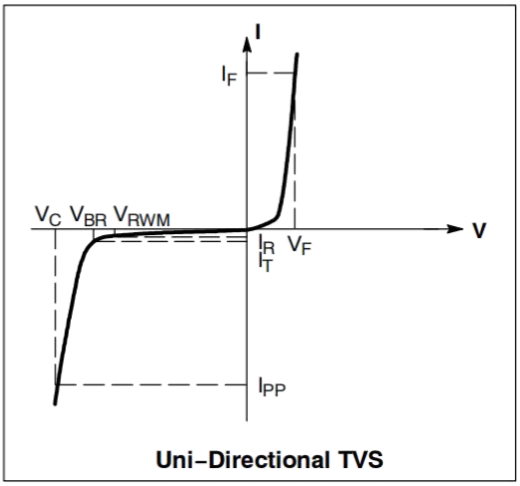
Absolute Maximum Ratings(Tamb=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppp	50	W
Peak Pulse Current (8/20μs)	Ipp	4.0	A
ESD per IEC 61000−4−2 (Air) ESD per IEC 61000−4−2 (Contact)	VESD	±20 ±15	KV
Operating Temperature Range	TJ	−55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (TA=25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM	--	--	3.3	V	Any I/O pin to ground
Breakdown Voltage	VBR	4.2	--	--	V	IT= 1mA,Any I/O pin to ground
Reverse Leakage Current	IR	--	--	0.1	uA	VRWM=3.3V,Any I/O pin to ground
Forward Voltage	VF	--	--	1.2	V	IF = 10 mA
Clamping Voltage	VC	--	--	10	V	Ipp=1A(8x 20us pulse), Any I/O pin to ground
Clamping Voltage	VC	--	--	14	V	Ipp=4A(8x 20us pulse), Any I/O pin to ground
Junction Capacitance	Cvo-GND	--	0.5	0.6	pF	VR=0V,f=1MHz,Any I/O to GND
	$C_{I/O-I/O}$	--	0.2	0.3	pF	VR=0V,f=1MHz,between I/O pins

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_F	Forward Current
V_F	Forward Voltage @ I_F
P_{pk}	Peak Power Dissipation
C	Capacitance @ V_R = 0 and f = 1.0 MHz



Uni−Directional TVS

Characteristic Curves

Fig1. 8/20μs Pulse Waveform

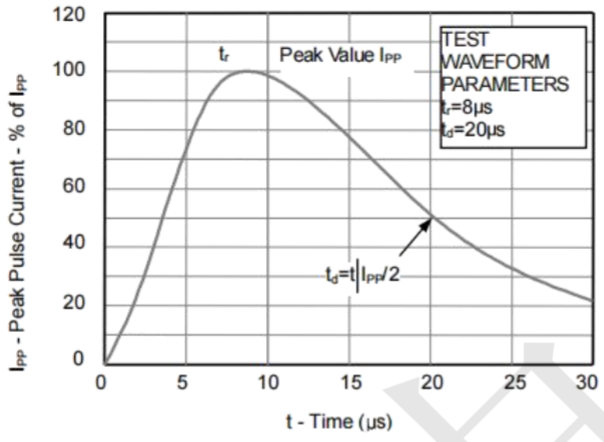


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)

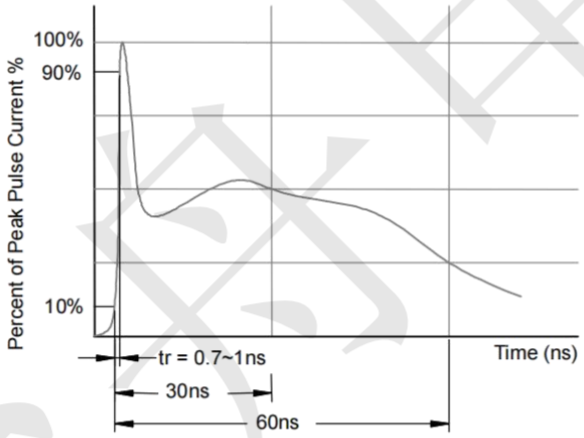
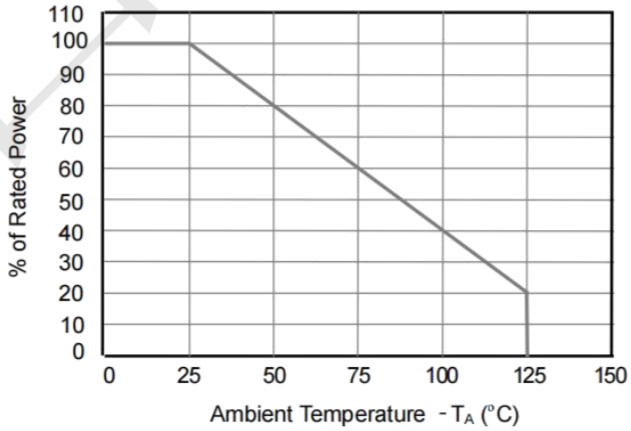
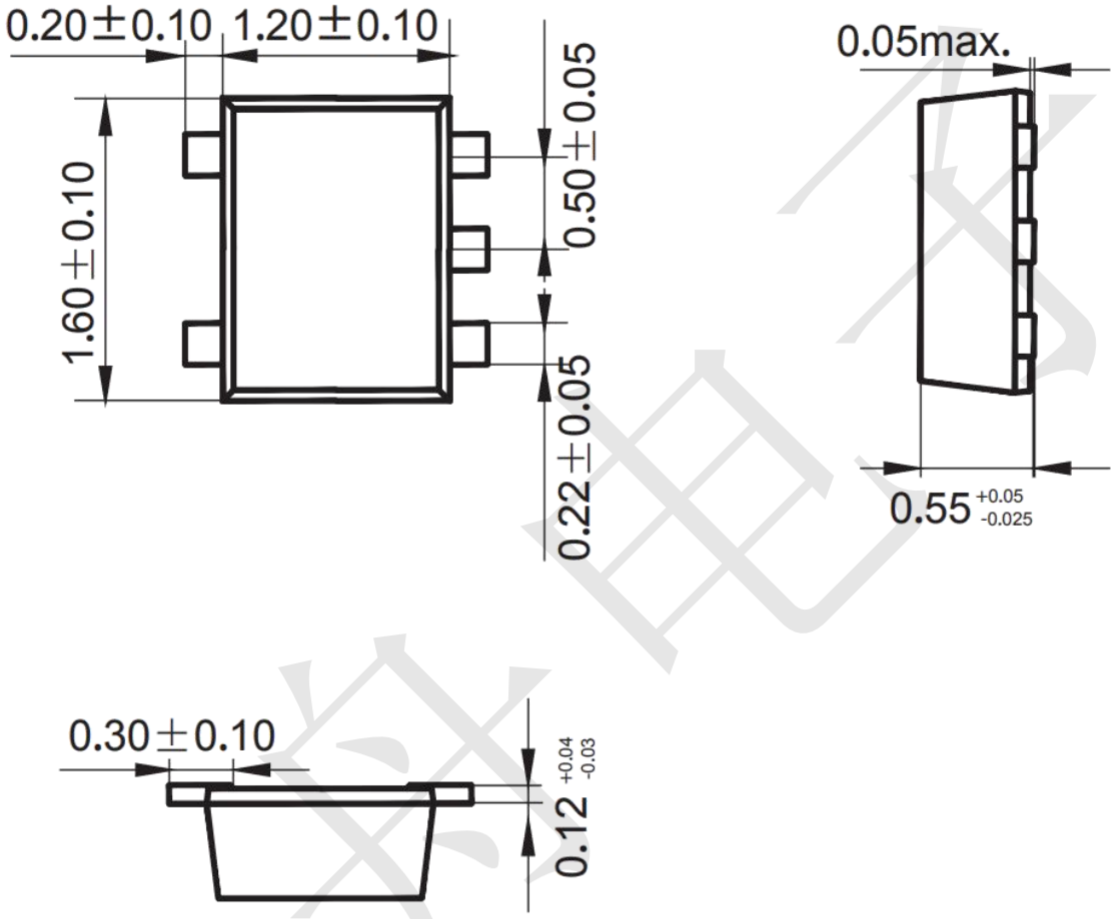


Fig3. Power Derating Curve



Package information (unit: mm)

SOT553



Mounting Pad Layout (unit: mm)

