TPSGM3756D6

High Efficiency, Support 0.3% PWM Dimming Boost WLED Driver

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GENERAL DESCRIPTION

The is a white LED driver with integrated boost converter. With an internal 38V switch FET, the drives up a string of up to 10 LEDs in series. The boost converter runs at 1.1MHz fixed switching frequency to reduce
output ripple, improve conversion efficiency, and allows for the use of small external components.

The default white LED current is set with the external sense resistor RSET, and the feedback voltage is regulated to 200mV, as shown in the typical application. During the operation, a pulse width modulation (PWM) signal can be applied to the CTRL pin through which the duty cycle determines the feedback reference voltage.

integrates built-in soft-start function to minimize the power supply inrush current.
also integrates over-current protection , LED open protection and over temperature protection(OTP) to prevent chip from entering abnormal operating conditions.

FEATURES

- Support 0.3% PWM dimming
- 1.1MHz Switching Frequency
- 38V Over-voltage Protection for up to 10 LEDs in Series
- 200mV Reference Voltage
- 2.7V to 5.5V Input Voltage Range
- Over-current and Over-temperature Protection
- Built-in Soft-start Limits Inrush Current
- Package DFN2x2-6L

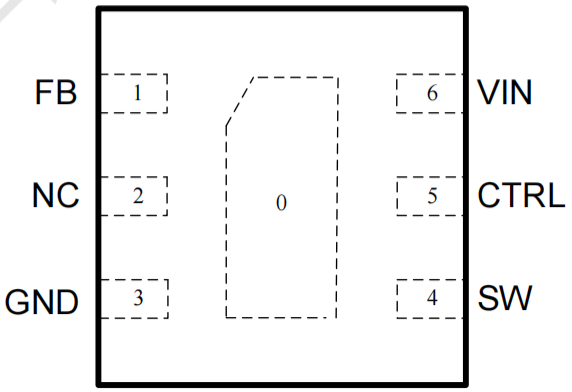
APPLICATIONS

- Cellular Phones
- Ultra Mobile Devices
- Portable Media Players
- PDAs, GPS Receivers
- White LED Backlighting

PIN CONFIGURATION

Top View

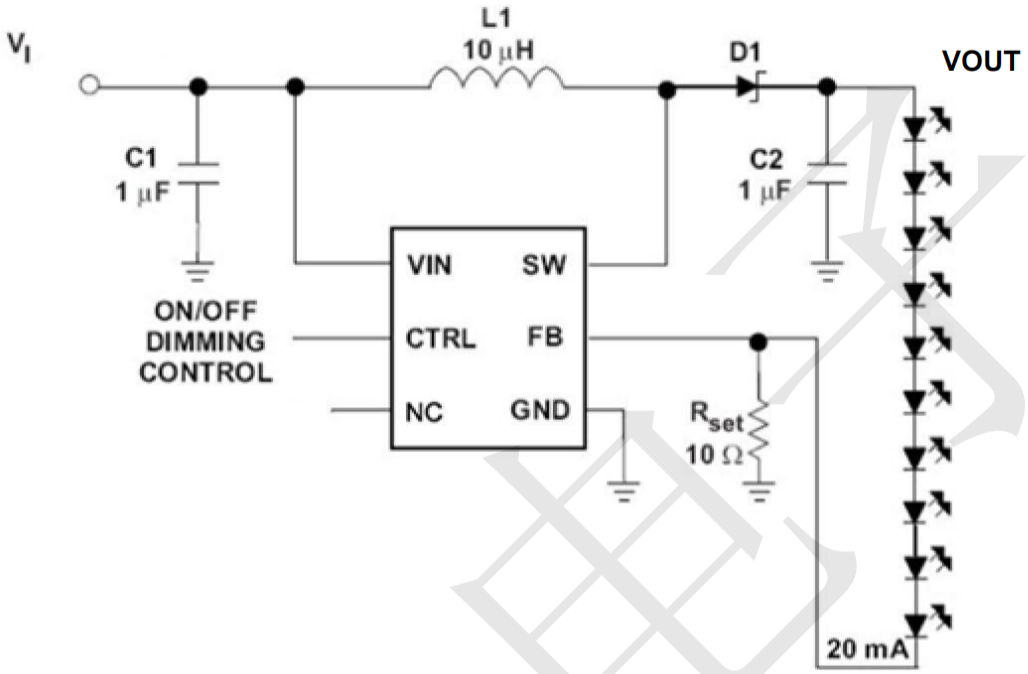
DFN2x2-6L



PIN DESCRIPTION

NO.	NAME	I/O	DESCRIPTION
1	FB	I	Feedback pin for current. Connect the sense resistor from FB to GND.
2	NC	-	No Connect
3	GND	G	Ground.
4	SW	I	This is the switching node of the IC. Connect the inductor between the VIN and SW pin. This pin is also used to sense the output voltage for open LED protection.
5	CTRL	I	Control pin of the boost regulator. It is a multi-functional pin which can be used for enable control and digital dimming.
6	VIN	I	The input supply pin for the IC. Connect VIN to a supply voltage between 2.7V and 5.5V.
0	GND	-	Ground，thermal pad.

Typical Application Circuit



ABSOLUTE MAXIMUM RATINGS(1)

PARAMETERS	RANGE
Supply voltage range VIN(2)	-0.3V to 6V
Voltage on FB,CTRL (2)	-0.3V to 6V
Voltage on SW(2)	-0.3V to 40V
Junction-to-ambient thermal resistance θ_{JA}	140℃/W
Operating free-air temperature range	-40℃ to 85℃
Operating Junction temperature T_J	-40℃ to 150℃
Storage temperature T_{STG}	-65℃ to 150℃
Lead Temperature (Soldering 10 Seconds)	260℃
ESD	
ALL PINS HBM (human body model)	±2000V
Latch-up	
Latch-up current maximum rating per JEDEC standard	+IT：450mA -IT：-450mA

(1) Conditions out of those ranges listed in "absolute maximum ratings" may cause permanent damages to the device. In spite of the limits above, functional operation conditions of the device should within the ranges listed in "recommended operating conditions". Exposure to absolute-maximum-rated conditions for prolonged periods may affect device reliability.
(2) All voltage values are with respect to network ground terminal.

RECOMMENDED OPERATING CONDITIONS

Symbol	Description	MIN	TYP	MAX	UNIT
V_{IN}	Input voltage range, VIN	2.7		5.5	V
L	Inductor(1)	10		22	μH
C_{IN}	Input capacitor	1		10	μF
C_O	Output capacitor(1)	0.47		10	uF
T_A	Operating ambient temperature	-40		85	°C
T_J	Operating junction temperature	-40		125	°C

(1) These values are recommended values that have been successfully tested in several applications. Other values may be acceptable in other applications but should be fully tested by the user.

DISSIPATION RATINGS

RqJC	RqJA	DERATING FACTOR ABOVE TA = 25°C	T_A < 25°C	T_A = 70°C	T_A = 85°C
20°C/W	140°C/W	7.1 mW/°C	893 mW	571 mW	464 mW

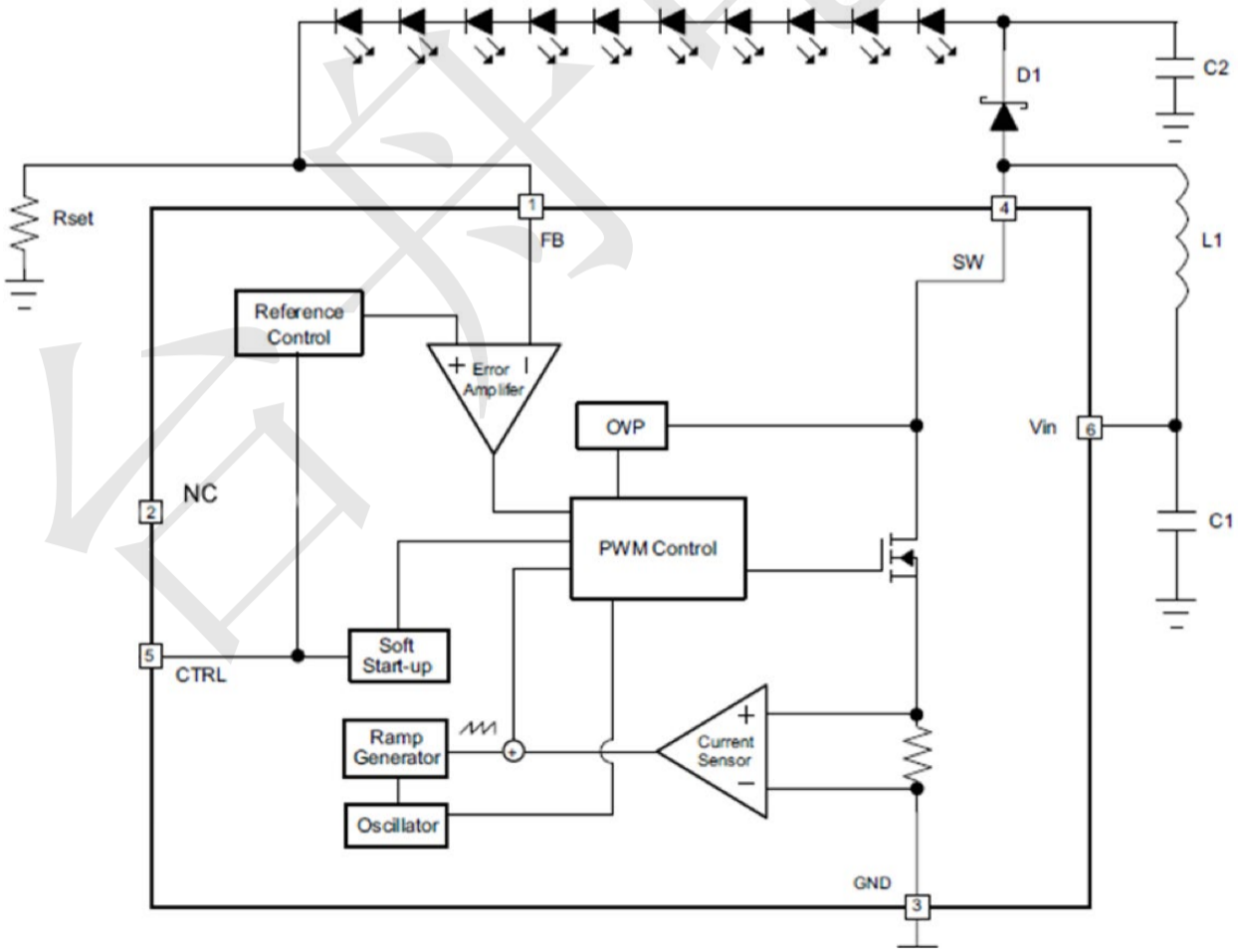
ELECTRICAL CHARACTERISTICS

Test Condition: TA = 25℃, VIN = 3.6V, VCTRL = VIN (Unless otherwise specified).

PARAMETER		TEST CONDITION	MIN	TYP	MAX	UNIT
SUPPLY VOLTAGE AND CURRENT						
V_{IN}	Input voltage range		2.7		5.5	V
V_{UVLO}	Under-voltage lockout threshold	V_{IN} falling		2.2	2.39	V
V_{HYS}	Under-voltage lockout hysteresis			100		mV
I_{SD}	Shutdown current	V_{CTRL} = GND, V_{IN}=4.2V		0.1	1	μA
I_Q	Operating quiescent current	V_{FB} = 1V		250	350	μA
PWM DIMMING CONTROL						
f_{PWM}	Frequency of PWM dimming		10		100	kHz
D_{PWM}	PWM dimming duty cycle		0.3		100	%
t_{MIN_ON}	Minimum on pulse width			50		ns
VOLTAGE AND CURRENT CONTROL						
V_{REF}	Voltage feedback regulation voltage		194	200	205	mV
V_{REF_PWM}	Voltage feedback regulation voltage under brightness control	PWM duty cycle = 1%		2.25		mV
		PWM duty cycle = 0.3%		0.6		mV
I_{FB}	Voltage feedback input bias current			0.1	1	μA
BOOST CONVERTER						
$R_{DS(on)}$	N-channel MOSFET on-resistance	V_{IN} = 3.6V		0.4	0.7	Ω
		V_{IN} = 3.0V			0.7	Ω
f_S	Oscillator frequency			1100		kHz
D_{MAX}	Maximum duty cycle		90	93		%
OCP AND OVP						
I_{LIM}	N-channel MOSFET current limit			1.5		A
V_{OVP}	Open LED overvoltage protection threshold	Measured on the SW pin		38		V

PARAMETER		TEST CONDITION	MIN	TYP	MAX	UNIT
CTRL INTERFACE						
V_{CTRL_H}	CTRL logic high voltage	V_{IN} = 2.7V to 5.5V	1.4			V
V_{CTRL_L}	CTRL logic low voltage	V_{IN} = 2.7V to 5.5V			0.4	V
R_{CTRL}	CTRL pull down resistor			600		kΩ
t_{OFF}	CTRL pulse width to shutdown	CTRL high to low	2.5			ms
THERMAL SHUTDOWN						
T_{OTP}	Thermal shutdown threshold			165		℃
T_{HYS}	Thermal shutdown threshold hysteresis			15		℃

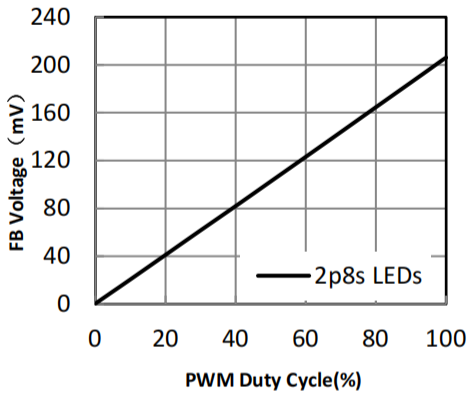
Function Block Diagram



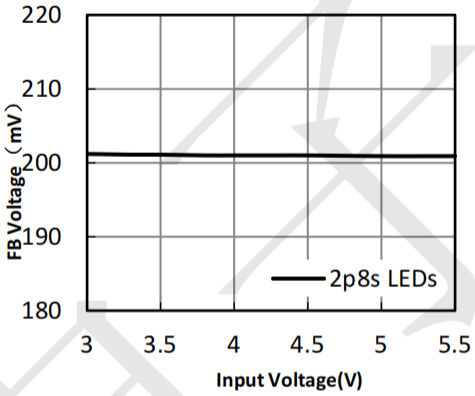
TYPICAL PERFORMANCE CHARACTERISTICS

Test Condition: TA = 25℃, VIN = 3.6V, VCTRL = VIN (Unless otherwise specified).

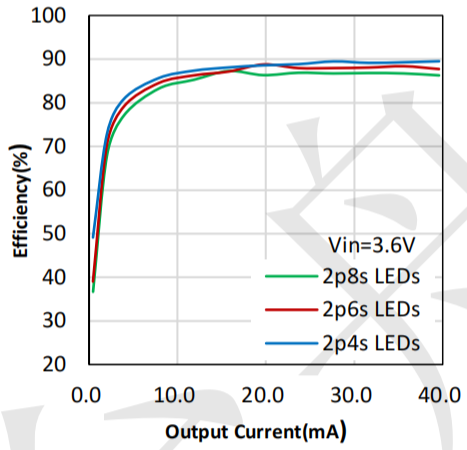
FB vs PWM DUTY CYCLE



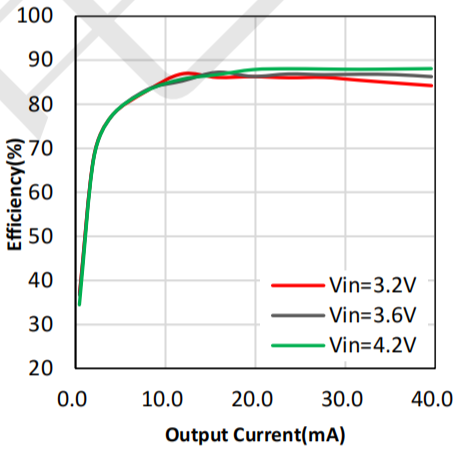
FB vs INPUT VOLTAGE



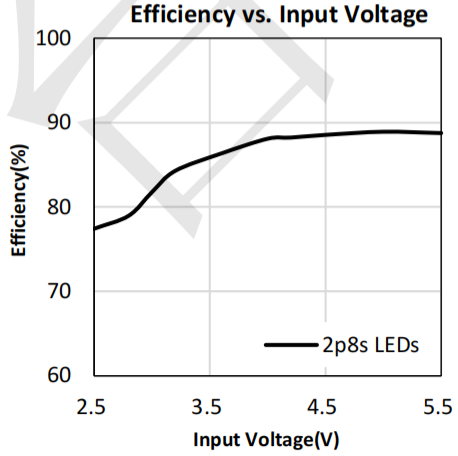
Efficiency vs. Output Current



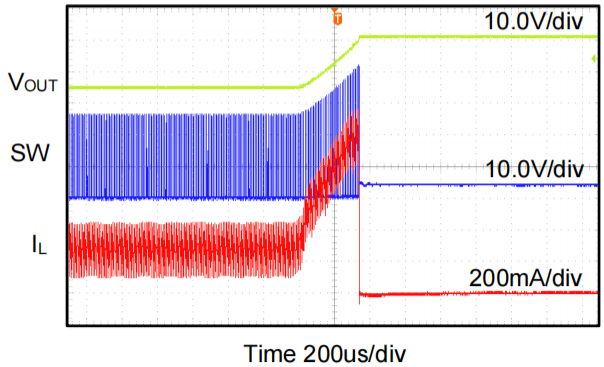
Efficiency vs. Output Current



Efficiency vs. Input Voltage



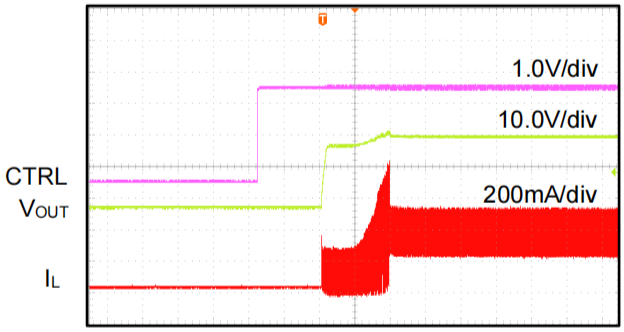
OPEN LED PROTECTION（VIN=3.6V）



TYPICAL PERFORMANCE CHARACTERISTICS

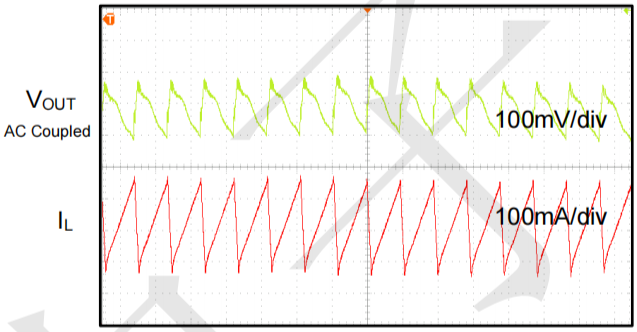
Test Condition: TA = 25℃, VIN = 3.6V, VCTRL = VIN (Unless otherwise specified).

SOFT-START WAVEFORM（V_{IN}=3.6V）



Time 2ms/div

SWITCHING WAVEFORM（V_{IN}=3.6V）



Time 1us/div

DETAILED DESCRIPTION

OPERATION

The is a high efficiency, high output voltage boost converter in small package size. The device is ideal for driving up to 10 white LEDs in series. The serial LED connection provides even illumination by sourcing the same output current through all LEDs. The device integrates 38V/2A switch FET and operates in pulse width modulation (PWM) with 1.1MHz fixed switching frequency. The duty cycle of the converter is set by the error amplifier output and the current signal applied to the PWM control comparator. The control architecture is based on traditional current-mode control. Therefore, slope compensation is added to the current signal to allow stable operation for duty cycle larger than 50%. The feedback loop regulates the FB pin to a low reference voltage (200mV typical), reducing the power dissipation in the current sense resistor.

SOFT START-UP

Soft-start circuitry is integrated into the IC to avoid a high inrush current during start-up. After the device is enabled, the voltage at FB pin ramps up to the reference voltage. This ensures that the output voltage rises slowly to reduce the input current.

OPEN LED PROTECTION

Open LED over-voltage protection circuitry prevents IC damage as the result of white LED disconnection. The monitors the voltage at the SW pin during each switching cycle. The circuitry turns off the switch FET as soon as the SW voltage exceeds the VOVP threshold for 8 clock cycles.

SHUTDOWN

The CTRL input is used to enable or disable the . Pulling the CTRL pin higher than 1.4V will enable the device. The has an internal shutdown delay circuitry, when the CTRL pin is held low for an amount of time longer than 2.5ms, the will enter shutdown mode and the input supply current for the device is less than 1μA. Although the internal FET does not switch in shutdown, there is still a DC current path between the input and the LEDs through the inductor and Schottky diode. The minimum forward voltage of the LED array must exceed the maximum input voltage to ensure that the LEDs remain off in shutdown. However, in the typical application with two or more LEDs, the forward voltage is large enough to reverse bias the Schottky and keep leakage current low.

UNDER-VOLTAGE LOCKOUT

An under-voltage lockout prevents operation of the device at input voltage below typical 2.2V. When the input voltage is below the under-voltage threshold, the internal switch FET is turned off. If the input voltage rises by under-voltage lockout hysteresis, the IC restarts.

CURRENT PROGRAM

The loop of Boost structure will keep the FB pin voltage equal to the reference voltage V_{FB}. Therefore, when R_{set} connects FB pin and GND, the current flows from V_{OUT} through LED and R_{set} to GND will be decided by the current on R_{set}, which is equal to following equation:

$$I_{LED} = V_{FB} / R_{set}$$

Where

I_{LED} =output current of LEDs

V_{FB} =regulated voltage of FB

R_{set} =current sense resistor

PWM BRIGHTNESS DIMMING

When the CTRL pin is constantly high, the FB voltage is regulated to 200mV typically. However, the CTRL pin allows a PWM signal to reduce this regulation voltage, it achieves LED brightness dimming. The relationship between the duty cycle and the FB voltage is given by the following equation:

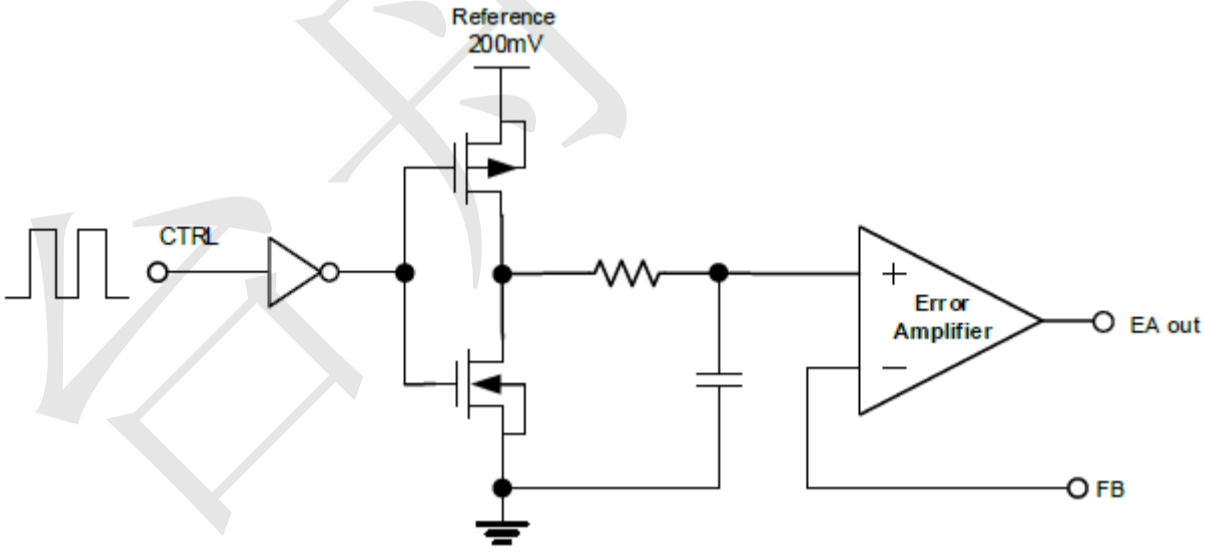
$$V_{FB} = Duty \times 200mV$$

Where:

Duty = duty cycle of the PWM signal

200mV = internal reference voltage

The IC chops up the internal 200mV reference voltage at the duty cycle of the PWM signal. The pulse signal is then filtered by an internal low pass filter. The output of the filter is connected to the error amplifier as the reference voltage for the FB pin regulation. Therefore, although a PWM signal is used for brightness dimming, only the WLED DC current is modulated, which is often referred as analog dimming. This eliminates the audible noise which often occurs when the LED current is pulsed in replica of the frequency and duty cycle of PWM control. Unlike other scheme which filters the PWM signal for analog dimming, regulation voltage is independent of the PWM logic voltage level which often has large variations.



THERMAL SHUTDOWN

An internal thermal shutdown turns off the device when the typical junction temperature of is exceeded 165℃. The device is released from shutdown automatically when the junction temperature decreases by 15℃.

APPLICATION INFORMATION

MAXIMUM OUTPUT CURRENT

The over-current limit in a boost converter limits the maximum input current and thus maximum input power for a given input voltage. Maximum output power is less than maximum input power due to power conversion losses. The current limit setting, input voltage, output voltage and efficiency can all change maximum current output. Therefore, the ripple has to be subtracted to derive maximum DC current. The ripple current is a function of switching frequency, inductor value and duty cycle. The following equations take into account of all the above factors for maximum output current calculation.

$$I_P = \frac{1}{L \times F_S \times (\frac{1}{V_{OUT} + V_F - V_{IN}} + \frac{1}{V_{IN}})}$$

Where:

I_P = inductor peak to peak ripple L= inductor value

V_F = Schottky diode forward voltage

F_S = switching frequency

V_{OUT} = output voltage of the boost converter. It is equal to the sum of V_{FB} and the voltage drop across LEDs.

$$I_{OUT_MAX} = \frac{V_{IN} \times (I_{LIM} - I_P/2) \times \eta}{V_{OUT}}$$

Where:

I_{OUT_MAX} = maximum output current of the boost converter

I_{LIM} = over-current limit, for worst case calculation the minimum value has to be chosen.

η= efficiency

INDUCTOR SELECTION

Inductor value, saturation current, and Direct Current Resistance (DCR) are the most important parameters used to select an inductor .

The suitable inductance range for this device is 10 μH to 22μH. Higher inductance lowers the value of the RMS current of the step-up converter. Together with a lower DCR value of the inductor, the total inductor power loss becomes much lower. Considering inductor size and cost, 10 μH inductance is recommended. The below table lists some recommended inductors.

Recommend Inductors

Part Number	L(μH)	DCR MAX (Ω)	Saturation Current (mA)	Size(L x W x H mm)	Vendor
LQH3NPN100NM0	10	0.3	750	3×3×1.5	Murata
VLCF5020T-220MR75-1	22	0.4	750	5×5×2.0	TDK
CDH3809/SLD	10	0.3	570	4×4×1.0	Sumida
A997AS-220M	22	0.4	510	4×4×1.8	TOKO

SCHOTTKY DIODE SELECTION

The high switching frequency of the demands a high-speed rectification for optimum efficiency. Ensure that the diode average and peak current rating exceeds the average output current and peak inductor current. In addition, the

diode's reverse breakdown voltage must exceed the open LED protection voltage. To achieve better efficiency, a low VF Schottky diode is recommended. ONSemi MBR0540 and ZETEX ZHCS400 are recommended.

OUTPUT CAPACITOR SELECTION

The output capacitor is mainly selected to meet the requirements for the output ripple and loop stability.

The output voltage of the could reach up to 38V. X7R type output capacitor rated of 50 V is suggested. To reduce power loss, a ceramic capacitor with a low Effective Series Resistance (ESR) is better. The capacitor in the range of 0.47uF to 10uF is recommended for input side. The recommended output capacitor value is 1uF.

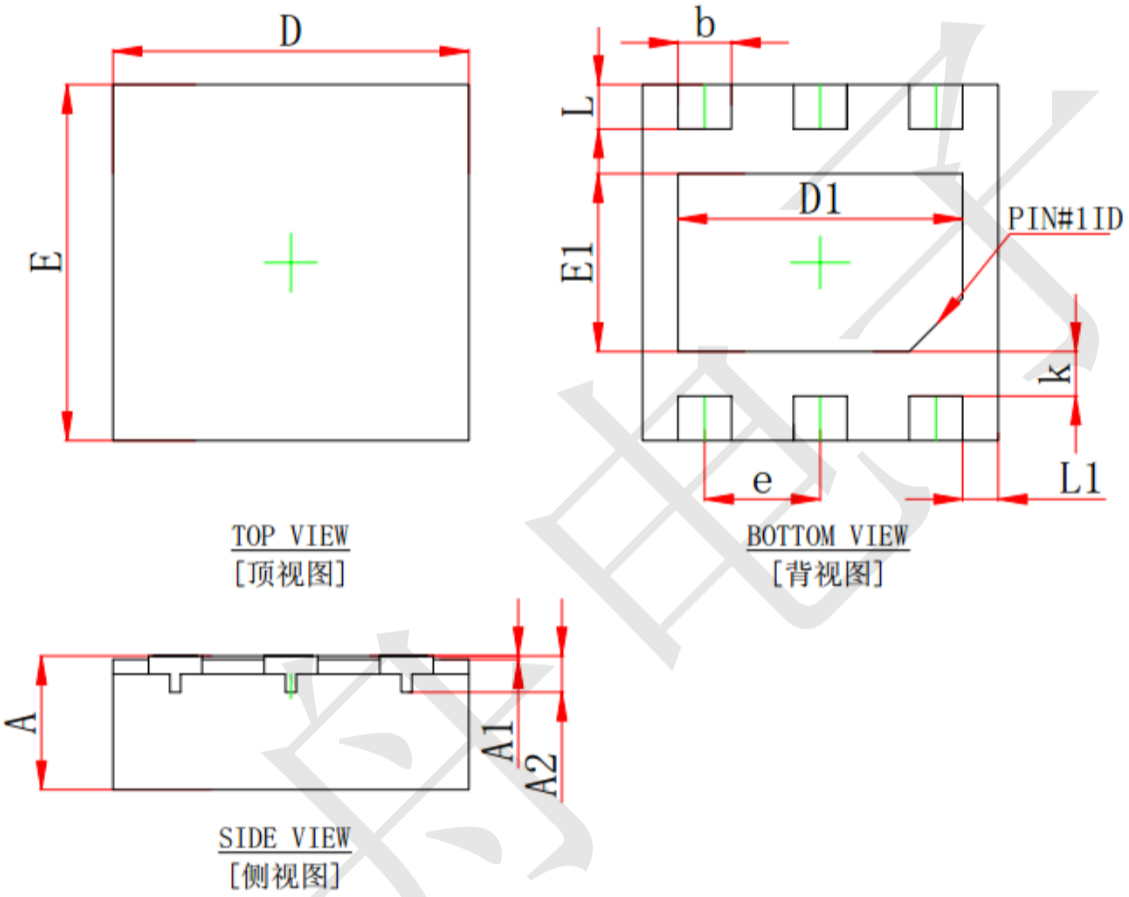
PCB LAYOUT CONSIDERATION

For best performance of the , the following guidelines must be followed.

1. Input /Output capacitors should be placed close to the IC and connected to ground to reduce noise coupling.
2. The Exposed Pad should be connected to a strong ground plane for heat sinking and noise protection.
3. Keep the main current traces as possible as short and wide.
4. Vin will be connected with Vbattery with wide track, to avoid the interference of RF PA and Audio PA.

PACKAGE INFORMATION

DFN2x2-6L



Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Nor.	Max.	Min.	Nor.	Max.
A	0.700	0.750	0.800	0.028	0.030	0.031
A1	0.000	——	0.050	0.000	——	0.002
A2		0.203REF.			0.008REF.	
D	1.924	2.000	2.076	0.076	0.079	0.082
E	1.924	2.000	2.076	0.076	0.079	0.082
D1	1.550	1.600	1.650	0.061	0.063	0.065
E1	0.950	1.000	1.050	0.037	0.039	0.041
b	0.250	0.300	0.350	0.010	0.012	0.014
e		0.650BSC.			0.026BSC.	
k	0.200	0.250	0.300	0.008	0.010	0.012
L	0.200	0.250	0.300	0.008	0.010	0.012
L1		0.200BSC.			0.079BSC.	