承　　认　　书

SPECIFICATION FOR APPROVAL

客户名称：
CUSTOMER：_____

产品名称　　多层片式瓷介电容器
PARTNAME：Multilayer Chip Ceramic Capacitor

产品规格　　“S”系列产品

SPECIFICATION：_____

承认书编号
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潮州三环（集团）股份有限公司

地址：中国广东省潮州市凤塘三环工业城

电话：86-768-6855932

传真：86-768-6855921

网址：HTTP：//WWW.CCTC.CC

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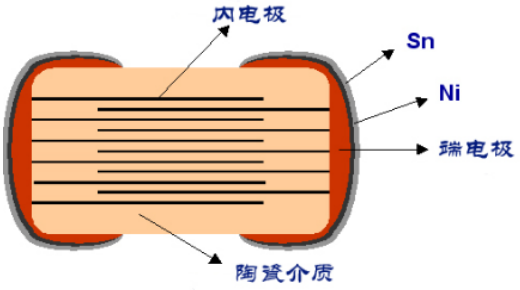
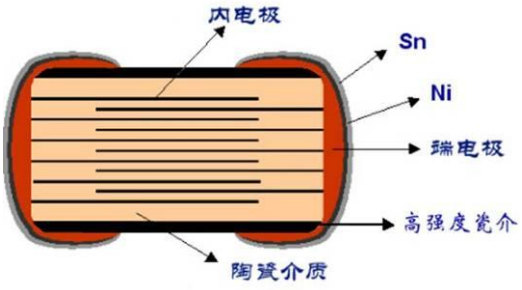
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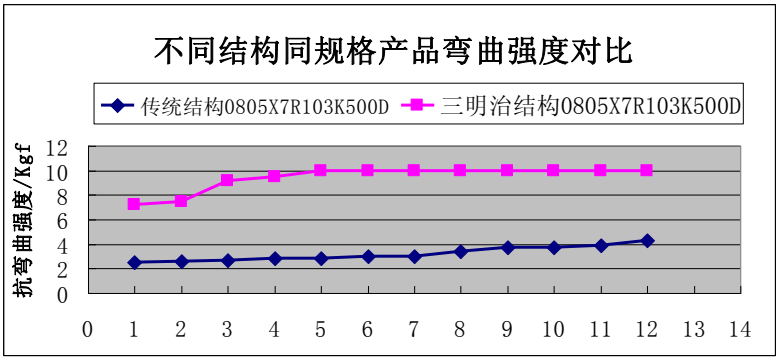
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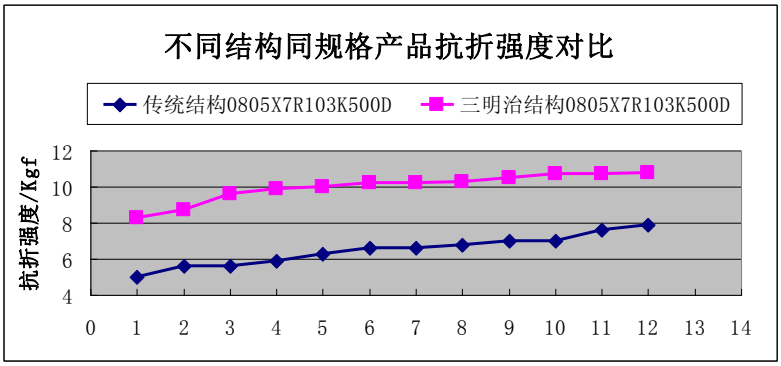
1. 产品优势 Advantages

1.1 产品结构对比 Structure

传统产品结构 Structure of traditional product	三明治产品结构 Structure of the sandwich-structure product
	
相对传统产品，三明治机构产品增加了高强度介质层。 The new product called sandwich-structure add high intensity layers .	

1.2 产品强度对比 Product Intensity Contrast





以上对比，表明三明治结构产品强度更高。

Above all , the sandwich-structure products are more stronger .

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2．产品应用 Product Application

三明治结构产品用于替代低容值常规 X7R 产品，如节能灯、LED 灯、独石等对 MLCC 产品强度要求较高的领域。

The sandwich-structure products are used to replace traditional X7R products . Usually used in the regions asking for high intensity , eg. ESL , LED , monolithic capacitor and so on .

3．电容器及介质分类 Types of Capacitor and Dielectric Material

※X7R：此类介质材料的电容器为Ⅱ类电容器，具有较高的介电常数，容量比Ⅰ类电容器高，具有较稳定的温度特性，适用于容量范围广，稳定性要求不高的电路中，如隔直、耦合、旁路、鉴频等电路中。

※ X7R： material is a kind of material has high dielectric constant. The capacitor made of this kind material is considered as Class Ⅱ capacitor whose capacitance is higher than that of class Ⅰ. These capacitors are classified as having a semi-stable temperature characteristic and used over a wide temperature range, such in these kinds of circuits, DC-blocking, decoupling, bypassing, frequency discriminating etc.

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4. 产品命名 Product Parts Numbering System

(例) (example)

TCC 0805 X7R 104 K 500 D T S

陶瓷电容器代号
Code of Ceramic Capacito

尺 寸 **Chip Size**
规格 Type=L×W
0603=1.60×0.80mm
0805=2.00×1.25mm
1206=3.20×1.60mm

温度特性
Dielectrics
X7R

电容值
Capacitance
104=10×10^4
=1000,00
4 表示 10^4
Two significant digits followed by no. of zero

容量容差
Capacitance Tolerance
J=±5.0%
K=±10%
M=±20%

额定电压
Rated Voltage
500=50V_{DC}
101=100V_{DC}
201=250V_{DC}
251=250V_{DC}

厚 度
Thickness
C:0.80±0.10mm
D:0.85±0.10mm
F:1.25±0.20mm

包 装 **Packing**
B: 袋散装
B: bulk packaging in a bag
T: 编带
T: tape carrier packaging

三明治结构
S

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5. 产品容量范围 Product Capacitance Range

	0603（1608）			0805（2012）						1206（3216）					
	X7R 系列			X7R 系列						X7R 系列					
CpV_{DC}	100	50	CpV_{DC}	500	250	200	100	50	CpV_{DC}	2000	1000	500	250	100	50
221	C	C	221	D/F	D/F	D/F	D/F	B	221	D/F	D/F	D/F	D/F	D/F	D
331	C	C	331	D/F	D/F	D/F	D/F	B	331	D/F	D/F	D/F	D/F	D/F	D
471	C	C	471	D/F	D/F	D/F	D/F	B	471	D/F	D/F	D/F	D/F	D/F	D
561	C	C	561	D/F	D/F	D/F	D/F	B	561	D/F	D/F	D/F	D/F	D/F	D
681	C	C	681	D/F	D/F	D/F	D/F	B	681	D/F	D/F	D/F	D/F	D/F	D
821	C	C	821	D/F	D/F	D/F	D/F	B	821	D/F	D/F	D/F	D/F	D/F	D
102	C	C	102	D/F	D/F	D/F	D/F	B	102	D/F	D/F	D/F	D/F	D/F	D
222	C	C	222	F	D/F	D/F	D/F	B	222		F	D/F	D/F	D/F	D
332	C	C	332		D/F	D/F	D/F	B	332		F	D/F	D/F	D/F	D
472	C	C	472		D/F	D/F	D/F	B	472		F	D/F	D/F	D/F	D
562	C	C	562		D/F	D/F	D/F	B	562		F	D/F	D/F	D/F	D
682	C	C	682		D/F	D/F	D/F	B	682		F	D/F	D/F	D/F	D
103	C	C	103		D/F	D/F	D/F	B	103		F	D/F	D/F	D/F	D
153	C	C	153		D/F	D/F	D/F	B	153			D/F	D/F	D/F	D
183	C	C	183		D/F	D/F	D/F	B	183			D/F	D/F	D/F	D
223		C	223		D/F	D/F	D/F	B	223			D/F	D/F	D/F	D
273		C	273		F	F	D/F	B	273			F	D/F	D/F	D
333		C	333		F	F	D/F	B	333			F	F	F	D
393		C	393		F	F	D/F	B	393			F	F	F	D
473		C	473		F	F	D/F	B	473			F	F	F	D
563		C	563				D/F	B	563				F	F	D
683		C	683				D/F	B	683				F	F	D
104		C	104				F	B	104				F	F	D
154			154						154						D
184			184						184						D
224			224						224						D

容量容差： X7R: J(±5.0%)；K(±10%)；M(±20%)

厚度： B：0.60±0.10mm、C：0.80±0.10mm、D：0.85±0.10mm、F：1.25±0.20mm；以上容量仅供参考，具体容量取决于使用要求。

Tolerance: X7R: J(±5.0%),K(±10%),M(±20%)

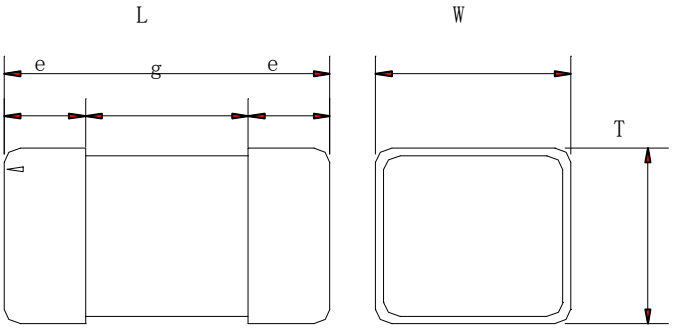
Thickness: B：0.60±0.10mm、C：0.80±0.10mm、D：0.85±0.10mm、F：1.25±0.20mm； Above capacitance for reference only, actual cap. range depends on the standard products.

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6. 产品尺寸 Dimensions

规格： 0603、0805、1206。

Chip Size： 0603、0805、1206。



规格 Type	L (mm)	W (mm)	e (mm)	g min (mm)	T (mm)				
0603	1.60±0.10	0.80±0.10	0.2~0.5	0.5	0.80±0.10	——	——	——	——
0805	2.00±0.10	1.25±0.10	0.2~0.7	0.7	0.85±0.10	——	——	——	——
1206	3.20±0.20	1.60±0.20	0.3~0.8	1.6	0.85±0.10	1.25±0.20	——	——	——

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7. 技术要求和测试条件 Specification and Test Condition

7.1 外观 Appearance

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	无损伤或异常 No defects or abnormalities	目视检查 Visual inspection.

7.2 尺寸 Dimensions

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	在要求的范围内 Within the specified dimensions	用千分尺 Using calipers on micrometer

7.3 容量 Capacitance

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	在要求的容值容差范围内 Within the specified tolerance J: ±5%； K: ±10%； M: ±20%	1.0±0.2Vrms, 1KHz±10%
备注：测试温度：25℃±3℃，测试湿度：＜70%RH.		

7.4 损耗 Dissipation Factor

类型 Dielectrics	技术要求 Specification	测试条件 TestingCondition
X7R	DF≤2.5%	1.0±0.2Vrms, 1KHz±10%,
备注：测试温度：25℃±3℃，测试湿度：＜70%RH.		

7.5 绝缘电阻 Insulation Resistance

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	大于 4GΩ 或 100s (大于其中较小的数值) More than 4 GΩ or 100s , whichever is smaller.	U_R≤250V $U_{测}=U_R$ ； 充电时间:60±5 秒、温度:25℃ Charge Time:60±5sec Temperture:25℃
备注：测试温度：25℃±3℃，测试湿度：＜70%RH.		

7.6 耐电压 Dielectric Strength

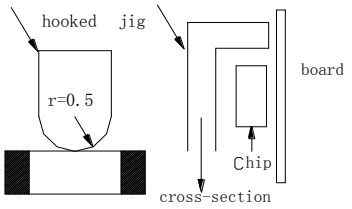
类型 Dielectrics	额定电压范围 Rated voltage range	耐电压性能测试方法 Measuring Method
X7R	$U_R \leqslant 50V$	施加额定电压的 250%,5 秒,最大电流不超过 50mA Force 300%Rated voltage for 5second. Max..current should not exceed 50 mA.
	$100V \leqslant U_R < 250V$	施加额定电压的 200%,5 秒,最大电流不超过 50mA Force 200%Rated voltage for 5second. Max..current should not exceed 50 mA.

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7.7 静电容量温度特性 Temperature Coefficient of Capacitance

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	容 量 变 化 在 ±15% 以 内 Capacitance change within ±15%	按系列温度顺序测试电容容量 Measure capacitance under follow table list temperature: 步骤 STEP / X7R 1 / 25 ±2 2 / -55±3 3 / 25 ±2 4 / 125±3 5 / 25 ±2 X7R 与 25℃时的电容容量相比较，电容容量在温度范围内的变化在要求的范围之内。 The ranges of capacitance change compared within the above 25℃ value over the temperature ranges shall be within the specified ranges.

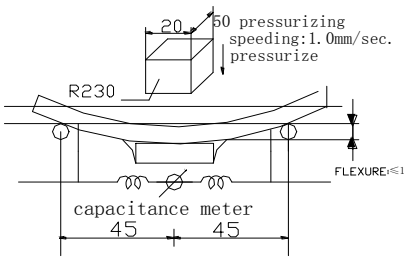
7.8 附着力 Adhesion

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	端电极无松动，也无其它不良现象 No removal of the terminations or other defect shall occur.	施加 6N 的压力，并保持 10±1 秒 The pressurizing force shall be 6N (=600g*f) and the duration of application shall be 10±1sec. 

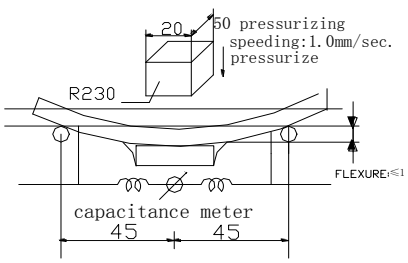
7.9 可焊性 Solderability of Termination

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	端电极挂锡面积不小于 95%，针孔或粗糙面积小于 5% 95% min. coverage of both terminal electrodes and less than 5% have pin holes or rough spots.	锡炉温度：245±5℃ 浸入时间：2±1 秒 两侧端电极完全浸入焊锡炉 Solder temperature: 245±5℃ Dipping time: 2±1 seconds. Completely soak both terminal electrodes in solder

7.10 耐焊性 Resistance to leaching

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
	95%，针孔或 无开裂 both terminal 5% have pin No remarkable visual damage.	预热：120℃~150℃/60 秒 锡炉温度：270±5℃ 浸入时间： 10±1 秒 两侧端电极完全浸入焊锡炉 Solder temperature: 270±5℃ preheated: 120℃~150℃/60sec Dipping time: 10±1 seconds. Completely soak both terminal electrodes in solder

7.11 端电极结合强度 Bending

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	无可见损伤； 容量变化小于等于±12.5% No remarkable visual damage Cp change ≤±12.5%	将片状电容器安装在测试夹具上，按图所示方向以 1.0mm/s 的速率施加压力，弯曲 1mm。 Solder the capacitor on testing substrate and put it on testing stand. The middle part of substrate shall successively be pressurized by pressuring rod at a rated of about 1.0mm/sec. Until the deflection become means of the 1.0mm. 

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7.12 耐焊接热 Resistance to Soldering Heat

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	无明显可见损伤 容量变化在±7.5%以内 DF 满足产品初始值的要求 IR 满足产品初始值的要求 No remarkable visual damage Cp change within ±7.5% DF meets initial standard value. IR meets initial standard value.	焊接温度: 270±5℃ 预热: 120~150℃ 60 秒 浸入时间: 10±1 秒 在室温下放置 24±2 (COG) 或 48±4(X7R) 小时以后测量 试验后在标准条件下恢复 Soldering temperature: 270±5℃ Preheating: 120~150℃ 60sec. Dipping time: 10±1 seconds. Measurement to be made after being kept at room temperature for 48±4(X7R) hours. Recov ery for the following period under the standard condition after test.

7.13 温度快速循环 Temperature Cycle

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	无明显可见损伤 容量变化在±7.5%以内 No remarkable visual damage Cp change within ±7.5%	按下列步骤进行 5 次循环： To perform 5 cycles of the stated environment

步骤 Step	温度 Temperature	时间 Time
1	下限类别温度 +0/-3℃ Min. operating Temp.+0/-3℃	30min
2	25℃	2~3 min
3	上限类别温度 +3/-0℃ Max. operating Temp.+3/-0℃	30 min
4	25℃	2~3 min

在室温下放置 48±4(X7R) 小时以后测量
Measurement to be made after being kept at room temperature for 48±4hrs (X7R) at room temperature, then measure.

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7.14 稳态湿热 Moisture Resistance ,steady state

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	外观无明显可见损伤 容量变化在±12.5%以内 DF 为初始值的 2 倍以下 IR:大于 1000MΩ 或 50Ω-F(取较小值) Cp change within ±12.5% DF:Not more than 2 times of initial value R*C≥1000MΩ or 50Ω·F, whichever is smaller	测试温度: 40±2℃ 湿度: 90~95% RH 测试时间: 500 ±12hrs 在室温下放置 48±4 小时以后测量 Test temperature: 40±2℃ Humidity: 90~95% RH Testing time: 500 ±12hrs Measurement to be made after being kept at room temperature for 48±4hrs

7.15 耐湿负荷 Damp heat with load

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	外观无明显可见损伤 容量变化在±12.5%以内 DF 为初始值的 2 倍以下 IR: 大于 500MΩ 或 25Ω-F(取较小值) No remarkable visual damage Cp change≤±12.5% DF:Not more than 2 times of initial value R*C≥500MΩ or 25Ω·F, whichever is smaller	测试温度: 40±2℃ 湿度: 90~95% RH 电压: 额定电压 测试时间: 500 ±12hrs 在室温下放置 48±4(X7R) 小时以后测量 *在 40±2℃温度下，将电容器加额定直流电压 1hrs. 去掉电压，将电容器在室温下放置 48±4hrs 测量初始电容值。 Test temperature: 40±2℃ Humidity: 90~95% RH Voltage: 100% of the rated voltage Testing time: 500 ±12hrs Measurement to be made after being kept at room temperature for 48±4hrs (X7R) *Apply the rated DC voltage for 1 hour at 40±2℃. Remove and let sit for 48±4hrs at room temperature. Perform the initial measurement.
备注：该项可靠性试验仅适用于常规产品，不适用于中高压产品。		

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7.16 耐久性 Life Test

类型 Dielectrics	技术要求 Specification	测试条件 Testing Condition
X7R	外观无明显可见损伤 容量变化在 12.5%以内 DF 为初始值的 2 倍以下 IR:大于 1000MΩ 或 50Ω-F(取较小值) No remarkable visual damage Cp change≤±12.5% DF:Not more than 2 times of initial value R*C≥1000MΩ or 50Ω·F, whichever is smaller	测试温度：上限类别温度±3℃ 电压： UR＜100V 2 倍额定电压 100V≤UR＜500V 1.5 倍额定电压 测试时间: 1000 小时 在室温下放置 48±4(X7R) 小时以后测量 Test temperature: Max. Operating Temp. ±3℃ Voltage: UR＜100V 200% of the rated voltage 100V≤UR＜500V 150% of the rated voltage 500V≤UR＜1000V 120% of the rated voltage 1000V≤UR 100% of the rated voltage Testing time: 1000 hrs Measurement to be made after being kept at room temperature for 48±4hrs (X7R)
备注：该项可靠性试验仅适用于常规产品，不适用于中高压产品。		

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8.产品包装 Packing

8.1 袋式散装 Bulk Packing

10000 个/袋或按客户要求。Standard packing 10Kpcs/bag; others are according to customer request.

8.2 编带式包装 Tape Packing

规格 Type	尺寸 Size (mm)			编带数量(个/盘 pcs/reel)	
	长度 L	宽度 W	厚度 T	纸带 Paper Tape	塑料带 Plastic Tape
0603	1.6	0.8	0.8	4,000	N/A
0805	2.0	1.25	＜0.85	4,000	N/A
1206	3.2	1.6	＜0.85	4,000	N/A
			≥0.85	N/A	2,000

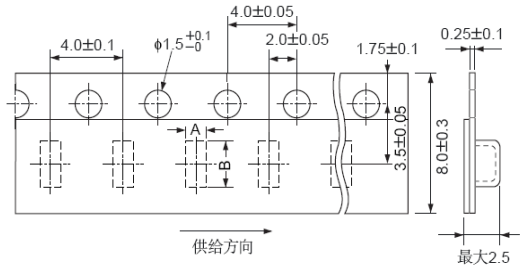
8.2.1 纸带尺寸 Dimensions of Packing Paper

Type	A	B	C	D	T
0603	1.05±0.10	1.85±0.10	4.0±0.10	2.0±0.10	1.1max
0805	1.55±0.15	2.3±0.15	4.0±0.10	2.0±0.10	1.1max
1206	1.95±0.15	3.5±0.15	4.0±0.10	2.0±0.10	1.1max

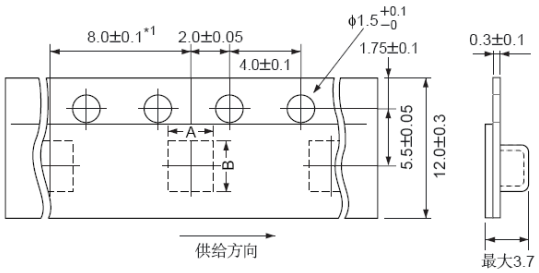
(单位 unit:毫米 mm)

8.2.2 塑料带尺寸 Dimensions of Embossed Packing

8mm宽，4mm间距编带

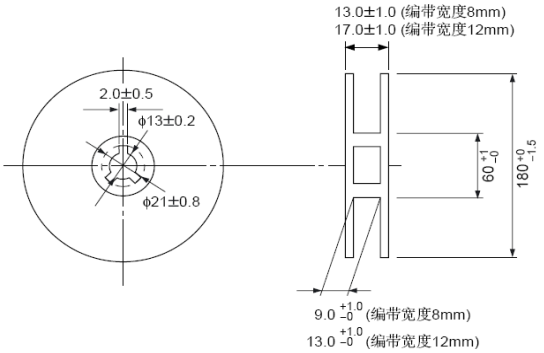


12mm宽，8mm/4mm间距编带

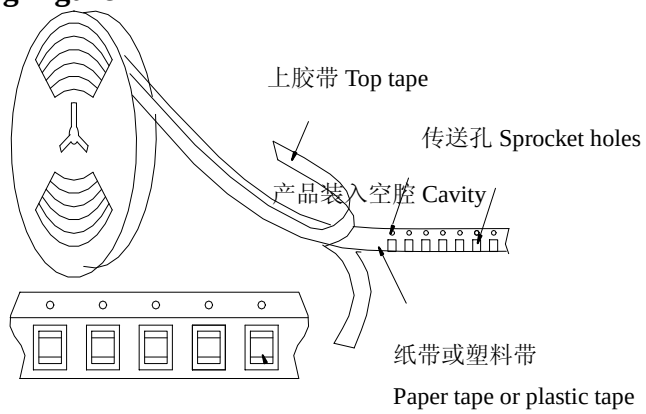


A：1.45±0.20 B：2.25±0.20 （0805）
A：1.95±0.20 B：3.50±0.20 （1206） (单位 unit:毫米 mm)

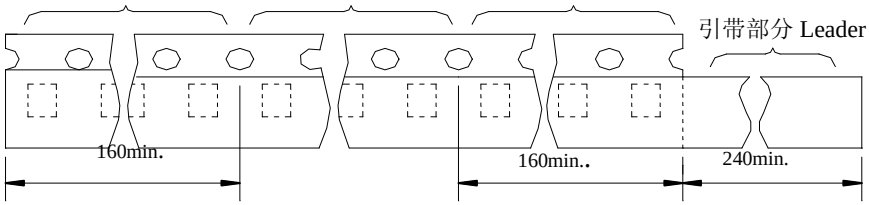
8.2.3 编带盘尺寸 Dimensions of Reel



8.2.4 编带方式 Taping Figure



空白部分 Empty section　产品装入部分 Chip insertion section 空白部分 Empty section



（单位 unit：毫米 mm）　　抽出方向 Feeding direction

8.2.5 编带方法 Taping Method

① 包装电容器的编带是顺时针卷绕的，由上往下的方向拉出编带时，传送孔处于编带的右侧。

② 在编带的前端，至少留出 5 个间距的引出带。

③ 在编带时，必须按下图留出引带部分或空白部分。

④ 在盘带的安装中的产品装错的数量每盘必须小于表示数量的 0.1%或 1 个为限，不连续发生错误。

⑤ 上胶带和下胶带不应超出编带的边缘，不能挡住传送孔。

⑥ 传送孔的累计误差为 10 个间距：±0.3 毫米以内。

⑦ 上胶带的剥离力矩应在 0.1 至 0.6 牛顿以内，其方向如下图所示。

①Tapes for capacitors are wound clockwise. The sprocket holes are to the right as the tape is pulled toward the user.

②The top tape and base tape are not attached at the end of the tape for a minimum of 5 pitches.

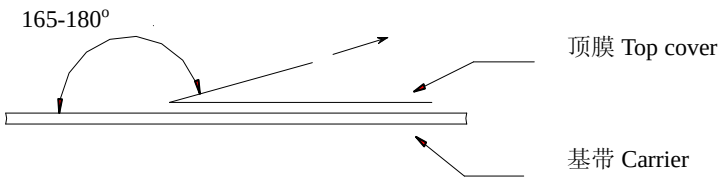
③Part of the leader and part of the empty tape shall be attached to the end of the tape as follows.

④Missing capacitors number within 0.1% of the number per reel or 1pc, whichever is greater, and are not continuous.

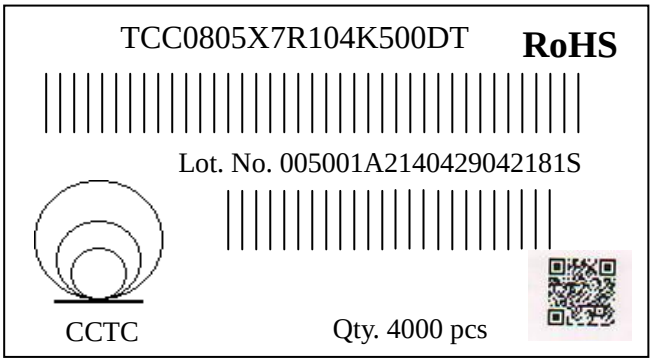
⑤The top tape and bottom tape shall not protrude beyond the edges of the tape and shall not cover sprocket holes.

⑥Cumulative tolerance of sprocket holes, 10 pitches: ±0.3mm.

⑦Peeling off force: 0.1 to 0.6N in the direction shown down.



8.2.6 产品标签 Reel Label



标签内容 The Contents of Label

(1) TCC 0805 X7R 104 K 500 D T

① ② ③ ④ ⑤ ⑥ ⑦ ⑧

①陶瓷电容器代号 Code of Ceramic Capacitor

②尺寸 chip size,③温度特性 dielectrics,④容量 capacitance,⑤容量容差 tolerance,

⑥额定电压 rated voltage,⑦厚度 thickness, ⑧包装 packing

(2) 产品批号 Lot. No.: 005001A2140429042181S

(3) 数量 Qty: 4000pcs

(4) RoHS:GREEN PARTS 绿色物料

8.2.7.外包装 Package

8.2.7.1 包装箱 Carton

8.2.7.1.1 包装箱尺寸 Carton Size

L	W	H
40cm	36cm	20cm

8.2.7.1.2 数量： 200Kpcs /箱 The Quantity: 200Kpcs /one carton

1 内包装盒=20,000PCS 1 INNER BOX=20,000PCS

1 包装箱 =20,000PCS ×10 包装盒=200,000PCS 1 CARTON=20,000PCS ×10BOX=200,000PCS

RoHS 标识(根据客户要求张贴) according to customer request

8.2.7.2 内包装盒 Inner Box

8.2.7.2.1 包装盒尺寸 Size

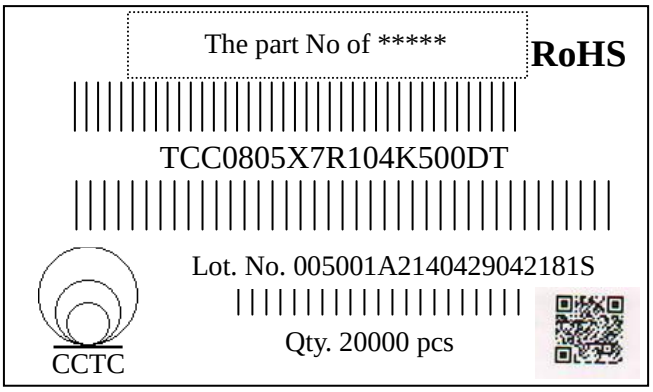
L	W	H
18.5cm	6.5cm	19cm

8.2.7.2.2 数量：20Kpcs /盒

1 盘=4000PCS 1 REEL=4,000PCS

1 包装盒=4,000PCS ×5 盘 =20,000PCS 1 INNER BOX=4,000PCS ×5 REEL =20,000PCS

8.2.7.2.3 内包装盒标签 The Label in Inner Box



9.MLCC 使用注意事项 Precautions on the use of MLCC

9.1 电路板设计 PCB Design

9.1.1 电路板图案设计 Design of Land-patterns

下面图和表格给出了部分推荐的设计图案，可以防止安装时焊锡量过多。

同时也给出了不正确的图案。

The following diagrams and tables show some examples recommended patterns to prevent excessive solder amounts (larger fillets which above the component end terminations)

Examples of improper pattern designs are also shown.

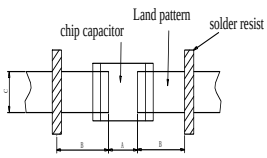
电路板设计推荐图案尺寸：

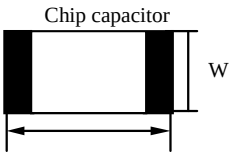
Recommended land dimensions for a typical chip capacitor land patterns for PCBs

波峰焊接时推荐设计的尺寸 (单位: mm)：

Recommended land dimensions for wave-soldering (unit: mm)

规格 SIZE		0603	0805	1206
尺寸	L	1.6	2.0	3.2
	W	0.8	1.25	1.6
A		0.8~1.0	1.0~1.4	1.8~2.5
B		0.5~0.8	0.8~1.5	0.8~1.7
C		0.6~0.8	0.9~1.2	1.2~1.6





再流焊接时推荐设计的尺寸 (单位: mm)

Recommended land dimensions for reflow-soldering (unit: mm)

规格 SIZE		0603	0805	1206
尺寸	L	1.6	2.00	3.2
	W	0.8	1.25	1.6
A		0.6~0.8	0.8~1.2	1.8~2.5
B		0.6~0.8	0.8~1.2	1.0~1.5
C		0.6~0.8	0.9~1.6	1.2~2.0

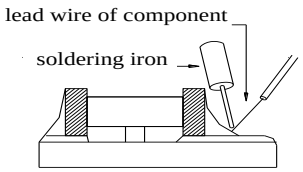
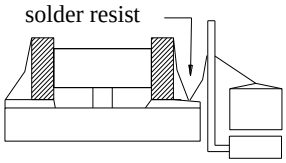
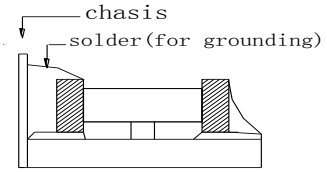
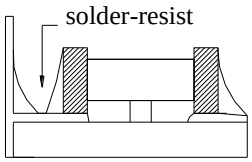
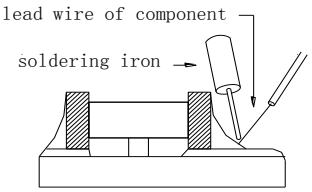
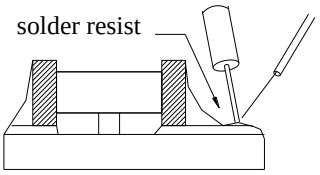
过量的焊锡会影响产品抵抗机械应力的能力，因此在设计图案时应引起注意。

Excess solder can affect the ability of chips to withstand mechanical stresses. Therefore, please take proper precautions when designing land-patterns.

在应用中一些焊接好与坏的情况：

Examples of good and bad solder application

产 品 标 准 书 SPECIFICATIONFOR APPROVAL	编 号 Document No. DRAAW108D/0-2014

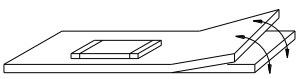
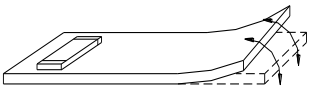
项目 Item	不推荐结构 Not recommended	推荐结构 Recommended
片状元件和带引线的元件的混合焊接 Mixed mounting of SMD and leaded component		
靠近底座的焊接 Component placement close to the chassis		
在片状元件附近带引线元件的焊接 Hand-soldering of leaded components near mounted components		

9.1.2 图案结构

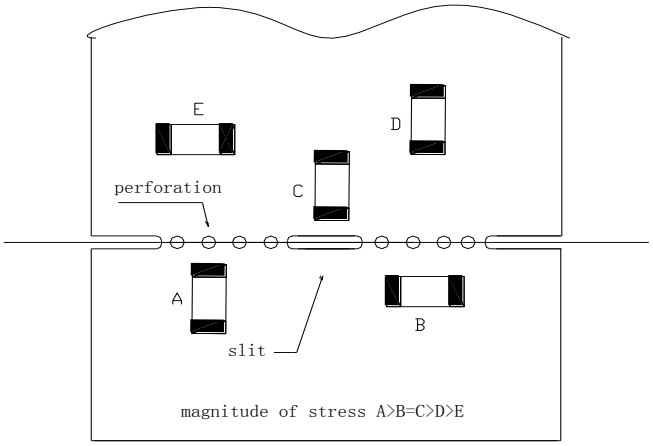
Pattern configurations

下面是电容器安装好与坏的例子。选择贴装位置，应尽可能减小电路板在弯曲时受到的机械应力。

The following are examples of good and bad capacitor layout, SMD capacitors should be located to minimize any possible mechanical stresses from board warp or deflection..

	不推荐结构 Not recommended	推荐结构 Recommended
电路板弯曲 Deflection of the board		

对于电路板分拨的电容器，在分拨时受到的机械应力大小与电容器的安装有关。下面推荐了一些好的设计。
To layout the capacitors for the breakaway PC board, it should be noted that the amount of mechanical stresses given depending on capacitor layout. The example below shows recommendations for better design.



在沿着分拨线分拨电路板时，对产品施加的机械应力与使用的方法关系很大。分折电路板时片状元件受到的疲劳按照如下顺序增大：分折、剪切、V 型槽、穿孔。因此，贴装时应该考虑电路板的分拨过程。

When breaking PC boards along their perforations, the amount lf mechanical stress on the capacitors can vary according to the method used. The following methods are listed in order from least stressful to most stressful: push-back, silt, -grooving, and perforation. Thus, any ideal SMD capacitor layout must also consider the PCB splitting procedure.

9.2 自动贴装注意事项 Considerations for automatic placement

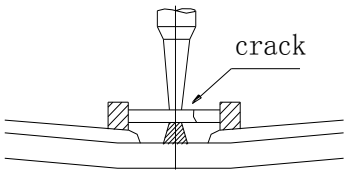
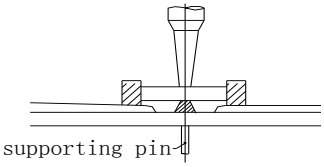
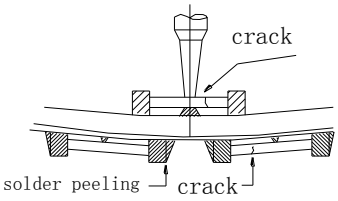
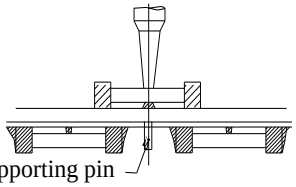
贴装机的调整 Adjustment of mounting machine

①. 产品在电路板贴装时，不应该受到过大的冲击。

②. 必须定期对吸头和定位爪进行检查、维修和更换

①. Excessive impact load should not be imposed on the capacitors when mounting the PC boards.

②. The maintenance and inspection of the mounters should be conducted periodically.

	不推荐结构 Not recommended	推荐结构 Recommended
单面贴装 Single-sided mounting		
双面贴装 Double-sided mounting		

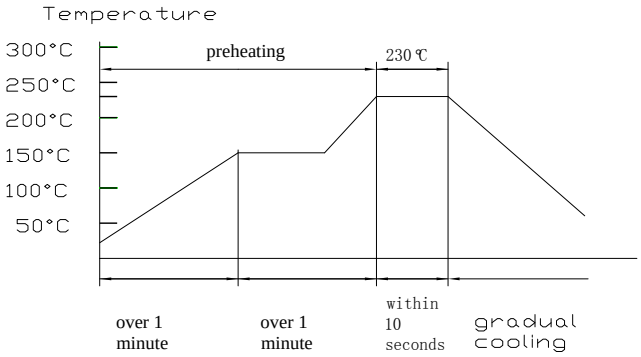
9.3 推荐焊接曲线 Recommended soldering profile

9.3.1 说明：① 产品推荐使用回流焊接工艺；② 大尺寸产品适用于回流焊接工艺

9.3.1 Re：①flow Soldering is recommended；②flow soldering is suitable for bigger size MLCCs

9.3.2 锡铅焊接曲线 Recommended Sn&Pb soldering profile

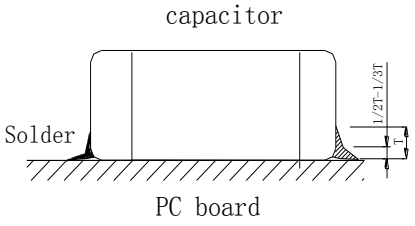
再流焊 Reflow soldering



注意 Caution

①. 理想状况的焊锡高度为电容器厚度的 1/2 ~1/3，如下图所示：

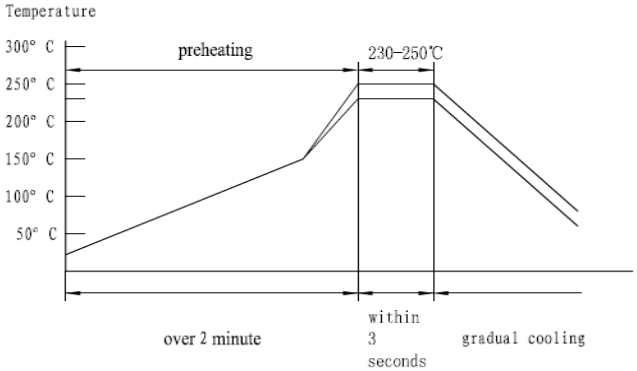
①.The ideal condition is to have solder mass (fillet) controlled to 1/2 to 1/3of the thickness of the capacitor, as shown below:



②. 过长的焊接时间会影响端头的可焊性，焊接时间尽可能保持与推荐时间一致。

②. Because excessive dwell times can detrimentally affect solderability, soldering duration should be kept as close to recommended times as possible.

波峰焊 Wave solder profile



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注意 Caution

①.确保电容器充分预热。

②.产品预热和焊接温度差不超过 100~130℃。

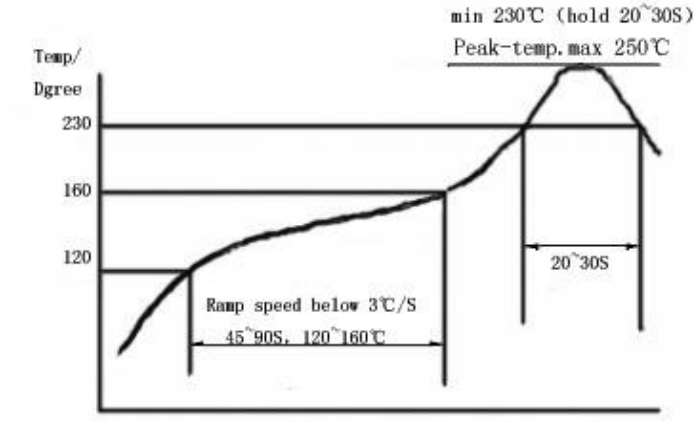
③.焊接后尽可能慢速冷却。

①.Make sure the capacitors are preheated sufficiently.

②.The temperature difference between the capacitor and melted solder should not be greater than 100 to 130℃.

③.Cooling after soldering should be gradual as possible.

手工焊接 Hand soldering



注意 Caution

①.用尖端最大直径 1.0mm 功率 20W 的焊接烙铁。

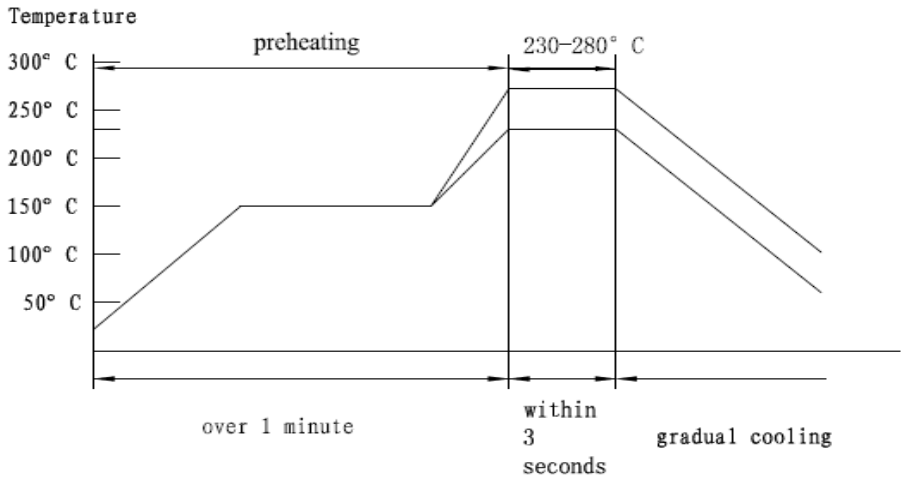
②.焊接烙铁不要直接接触产品。

①.Use a 20w soldering iron with a maximum tip diameter of 1.0mm.

②.The soldering iron should not directly touch the capacitor.

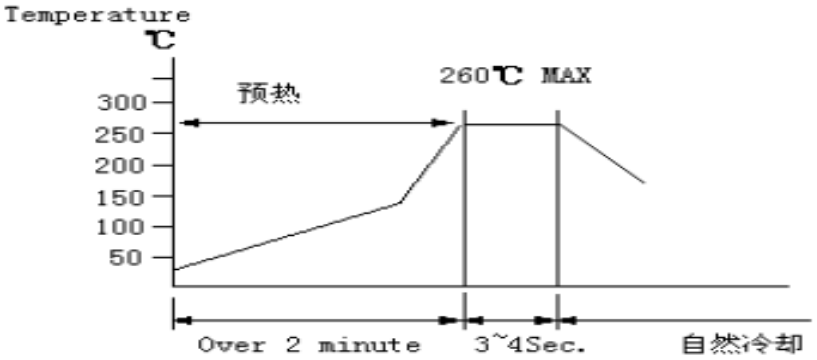
9.3.3 无铅焊接曲线 Recommended Pb-Free soldering profile

回流焊接 Reflow soldering



产 品 标 准 书	编 号 Document No.
SPECIFICATIONFOR APPROVAL	DRAAW108D/0-2014

波峰焊接 Wave solder profile



9.4 分拨电路板 Handling

Breakaway PC boards (splitting along perforations)

⑴.在电容器或其它贴装后，必须注意因电路板弯曲或变形带来的应力。

⑵.分拨电路板时必须使用专用的夹具，不可以用手拨断。

⑴.When splitting the PC board after mounting capacitors and other components, care is required so as not to give any stresses of deflection or twisting to the board.

⑵.Board separation should not be done manually, but by using the appropriate devices.

9.5 保存 Storage

⑴.在下列环境中保存产品：温度 5~40℃；湿度 ≤70% RH

⑵.产品自生产之日保存期为一年，产品使用之前请勿拆开编带。

⑶.编带拆开后，产品应在三个月内使用。

⑷.高介电常数电容器的容值随时间会逐渐减小，所以在电路设计时应充分考虑这一现象。容值减小的电容器在150℃热处理 1 小时后容值会恢复到初试值。

⑴. Keep the storage environment conditions as following:

Temperature: 5~40℃

Humidity: ≤70% RH

⑵. Don't open the tape until the parts are to be used, and store them within one year since the date printed on the reel.

⑶. Use the chips within 3 months after the tape is opened.

⑷. The capacitance value of high dielectric constant capacitors will gradually decrease with the passage of time, so this should be taken into consideration in the circuit design. If such a capacitance reduction occurs, a heat treatment of 150℃ for 1 hour will return the capacitance to its initial level.