1. DESCRIPTION

The XA2188-AS79 is a high performance L, S-band SPDT (Single Pole Double Throw) switch for mobile phone and other L, S- band applications. This device operates with dual control voltages of 1.7 V to 3.3V.This device can operate from 0.1 to 3.0 GHz, with low insertion loss and high isolation.This device is housed in a 6- pin SOT363 package, and is suitable for high- density surface mounting.

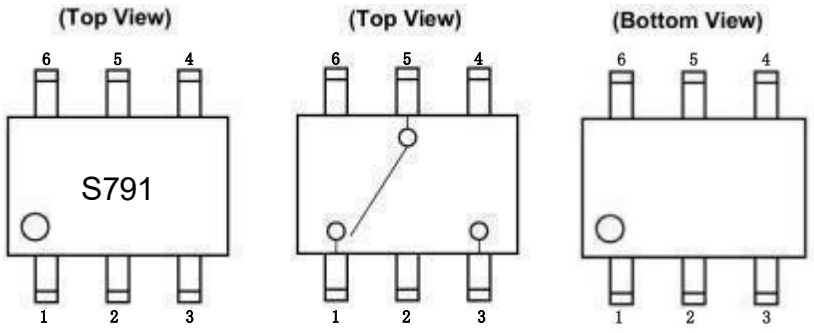
2. FEATURES

- Switch control voltage : Vcont (H) = 1.7 V to 3.3 V (3.0 V TYP.)
  : Vcont (L) = -0.2 V to +0.2 V (0 V TYP.)
- Low insertion loss : Lins1 = 0.28 dB TYP. @ f = 0.1 to 1.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
  : Lins2 = 0.28dB TYP. @ f = 1.0 to 2.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
  : Lins3 = 0. 30dB TYP. @ f = 2.0 to 2.5 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
  :Lins4 = 0.35dB TYP. @ f = 2.5 to 3.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
- High isolation : ISL1 = 22dB TYP. @ f = 0.05 to 2.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
  : ISL2 = 15dB TYP. @ f = 2.0 to 3.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
- Power handling : Pin (0.1 dB) = +27.0 dBm TYP. @ f = 0.5 to 3.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
  : Pin (1 dB) = +30.0 dBm TYP. @ f = 0.5 to 3.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
- High-density surface mounting : 6-pin super minimold package (SOT363 , 2.0 × 1.25 × 0.9 mm)

3. APPLICATIONS

- L, S-band digital cellular or cordless telephone
- PCS, W- LAN, WLL and Bluetooth™ etc.

4. PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name
1	OUTPUT1
2	GND
3	OUTPUT2
4	V_{cont2}
5	INPUT
6	V_{cont1}

5. TRUTH TABLE

Vcont1	Vcont2	INPUT-OUTPUT1	INPUT-OUTPUT2
Low	High	ON	OFF
High	Low	OFF	ON

6. ABSOLUTE MAXIMUM RATINGS (T_A = +25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Switch Control Voltage	V_{cont}	4.0[Note]	V
Input Power	P_{in}	+32	dBm
Operating Ambient Temperature	T_A	-45 to +85	℃
Storage Temperature	T_{stg}	-55 to +150	℃

Note | V_{cont1} - V_{cont2} | ≤ 4.2 V

7. RECOMMENDED OPERATING RANGE (T_A = +25°C, unless otherwise specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Switch Control Voltage (H)	$V_{cont\ (H)}$	1.8	3.0	3.3	V
Switch Control Voltage (L)	$V_{cont\ (L)}$	-0.2	0	0.2	V

8. ELECTRICAL CHARACTERISTICS

(T_A = +25°C, $V_{cont\ (H)}$ = 3.0 V, $V_{cont\ (L)}$ = 0 V, DC blocking capacitors = 100 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss 1	L_{ins1}	f = 0.1 to 1.0 GHz Note1	—	0.28	0.45	dB
Insertion Loss 2	L_{ins2}	f = 1.0 to 2.0 GHz	—	0.28	0.40	dB
Insertion Loss 3	L_{ins3}	f = 2.0 to 2.5 GHz	—	0.30	0.40	dB
Insertion Loss 4	L_{ins4}	f = 2.5 to 3.0 GHz	—	0.35	0.70	dB
Isolation 1	ISL1	f = 0.1 to 2.0 GHz Note1	27	31	—	dB
Isolation 2	ISL2	f = 2.0 to 3.0 GHz	20	27	—	dB
Input Return Loss	RL_{in}	f = 0.1 to 3.0 GHz Note1	12	32	—	dB
Output Return Loss	RL_{out}	f = 0.1 to 3.0 GHz Note1	15	32	—	dB
0.1 dB Loss Compression Input Power Note2	$P_{in\ (0.1\ dB)}$	f = 2.0 GHz	+23	+27.0	—	dBm
		f = 2.5 GHz	+23	+27.0	—	dBm
Switch Control Current	I_{cont}	No signal	—	54	68	μA
Switch Control Speed	t_{SW}	50%CTL to 90/10%RF	—	50	500	ns

Note

1. DC blocking capacitor = 1000 pF at f = 0.05 to 0.5 GHz.

2. $P_{in\ (0.1\ dB)}$ is the measured input power level when the insertion loss increases 0.1 dB more than that of linear range.

9. STANDARD CHARACTERISTICS FOR REFERENCE

(T_A = +25°C, $V_{cont\ (H)}$ = 3.0 V, $V_{cont\ (L)}$ = 0 V, DC blocking capacitors = 100 pF, unless otherwise specified)

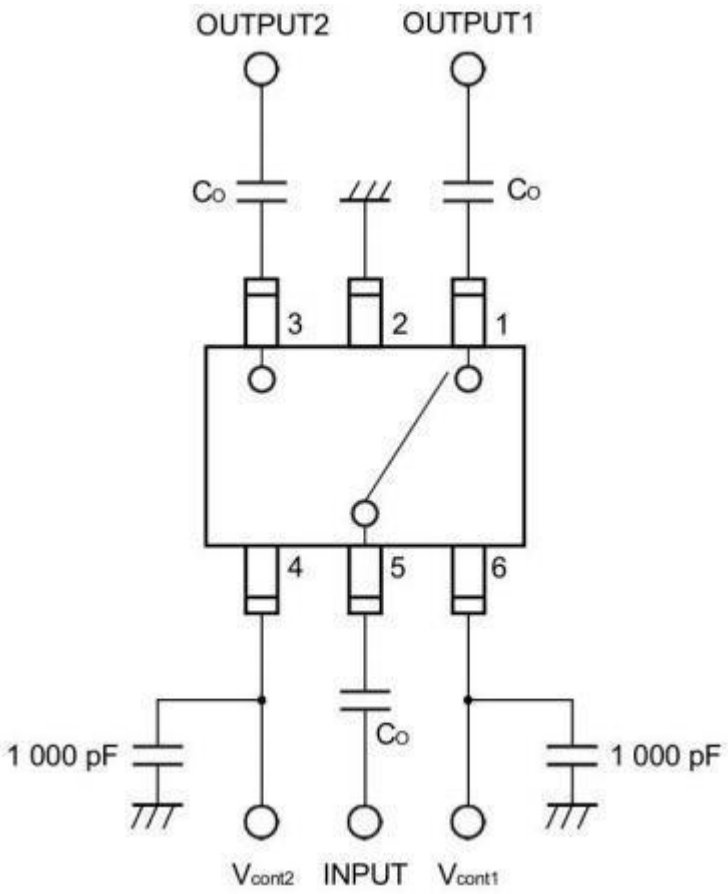
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
1 dB Loss Compression Input Power Note	$P_{in\ (1\ dB)}$	f = 0.5 to 3.0 GHz	—	+30.0	—	dBm
3rd Order Intermodulation Intercept Point	IIP3	f = 0.5 to 3.0 GHz, 2 tone,5 MHz spicing	—	+56.0	—	dBm

Note

Pin (1 dB) is the measured input power level when the insertion loss increases 1 dB more than that of linear range.

Caution When using this IC, a DC coupling capacitor must be externally attached to the I/O pins. A DC coupling capacitor with a capacitance of 100 pF or lower is recommended when using a frequency of 0. 5 GHz or higher, and one with a capacitance of 1, 000 pF is recommended when using a frequency of less than 0.5 GHz. The ideal value changes depending on the frequency and bandwidth used, so select a capacitor with a suitable capacitance according to the usage conditions.

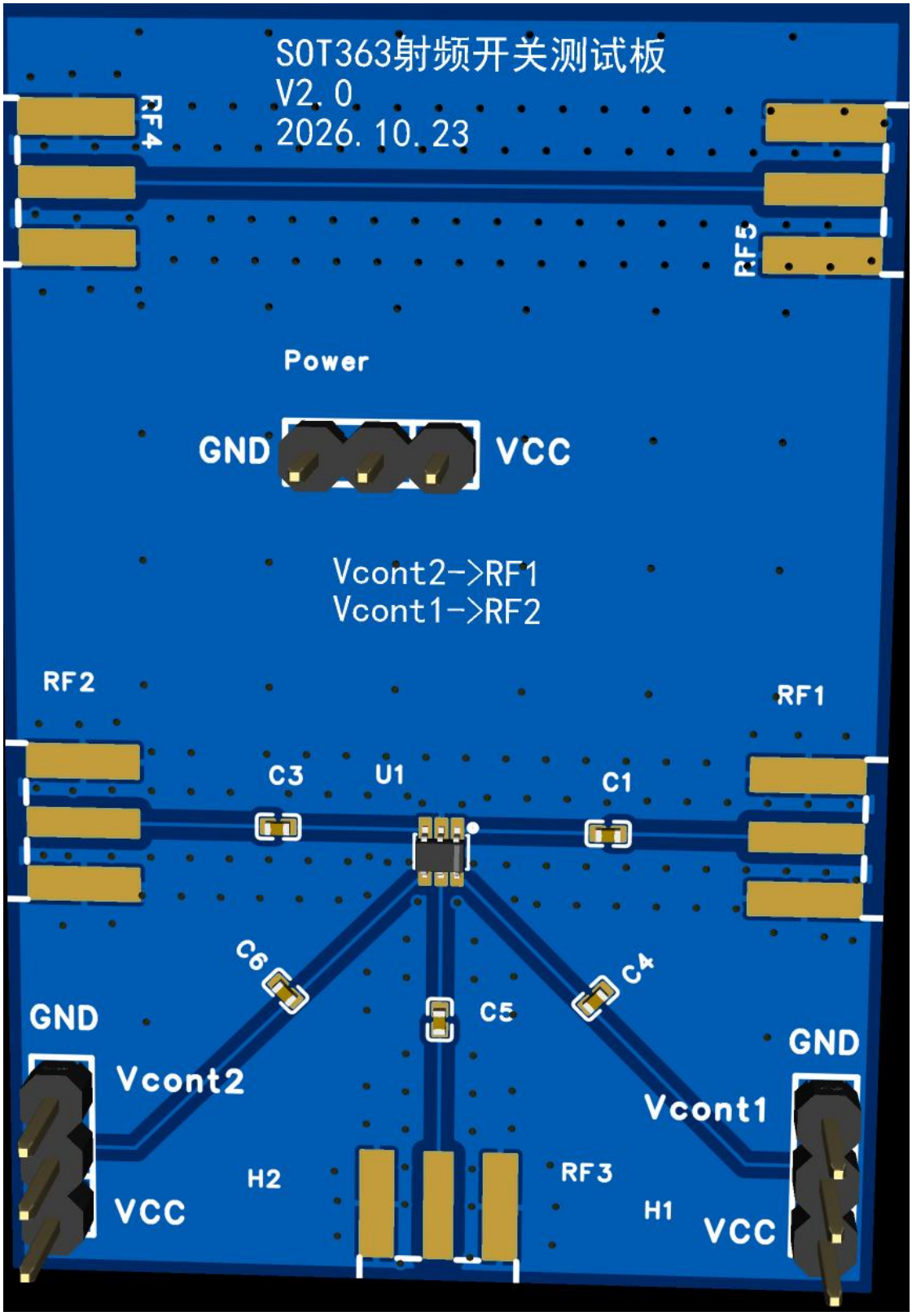
10. APPLICATION CIRCUIT



Remark CO : 0.1 to 0.5 GHz 1000 pF
0.5 to 3.0 GHz 100 pF

The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

11. ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD

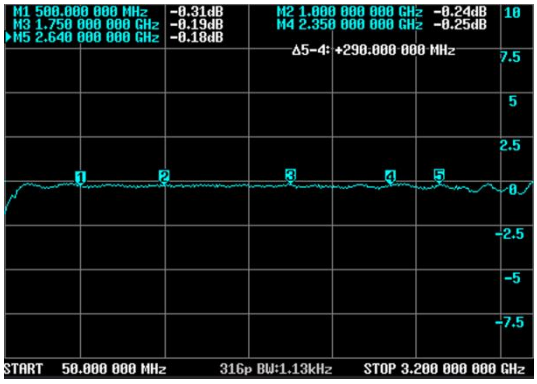


12. USING THE EVALUATION BOARD

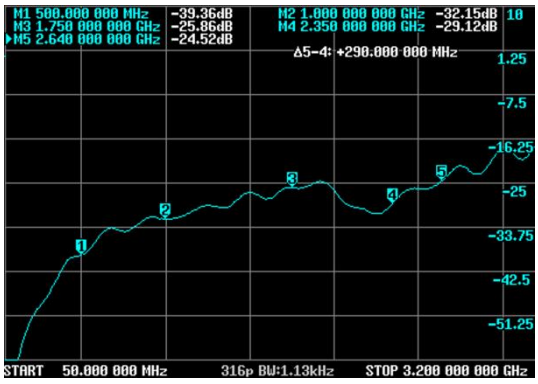
Symbol	Values
C1, C2, C3	100 pF
C4, C5	1000 pF

13. TYPICAL CHARAC TERISTICS

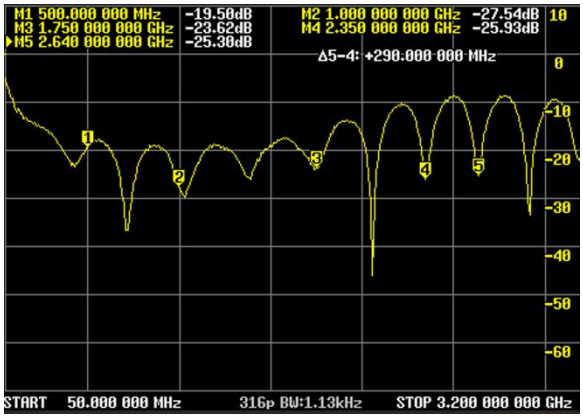
(T_A = +25°C, $V_{cont\ (H)}$ = 3.0 V, $V_{cont\ (L)}$ = 0 V, DC blocking capacitors = 100 pF, unless otherwise specified)



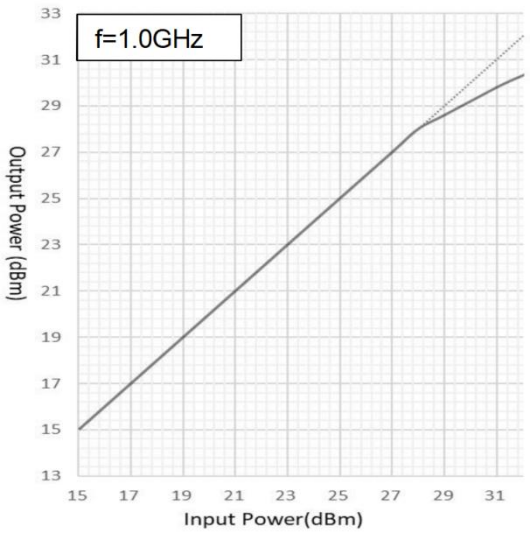
INPUT–OUTPUT INSERTION LOSS vs. FREQUENCY



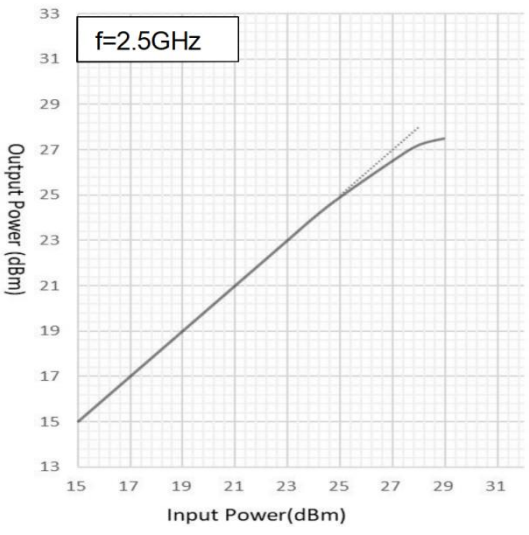
INPUT–OUTPUT ISOLATION vs. FREQUENCY



INPUT–OUTPUT RETURN LOSS vs. FREQUENCY



OUTPUT Power vs INPUT Power



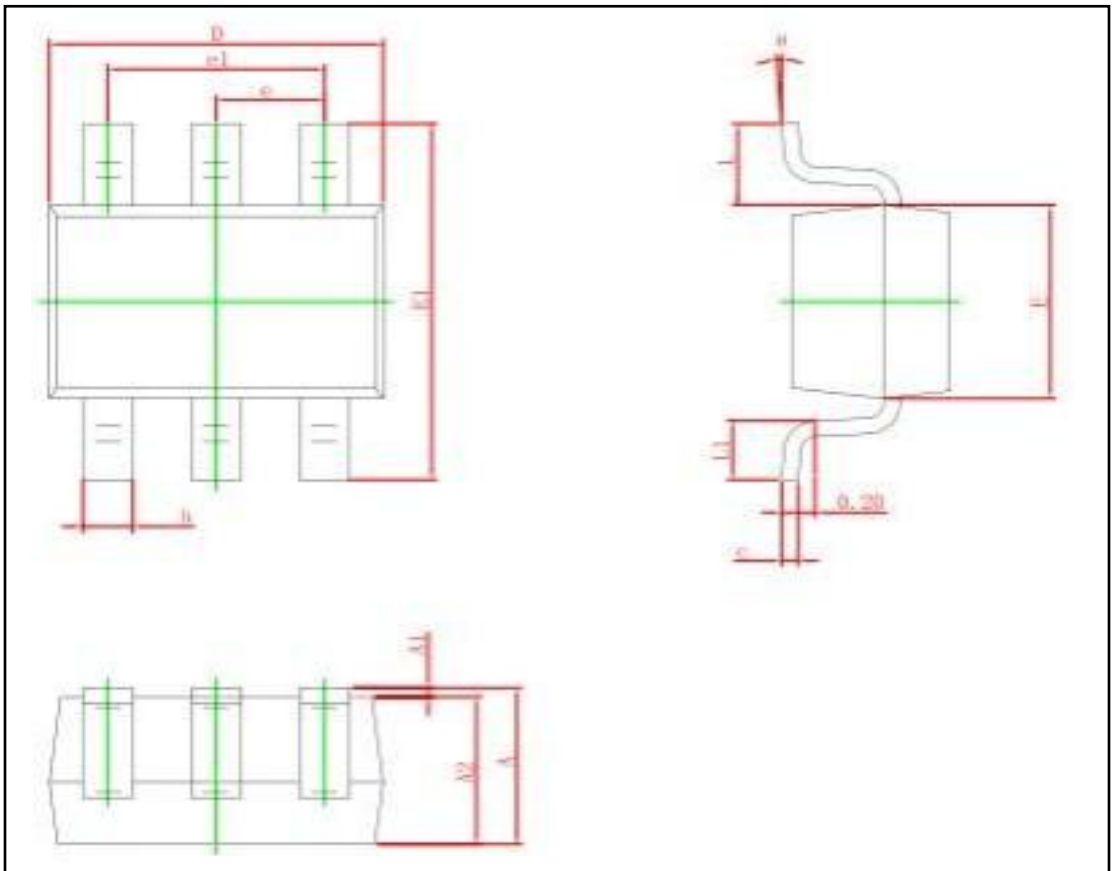
OUTPUT Power vs INPUT Power

14. ORDERING INFORMATION

Ordering Information

Part Number	Device Marking	Package Type	Body size (mm)	Temperature (°C)	MSL	Transport Media	Package Quantity
XA2188-AS79	S791	SOT363	2.10 * 1.22	- 40 to 85	MSL3	T&R	3000

15. DIMENSIONAL DRAWINGS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.110	0.175	0.004	0.007
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP.		0.026 TYP.	
e1	1.200	1.400	0.047	0.055
L	0.525 REF.		0.021 REF.	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°