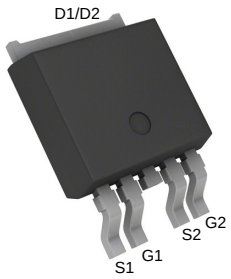
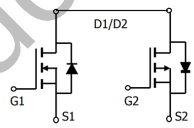
TO-252-4L

RoHS COMPLIANT





WGM023V04A

Features:

- ☐ Low Intrinsic Capacitances.
- ☐ Excellent Switching Characteristics.
- ☐ Extended Safe Operating Area.
- ☐ V_{DS} = 40V I_D =28 A
- ☐ $R_{DS(ON)}$ < 22mΩ @ V_{GS}=10V (Type: 17mΩ)
- ☐ V_{DS} = -40V I_D =-25A
- ☐ $R_{DS(ON)}$ < 24 mΩ @ V_{GS}=10V (Type: 20mΩ)

Absolute Maximum Ratings* (Tc=25℃ Unless otherwise noted)

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage	40	-40	V
V_{GS}	Gate-Source Voltage	±20	±20	V
I_D@T_C=25°C	Continuous Drain Current, V_{GS} @ 10V[1]	28	-25	A
I_D@T_C=100°C	Continuous Drain Current, V_{GS} @ 10V[1]	18	-16	A
I_{DM}	Pulsed Drain Current[2]	46	-40	A
EAS	Single Pulse Avalanche Energy[3]	28	66	mJ
I_{AS}	Avalanche Current	13.0	-19.0	A
P_D@T_C=25°C	Total Power Dissipation[4]	25	31.3	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	°C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	62		°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	5		°C/W

WildGoose Semiconductor

N-Channel **Electrical Characteristics (T_C=25℃unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	40	44	---	V
△BVDSS/△TJ	BVDSS Temperature Coefficient	Reference to 25℃ , I_D=1mA	---	0.034	---	V/℃
RDS(ON)	Static Drain-Source On-Resistance[2]	V_{GS}=10V , I_D=5A	---	17	22	mΩ
		V_{GS}=4.5V , I_D=4A	---	21	35	
VGS(th)	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	1.2	1.7	2.5	V
△$V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-4.56	---	mV/℃
IDSS	Drain-Source Leakage Current	V_{DS}=32V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=32V , V_{GS}=0V , T_J=55℃	---	---	5	
IGSS	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=5V , I_D=5A	---	14	---	S
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	2.6	---	Ω
Q_g	Total Gate Charge (4.5V)	V_{DS}=20V , V_{GS}=4.5V , I_D=5A	---	5.5	---	nC
Q_{gs}	Gate-Source Charge		---	1.25	---	
Q_{gd}	Gate-Drain Charge		---	2.5	---	
Td(on)	Turn-On Delay Time	V_{DD}=20V , V_{GS}=10V , R_G=3.3Ω I_D=1A	---	8.9	---	ns
T_r	Rise Time		---	2.2	---	
Td(off)	Turn-Off Delay Time		---	41	---	
T_f	Fall Time		---	2.7	---	
C_{iss}	Input Capacitance	V_{DS}=15V , V_{GS}=0V , f=1MHz	---	593	---	pF
Coss	Output Capacitance		---	76	---	
Crss	Reverse Transfer Capacitance		---	56	---	
I_S	Continuous Source Current[1,5]	V_G=V_D=0V , Force Current	---	---	6.1	A
ISM	Pulsed Source Current[2,5]		---	---	23	A
VSD	Diode Forward Voltage[2]	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1.2	V

Note :

1、The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper.

2、The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%

3、The EAS data shows Max. rating . The test condition is V_{DD}=50V,V_{GS}=10V,L=0.5mH,I_{AS}=13A

4、The power dissipation is limited by 150℃ junction temperature

5、The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

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P-Channel **Electrical Characteristics (T_C=25℃unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-40	-44	---	V
△BVDSS/△TJ	BV_{DSS} Temperature Coefficient	Reference to 25℃ , I_D=-1mA	---	-0.02	---	V/℃
RDS(ON)	Static Drain-Source On-Resistance[2]	V_{GS}=-10V , I_D=-6A	---	20	24	mΩ
		V_{GS}=-4.5V , I_D=-3A	---	25	29	
VGS(th)	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-1.2	-1.8	-2.5	V
△$V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	3.72	---	mV/℃
IDSS	Drain-Source Leakage Current	V_{DS}=-32V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=-32V , V_{GS}=0V , T_J=55℃	---	---	5	
IGSS	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=-5V , I_D=-6A	---	13	---	S
Q_g	Total Gate Charge (-4.5V)	V_{DS}=-20V , V_{GS}=-4.5V , I_D=-6A	---	11.5	---	nC
Q_{gs}	Gate-Source Charge		---	3.5	---	
Q_{gd}	Gate-Drain Charge		---	3.3	---	
Td(on)	Turn-On Delay Time	V_{DD}=-15V , V_{GS}=-10V , R_G=3.3Ω, I_D=-1A	---	22	---	ns
T_r	Rise Time		---	15.7	---	
Td(off)	Turn-Off Delay Time		---	59	---	
T_f	Fall Time		---	5.5	---	
C_{iss}	Input Capacitance	V_{DS}=-15V , V_{GS}=0V , f=1MHz	---	1415	---	pF
Coss	Output Capacitance		---	134	---	
Crss	Reverse Transfer Capacitance		---	102	---	
I_S	Continuous Source Current[1,5]	V_G=V_D=0V , Force Current	---	---	-6	A
ISM	Pulsed Source Current[2,5]		---	---	-22	A
VSD	Diode Forward Voltage[2]	V_{GS}=0V , I_S=-1A , T_J=25℃	---	---	-1.2	V

Note :

1、The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.

2、The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%

3、The EAS data shows Max. rating . The test condition is VDD=-50V,VGS=-10V,L=0.5mH,IAS=-19A

4、The power dissipation is limited by 150℃ junction temperature

5 、The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

WildGoose Semiconductor

Typical Characteristics

N-Channel Typical Characteristics

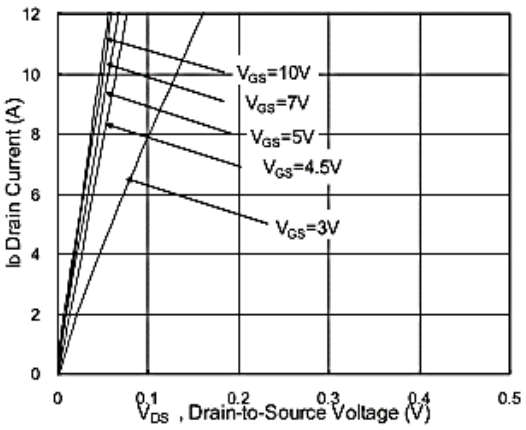


Fig.1 Typical Output Characteristics

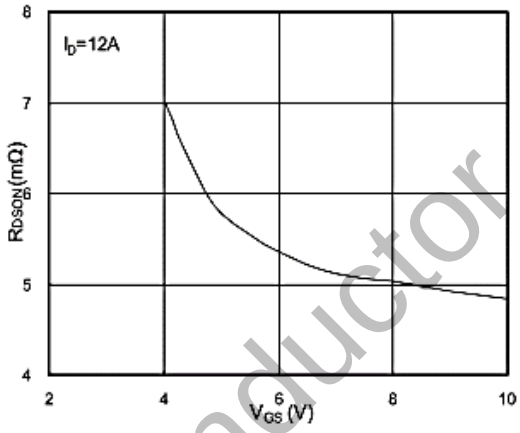


Fig.2 On-Resistance vs. G-S Voltage

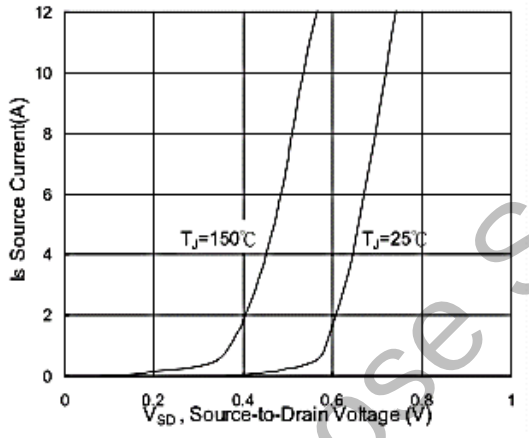


Fig.3 Forward Characteristics Of Reverse

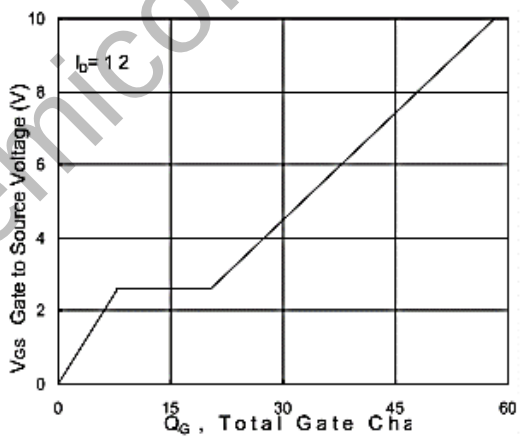


Fig.4 Gate-Charge Characteristics

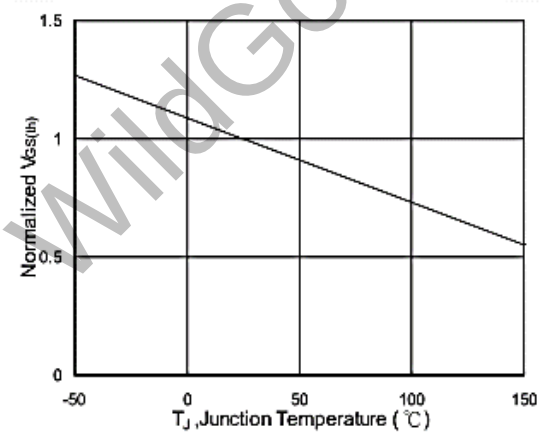


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

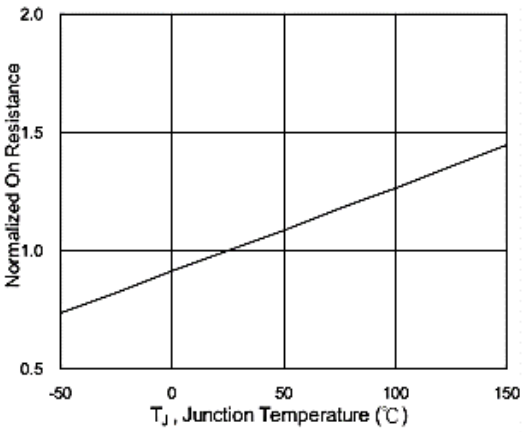


Fig.6 Normalized R_{DSON} vs. T_J

WildGoose Semiconductor

Typical Characteristics

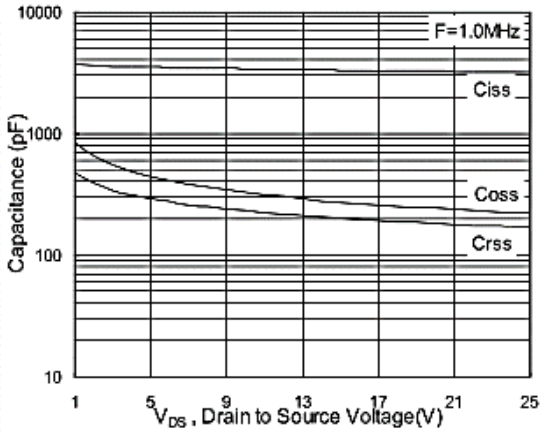


Fig.7 Capacitance

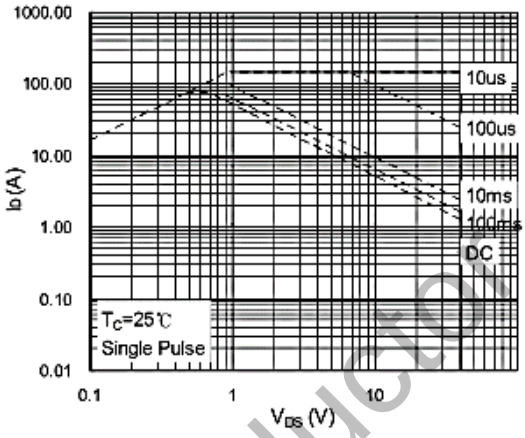


Fig.8 Safe Operating Area

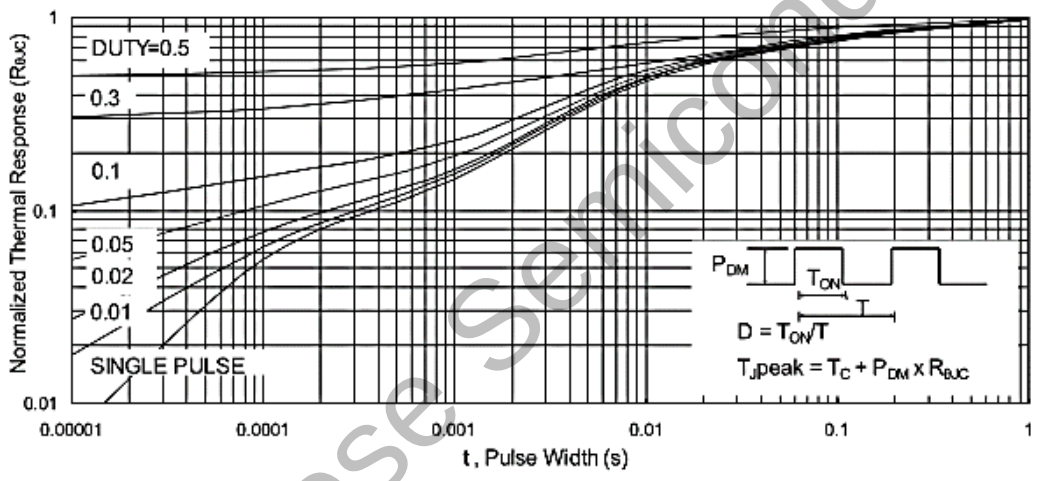


Fig.9 Normalized Maximum Transient Thermal Impedance

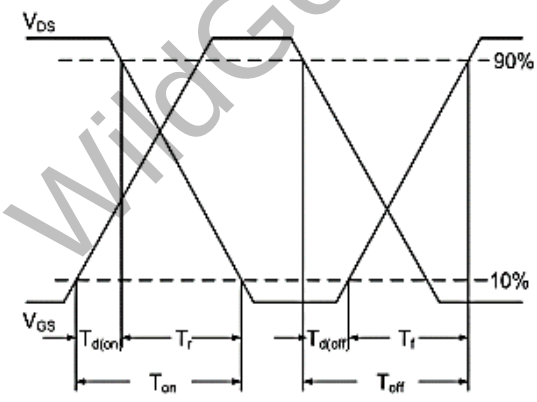


Fig.10 Switching Time Waveform

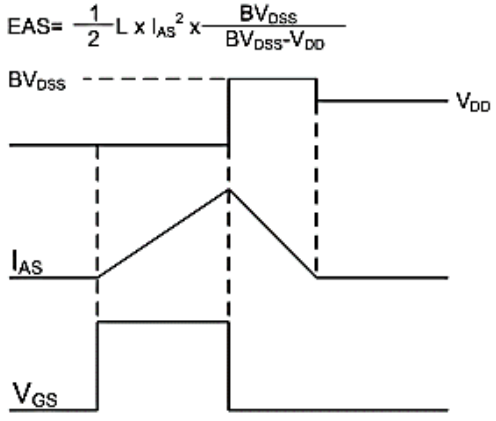


Fig.11 Unclamped Inductive Switching Wave

WildGoose Semiconductor

Typical Characteristics

P-Channel Typical Characteristics

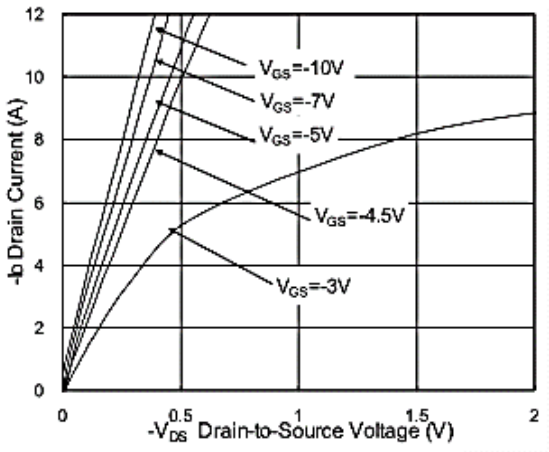


Fig.1 Typical Output Characteristics

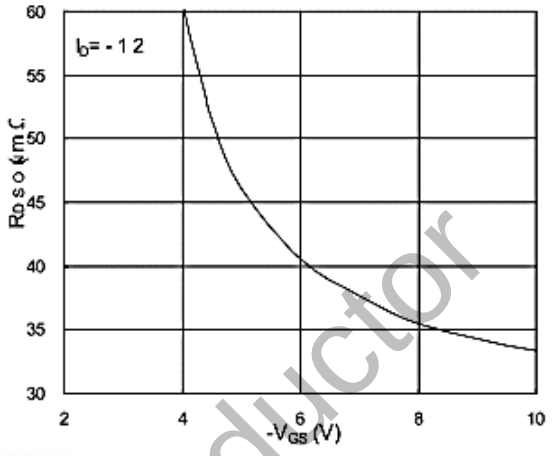


Fig.2 On-Resistance v.s Gate-Source

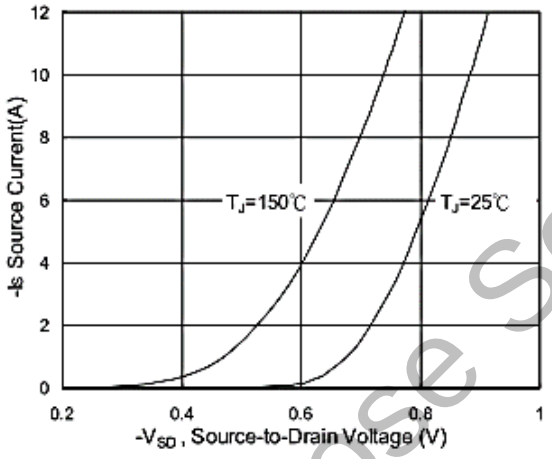


Fig.3 Forward Characteristics of Reverse

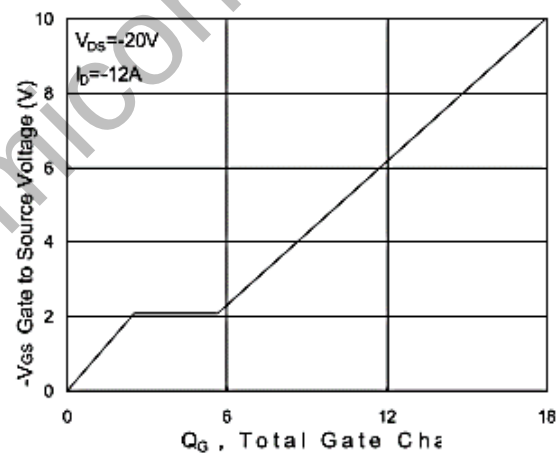


Fig.4 Gate-Charge Characteristics

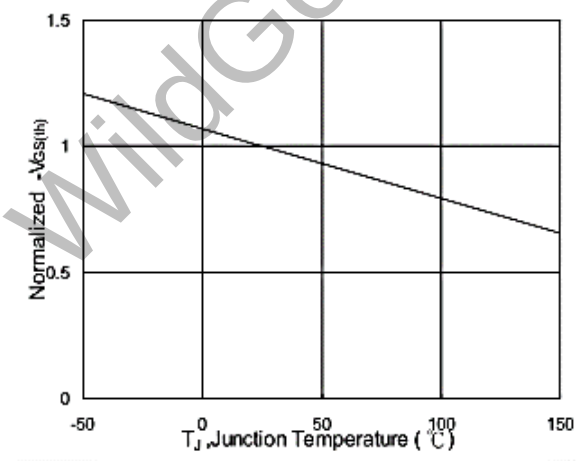


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

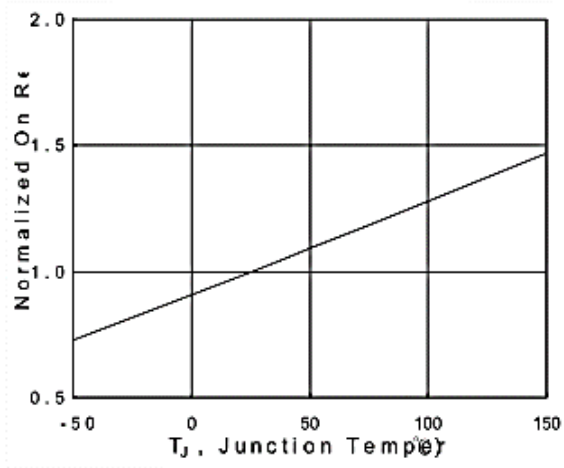


Fig.6 Normalized R_{DSON} v.s T_J

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Typical Characteristics

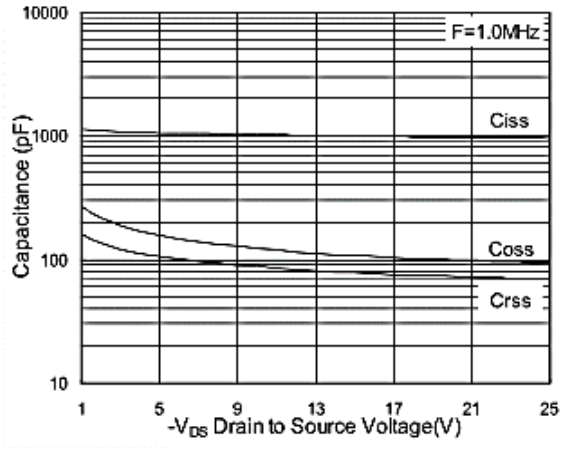


Fig.7 Capacitance

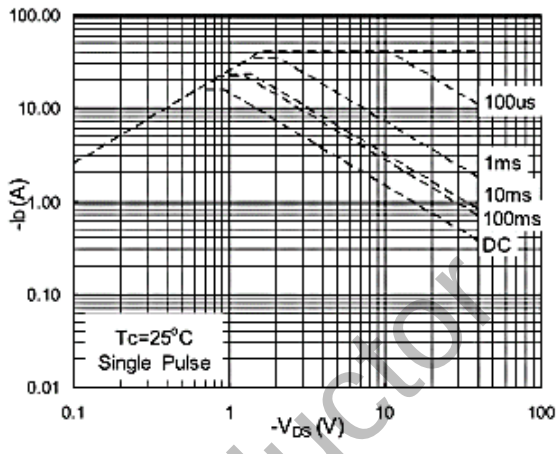


Fig.8 Safe Operating Area

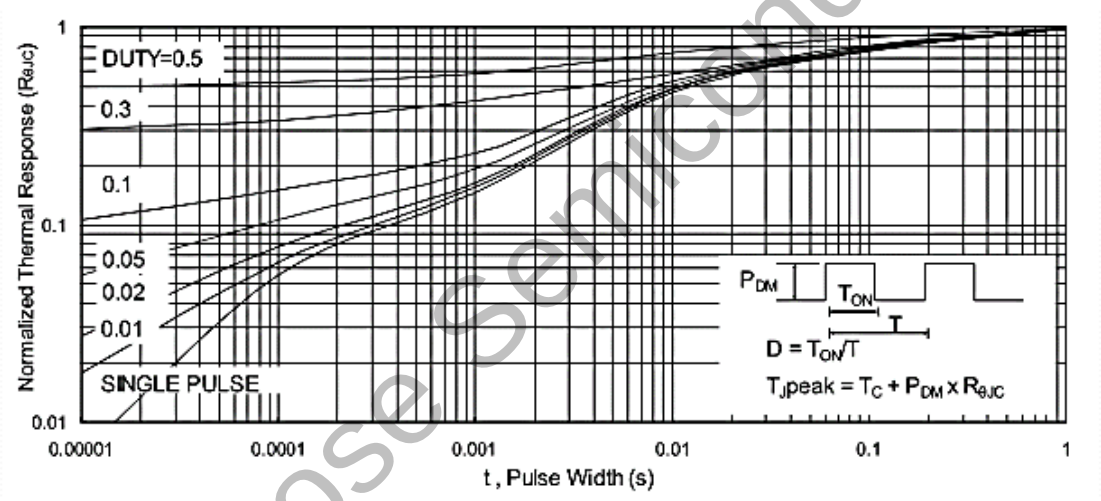


Fig.9 Normalized Maximum Transient Thermal Impedance

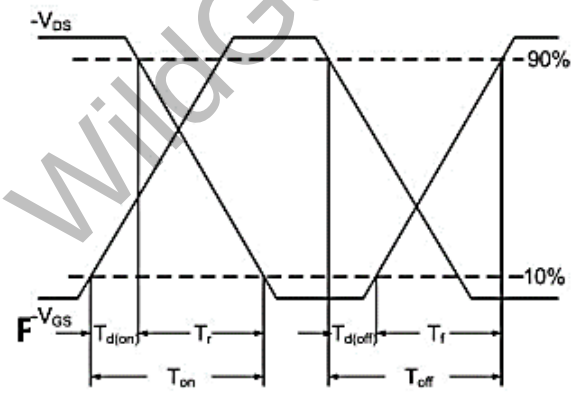


Fig.10 Switching Time Waveform

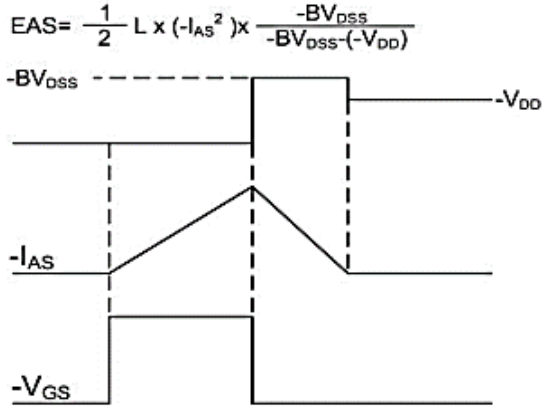


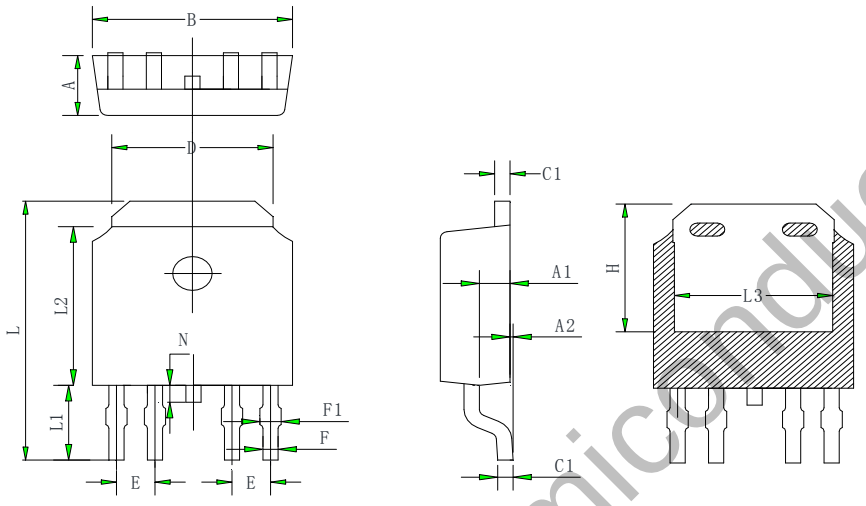
Fig.11 Unclamped Inductive Waveform

WildGoose Semiconductor

Package Dimension

TO-252-4L

Unit: mm



Symbol	Min	Typ	Max
A	2.20	2.30	2.40
A1	0.91	1.01	1.11
A2	0.05	0.15	0.25
B	6.45	6.60	6.75
C	0.45	0.50	0.58
C1	0.45	0.50	0.58
D	5.12	5.32	5.52
E		1.27 TYP	
F1	0.45	0.60	0.75
F	0.40	0.50	0.60
H	4.70	4.90	5.10
L	9.70	10.00	10.20
L1	2.6	2.8	3.0
L2	5.95	6.10	6.25
L3	5.00	5.20	5.40`
N	0.45	0.65	0.85

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