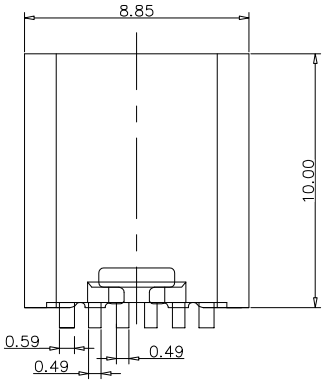
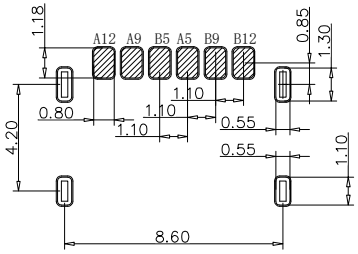
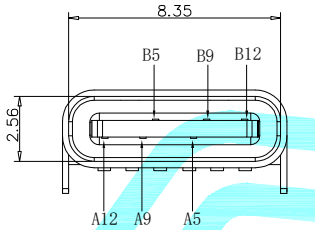
ROHS

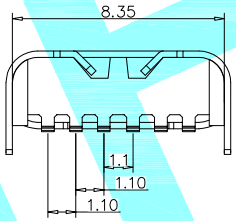
Http://www.krhro.com

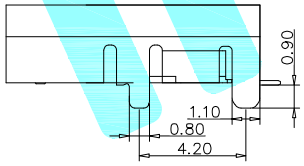


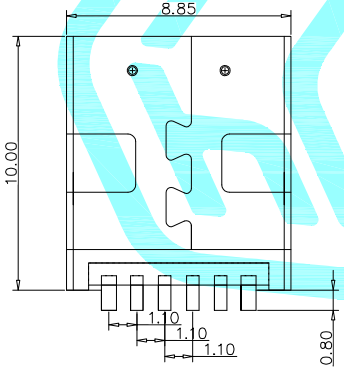


RECOMMEND P.C.B LAYOUT(COMPONENT SIDE)
TOLERANCE FOR PCB LAYOUT IS ±0.05









NOTE
1. Material:
1.1 Insulating core: LCP UL94 V-0
1.2 Terminal: C2680, T=0.2mm; nickel base, gold plated
1.3 Housing: Stainless Steel 201-H, nickel-plated surface

2. Electrical Properties:
2.1 Contact area: Contact resistance 40mΩ max, allowed change 10mΩ. Test conditions: measured at 20mV, 100mA.
2.2 Contact current: 3A for VBUS pin; 1.25A for GND pin; 0.25A for other pins.
2.3 Rated voltage: 5V
2.4 Withstand voltage: 100VAC
2.5 Insulation resistance: at least 100 MΩ

3. Mechanical Performance:
3.1 Insertion force: 0.5-2.0Kg
3.2 Extraction force: 0.6-2.04Kg
3.3 Number of insertions/removals: 5000 times

4. Operating Environment: -20℃ to 85℃

A5	CC1	B12	GND
A9	VBUS	B9	VBUS
A12	GND	B5	CC2
PIN	SIGNAL NAME	PIN	SIGNAL NAME

ECN NO.	REV.	DATE.	DESCRIPTION.	CHANGE.	CHECK.	APPRO.
	A		NEW			

APPROVALS		DATE
DRAWN	LIUCAN	2023.2.7
CHECKED		
APPROVALS		

TOLERANCES ARE: 30~以上 ±0.30; 10~30 ±0.25; 5~10 ±0.20; ~5 ±0.15; ANGLE ±2°

TITLE:	USB CONNECTOR
PART NO.	TYPE-C-31-M-50
UNIT: mm	SCALE: 1:1
DRAWING NO.	
PROJ:	SHEET 1 OF 1