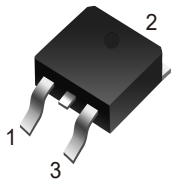
WGD120N04

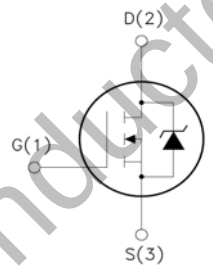
Features:

- ☐ Advanced Trench Technology
- ☐ Excellent Switching Characteristics
- ☐ Extended Safe Operating Area
- ☐ Ultra Low Gate Charge:Qg=147nC (Typ.)
- ☐ VDSS=40V, ID=120A
- ☐ Rds(on):3.4mΩ(Typ.) @VG=10V
- ☐ 100% Avalanche Tested

TO-252

RoHS COMPLIANT





1.Gate (G)
2.Drain (D)
3.Source (S)

Absolute Maximum Ratings (@ T_C = 25°C unless otherwise specified)

Symbol	Parameter		Value	Unit
V_{DS}	Drain-to-Source Voltage		40	V
V_{GS}	Gate-to-Source Voltage		±25	V
I_D	Continuous Drain Current	T_C = 25°C	120	A
		T_C = 100°C	86	
I_{DM}	Pulsed Drain Current (1)		Refer to Fig.4	A
E_{AS}	Single Pulsed Avalanche Energy (2)		711	mJ
P_D	Power Dissipation	T_C = 25°C	180	W
		T_C = 100°C	98	
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient(3)	50	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.4	

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = 250μA, V_{GS} = 0V	40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 40V, V_{GS} = 0V	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±25V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	1.0	1.8	2.5	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(4)	V_{GS} = 10V, I_D = 30A	-	3.4	4.6	mΩ
Dynamic Characteristics						
R_g	Gate Resistance	f = 1MHz	-	1.0	-	Ω
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = 20V, f = 1MHz	6797	9516	12847	pF
C_{oss}	Output Capacitance		687	962	1298	pF
C_{rss}	Reverse Transfer Capacitance		407	570	769	pF
Q_g	Total Gate Charge	V_{GS} = 0 to 10V V_{DS} = 20V, I_D = 30A	105	147	199	nC
Q_{gs}	Gate Source Charge		36	50	67	nC
Q_{gd}	Gate Drain("Miller") Charge		22	31	42	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = 10V, V_{DD} = 20V I_D= 30A, R_{GEN} = 2.7Ω	-	26	-	ns
t_r	Turn-On Rise Time		-	30	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	59	-	ns
t_f	Turn-Off Fall Time		-	19	-	ns
Body Diode Characteristics						
I_S	Maximum Continuous Body Diode Forward Current		-	-	190	A
I_{SM}	Maximum Pulsed Body Diode Forward Current		-	-	761	A
V_{SD}	Body Diode Forward Voltage	V_{GS} = 0V, I_S = 30A	-		1.2	V
trr	Body Diode Reverse Recovery Time	I_F = 30A, di/dt = 100A/us	23	33	44	ns
Qrr	Body Diode Reverse Recovery Charge		-	29	-	nC

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. E_{AS} condition: Starting T_J=25C, V_{DD}=20V, V_{GS}=10V, R_G=25ohm, L=0.5mH, I_{AS}=53.34A, V_{DD}=0V during time in avalanche.

3. $R_{\theta JA}$ is measured with the device mounted on a 1inch^2 pad of 2oz copper FR4 PCB.

4. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

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Typical Performance Characteristics

Figure 1: Power De-rating

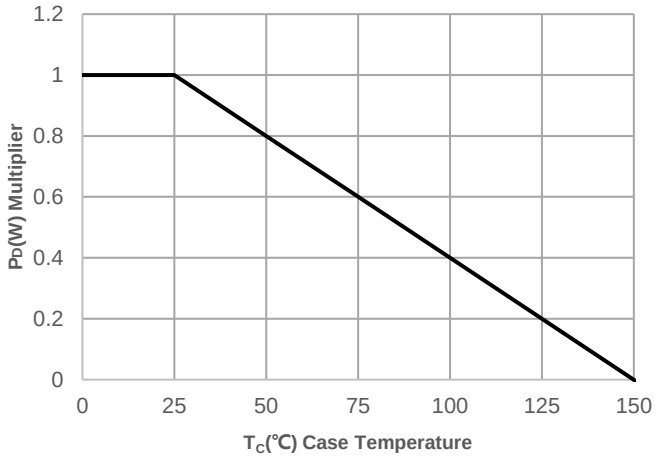


Figure 2: Current De-rating

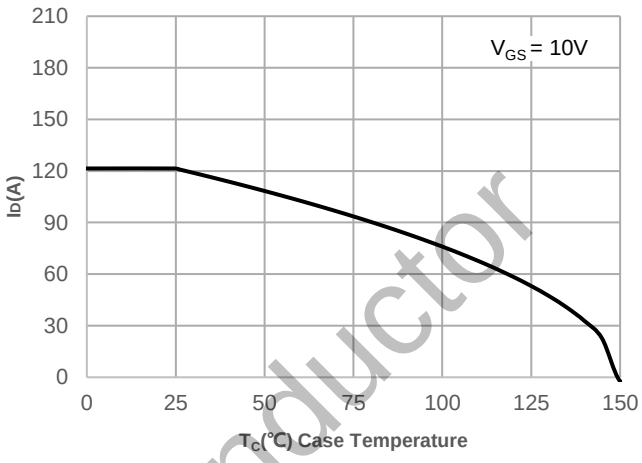


Figure 3: Normalized Maximum Transient Thermal Impedance

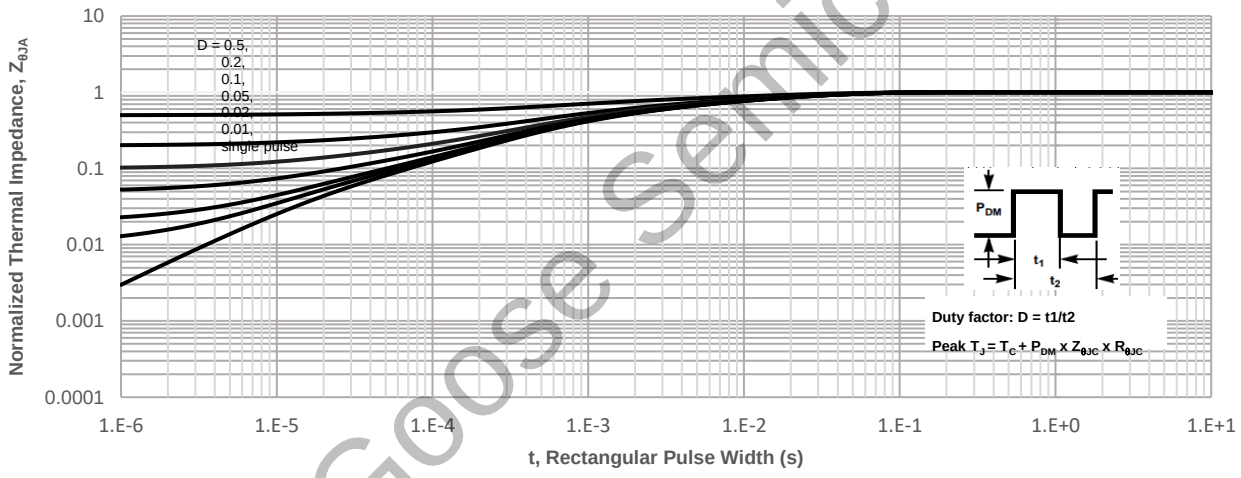
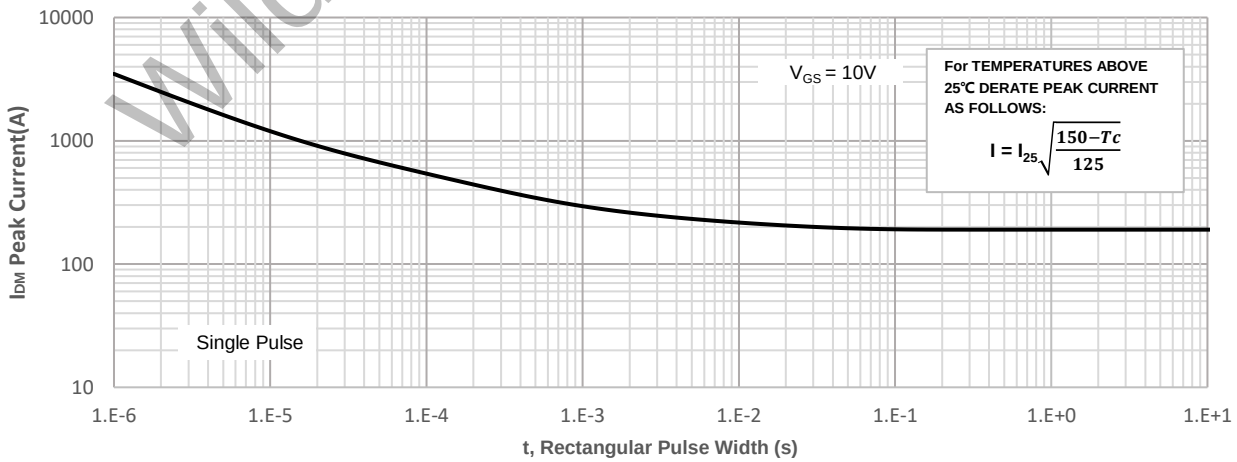


Figure 4: Peak Current Capacity



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Typical Performance Characteristics

Figure 5: Output Characteristics

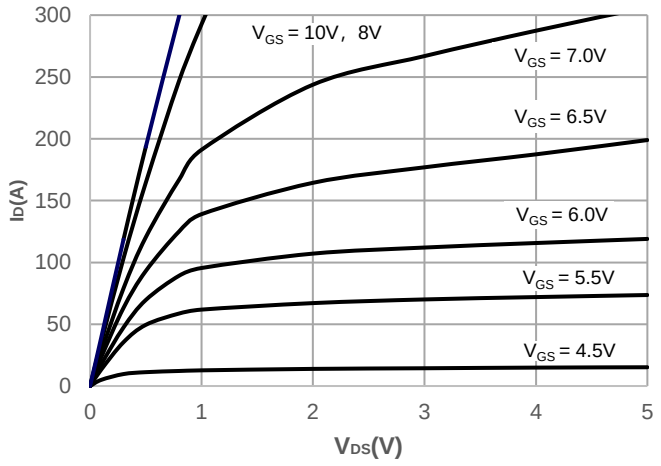


Figure 6: Typical Transfer Characteristics

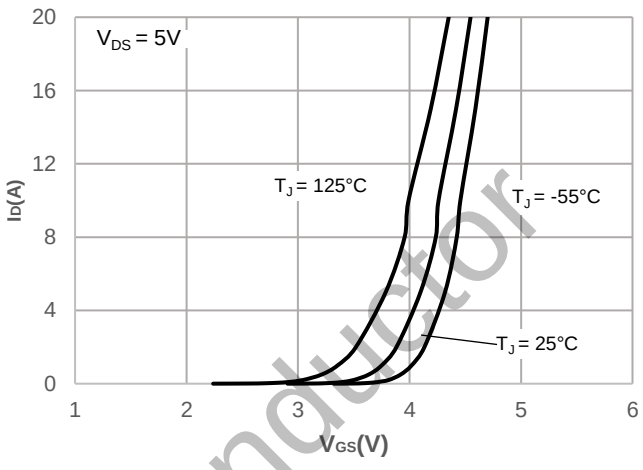


Figure 7: On-resistance vs. Drain Current

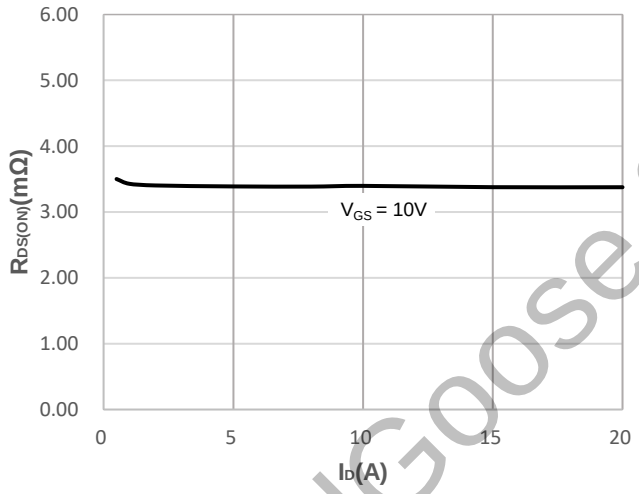


Figure 8: Body Diode Characteristics

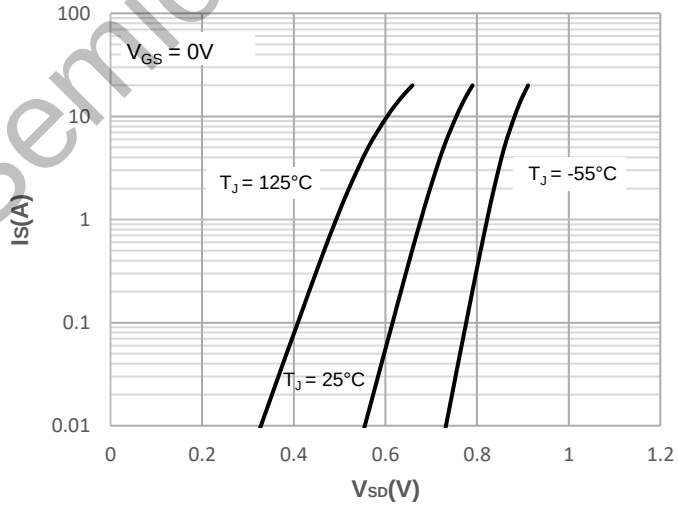


Figure 9: Gate Charge Characteristics

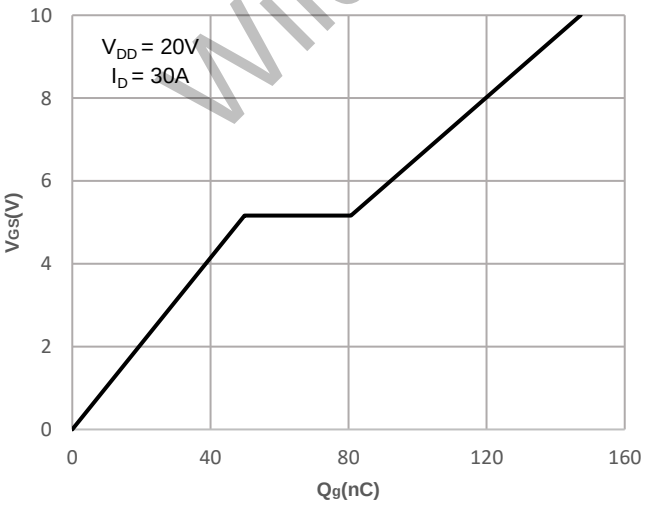
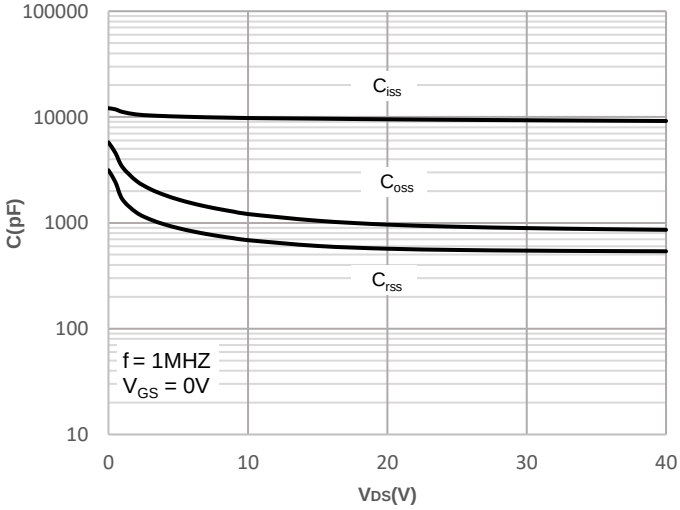


Figure 10: Capacitance Characteristics



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Typical Performance Characteristics

Figure 11: Normalized Breakdown voltage vs. Junction Temperature

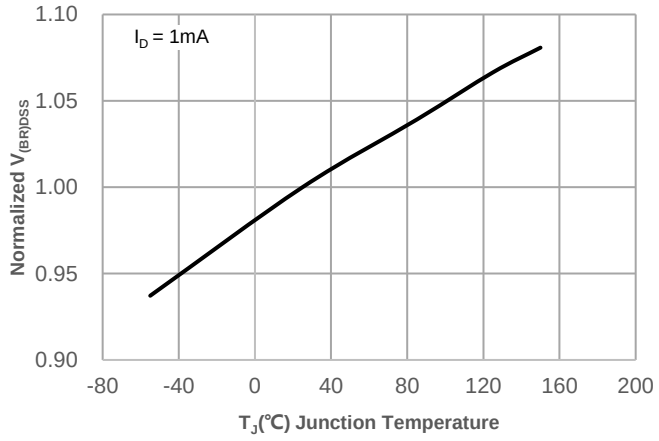


Figure 12: Normalized on Resistance vs. Junction Temperature

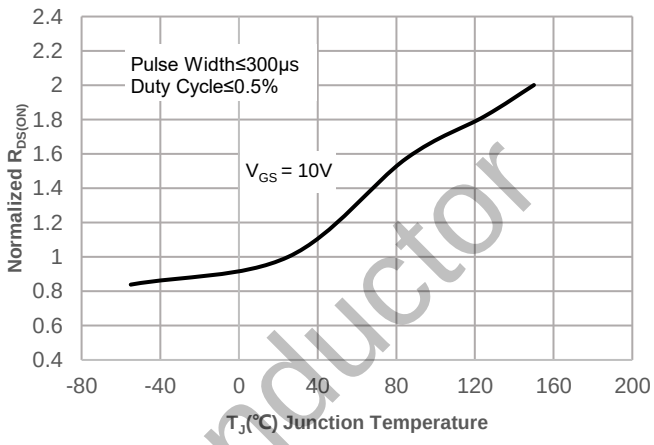


Figure 13: Normalized Threshold Voltage vs. Junction Temperature

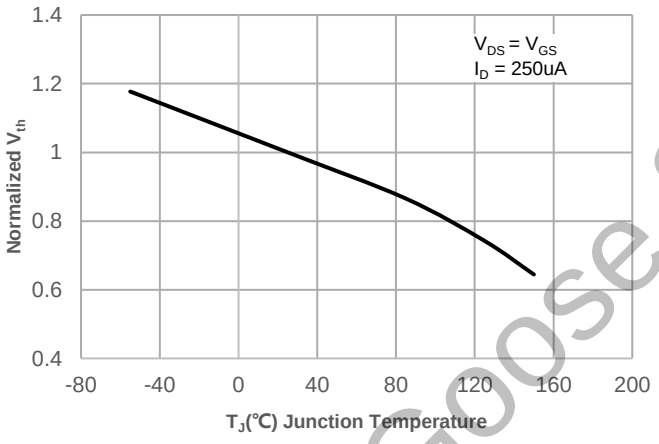


Figure 14: $R_{DS(ON)}$ vs. V_{GS}

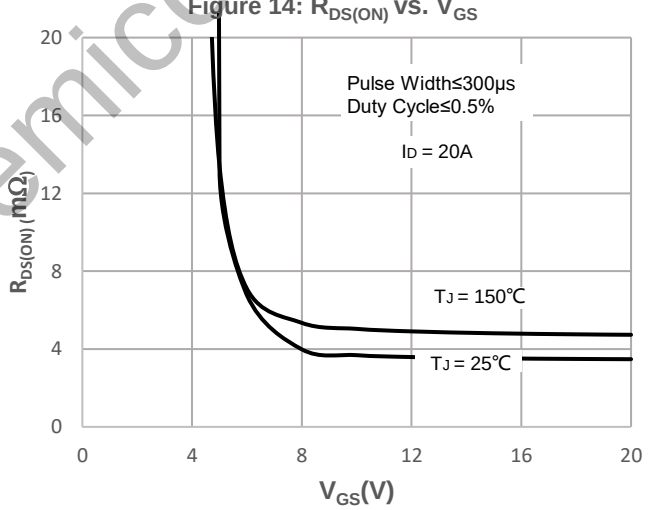
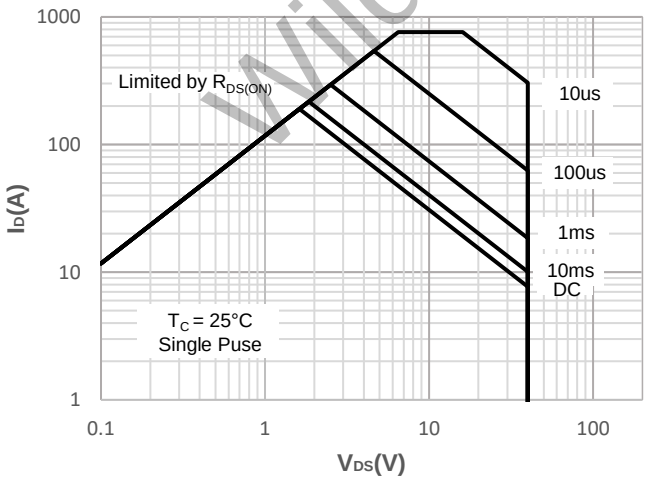


Figure 15: Maximum Safe Operating Area



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Test Circuit

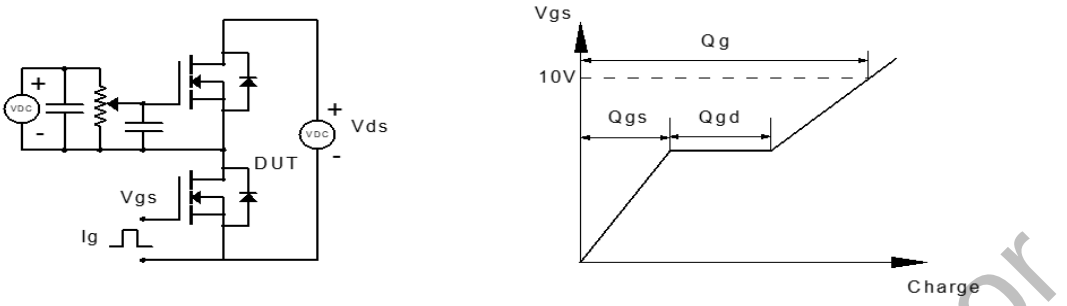

Figure 1: Gate Charge Test Circuit & Waveform

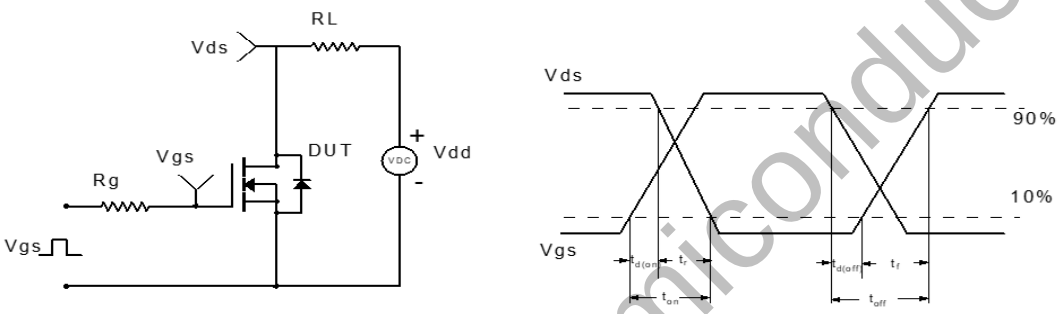

Figure 2: Resistive Switching Test Circuit & Waveform

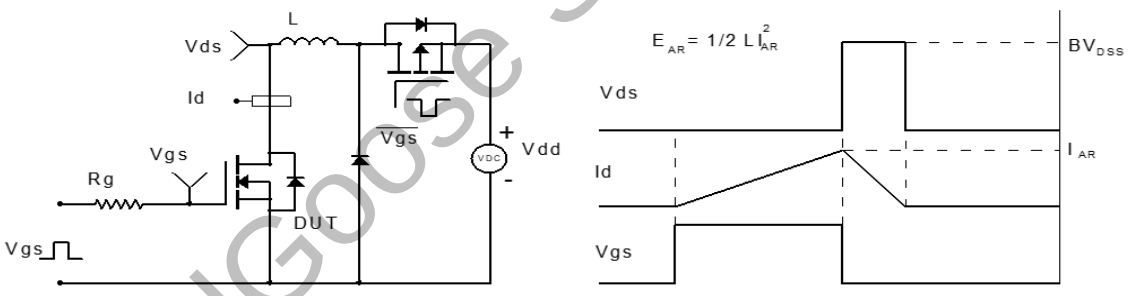

Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

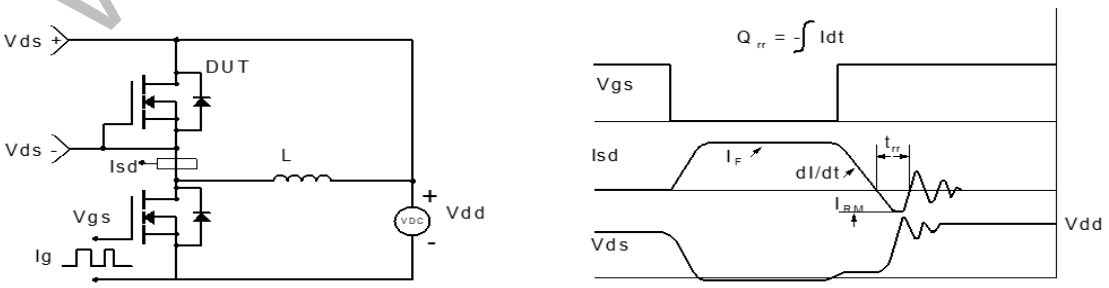

Figure 4: Diode Recovery Test Circuit & Waveform

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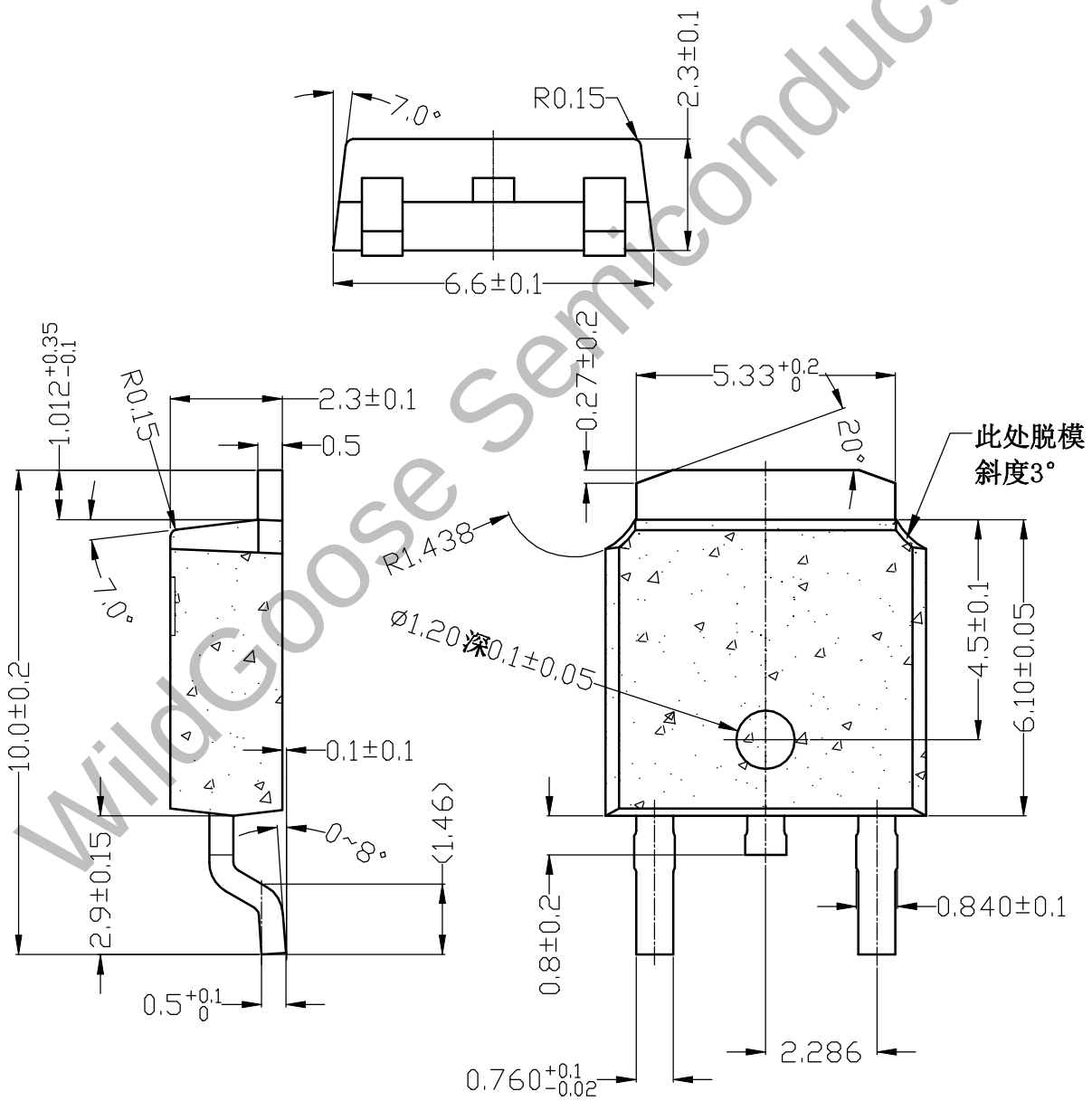
Package Dimension

TO-252

Units：mm



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