AiP74HC/HCT08
Quad 2-input And Gate

Product Specification

Specification Revision History：

Version	Date	Description
2012-05-A1	2012-05	New
2023-04-B1	2023-04	Update the template
2026-02-B2	2026-02	Add junction temperature and ESD
2026-05-B3	2026-05	Add DHVQFN14 package form

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1、General Description

The AiP74HC/HCT08 is a quad 2-input AND gate. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC}.

Features:

- Input levels:
  For AiP74HC08: CMOS level
  For AiP74HCT08: TTL level
- Specified from -40℃ to +125℃
- Packaging information: DIP14/SOP14/TSSOP14/DHVQFN14

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Ordering Information:

Tube packing specifications:

Part number	Packaging form	Marking code	Tube quantity	Boxed tube quantity	Boxed quantity	Notes
AiP74HC08DA14.TB	DIP14	74HC08	25 PCS/tube	40 tube/box	1000 PCS/box	Dimensions of plastic enclosure: 19.0mm×6.4mm Pin spacing: 2.54mm
AiP74HCT08DA14.TB	DIP14	74HCT08	25 PCS/tube	40 tube/box	1000 PCS/box	Dimensions of plastic enclosure: 19.0mm×6.4mm Pin spacing: 2.54mm
AiP74HC08SA14.TB	SOP14	74HC08	50 PCS/tube	200 tube/box	10000 PCS/box	Dimensions of plastic enclosure: 8.7mm×3.9mm Pin spacing: 1.27mm
AiP74HCT08SA14.TB	SOP14	74HCT08	50 PCS/tube	200 tube/box	10000 PCS/box	Dimensions of plastic enclosure: 8.7mm×3.9mm Pin spacing: 1.27mm
AiP74HC08TA14.TB	TSSOP14	74HC08	96 PCS/tube	200 tube/box	19200 PCS/box	Dimensions of plastic enclosure: 5.0mm×4.4mm Pin spacing: 0.65mm
AiP74HCT08TA14.TB	TSSOP14	74HCT08	96 PCS/tube	200 tube/box	19200 PCS/box	Dimensions of plastic enclosure: 5.0mm×4.4mm Pin spacing: 0.65mm

Reel packing specifications:

Part number	Packaging form	Marking code	Reel quantity	Boxed reel quantity	Notes
AiP74HC08SA14.TR	SOP14	74HC08	4000 PCS/reel	8000 PCS/box	Dimensions of plastic enclosure: 8.7mm×3.9mm Pin spacing: 1.27mm
AiP74HCT08SA14.TR	SOP14	74HCT08	4000 PCS/reel	8000 PCS/box	Dimensions of plastic enclosure: 8.7mm×3.9mm Pin spacing: 1.27mm
AiP74HC08TA14.TR	TSSOP14	74HC08	5000 PCS/reel	10000 PCS/box	Dimensions of plastic enclosure: 5.0mm×4.4mm Pin spacing: 0.65mm
AiP74HCT08TA14.TR	TSSOP14	74HCT08	5000 PCS/reel	10000 PCS/box	Dimensions of plastic enclosure: 5.0mm×4.4mm Pin spacing: 0.65mm
AiP74HC08QE14.TR	DHVQFN14	74HC08	3000 PCS/reel	3000 PCS/box	Dimensions of plastic enclosure: 3.0mm×2.5mm Pin spacing: 0.5mm
AiP74HCT08QE14.TR	DHVQFN14	74HCT08	3000 PCS/reel	3000 PCS/box	Dimensions of plastic enclosure: 3.0mm×2.5mm 3in spacing: 0.50mm

Note: If the physical information is inconsistent with the ordering information, please refer to the actual product.

2、Block Diagram And Pin Description

2.1、Block Diagram

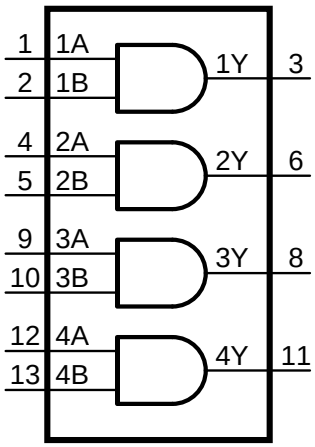


Figure 1. Logic symbol

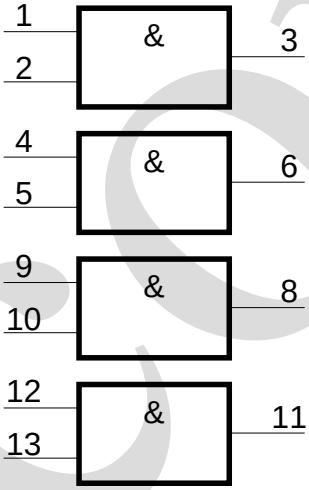


Figure 2. IEC logic symbol

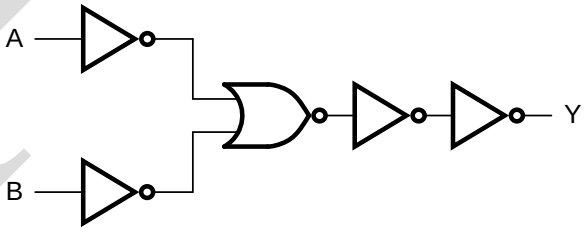
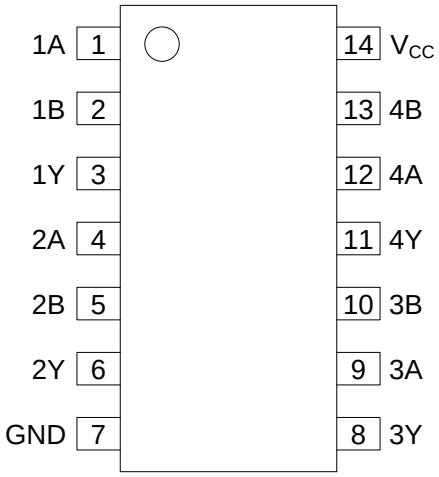
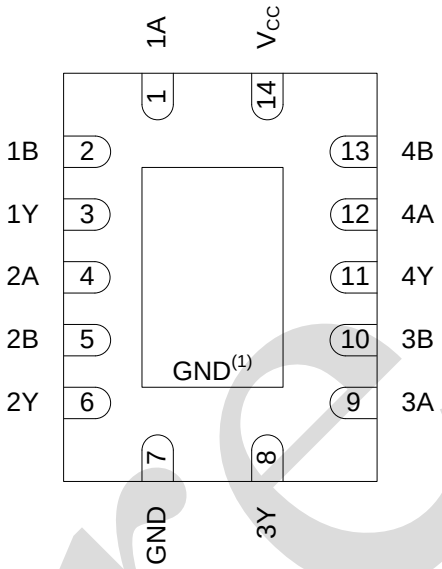


Figure 3. Logic diagram for one gate

2.2、Pin Configurations



DIP14/SOP14/TSSOP14



DHVQFN14

Note: (1) If it is soldered, the solder land should remain floating or be connected to GND.

2.3、Pin Description

Pin No.	Pin Name	Description
1	1A	data input
2	1B	data input
3	1Y	data output
4	2A	data input
5	2B	data input
6	2Y	data output
7	GND	ground (0V)
8	3Y	data output
9	3A	data input
10	3B	data input
11	4Y	data output
12	4A	data input
13	4B	data input
14	V_{CC}	supply voltage

2.4、Function Table

Input		Output
nA	**nB**	**nY**
L	L	L
L	H	L
H	L	L
H	H	H

Note: H=HIGH voltage level; L=LOW voltage level.

3、Electrical Parameter

3.1、Absolute Maximum Ratings

(Voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Max.	Unit
supply voltage	V_{CC}	-		-0.5	+7	V
input clamping current	I_{IK}	$V_I < -0.5V$ or $V_I > V_{CC}+0.5V$		-	±20	mA
output clamping current	I_{OK}	$V_O < -0.5V$ or $V_O > V_{CC}+0.5V$		-	±20	mA
output current	I_O	$-0.5V < V_O < V_{CC}+0.5V$		-	±25	mA
supply current	I_{CC}	-		-	50	mA
ground current	I_{GND}	-		-50	-	mA
total power dissipation	P_{tot}	-		-	500	mW
storage temperature	T_{stg}	-		-65	+150	℃
junction temperature	T_J	-		150		℃
soldering temperature	T_L	10s	DIP	245		℃
			SOP/TSSOP/DHVQFN	260		℃
electrostatic discharge	ESD	HBM		2000		V

3.2、Recommended Operating Conditions

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
AiP74HC08						
supply voltage	V_{CC}	-	2.0	5.0	6.0	V
input voltage	V_I	-	0	-	V_{CC}	V
output voltage	V_O	-	0	-	V_{CC}	V
input transition rise and fall rate	Δt/ΔV	V_{CC}=2.0V	-	-	625	ns/V
		V_{CC}=4.5V	-	1.67	139	ns/V
		V_{CC}=6.0V	-	-	83	ns/V
ambient temperature	T_{amb}	-	-40	-	+125	℃
AiP74HCT08						
supply voltage	V_{CC}	-	4.5	5.0	5.5	V
input voltage	V_I	-	0	-	V_{CC}	V
output voltage	V_O	-	0	-	V_{CC}	V
input transition rise and fall rate	Δt/ΔV	V_{CC}=2.0V	-	-	-	ns/V
		V_{CC}=4.5V	-	1.67	139	ns/V
		V_{CC}=6.0V	-	-	-	ns/V
ambient temperature	T_{amb}	-	-40	-	+125	℃

3.3、Electrical Characteristics

3.3.1、DC Characteristics 1

(T_{amb}=25℃, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
AiP74HC08							
HIGH-level input voltage	V_{IH}	V_{CC}=2.0V		1.5	1.2	-	V
		V_{CC}=4.5V		3.15	2.4	-	V
		V_{CC}=6.0V		4.2	3.2	-	V
LOW-level input voltage	V_{IL}	V_{CC}=2.0V		-	0.8	0.5	V
		V_{CC}=4.5V		-	2.1	1.35	V
		V_{CC}=6.0V		-	2.8	1.8	V
HIGH-level output voltage	V_{OH}	V_I=V_{IH} or V_{IL}	I_O=-20uA; V_{CC}=2.0V	1.9	2.0	-	V
			I_O=-20uA; V_{CC}=4.5V	4.4	4.5	-	V
			I_O=-20uA; V_{CC}=6.0V	5.9	6.0	-	V
			I_O=-4.0mA; V_{CC}=4.5V	3.98	4.32	-	V
			I_O=-5.2mA; V_{CC}=6.0V	5.48	5.81	-	V
LOW-level output voltage	V_{OL}	V_I=V_{IH} or V_{IL}	I_O=20uA; V_{CC}=2.0V	-	0	0.1	V
			I_O=20uA; V_{CC}=4.5V	-	0	0.1	V
			I_O=20uA; V_{CC}=6.0V	-	0	0.1	V
			I_O=4.0mA; V_{CC}=4.5V	-	0.15	0.26	V
			I_O=5.2mA; V_{CC}=6.0V	-	0.16	0.26	V
input leakage current	I_I	V_I=V_{CC} or GND; V_{CC}=6.0V		-	-	±1	uA
supply current	I_{CC}	V_I=V_{CC} or GND; I_O=0A; V_{CC}=6.0V		-	-	2.0	uA
input	C_I	-		-	3.5	-	pF

capacitance						
AiP74HCT08						
HIGH-level input voltage	V_{IH}	V_{CC}=4.5V to 5.5V	2.0	1.6	-	V
LOW-level input voltage	V_{IL}	V_{CC}=4.5V to 5.5V	-	1.2	0.8	V
HIGH-level output voltage	V_{OH}	V_I=V_{IH} or V_{IL}; I_O=-20uA; V_{CC}=4.5V	4.4	4.5	-	V
		V_I=V_{IH} or V_{IL}; I_O=-4.0mA; V_{CC}=4.5V	3.98	4.32	-	V
LOW-level output voltage	V_{OL}	V_I=V_{IH} or V_{IL}; I_O=20uA; V_{CC}=4.5V	-	0	0.1	V
		V_I=V_{IH} or V_{IL}; I_O=5.2mA; V_{CC}=4.5V	-	0.15	0.26	V
input leakage current	I_I	V_I=V_{CC} or GND; V_{CC}=6.0V	-	-	±1	uA
supply current	I_{CC}	V_I=V_{CC} or GND; I_O=0A; V_{CC}=5.5V	-	-	2.0	uA
additional supply current	ΔI_{CC}	per input pin; V_I=V_{CC}-2.1V; I_O=0A; other inputs at V_{CC} or GND; V_{CC}=4.5V to 5.5V	-	-	216	uA
input capacitance	C_I	-	-	3.5	-	pF

3.3.2、DC Characteristics 2

(T_{amb}=-40℃ to +85℃, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
AiP74HC08						
HIGH-level input voltage	V_{IH}	V_{CC}=2.0V	1.5	-	-	V
		V_{CC}=4.5V	3.15	-	-	V
		V_{CC}=6.0V	4.2	-	-	V
LOW-level input voltage	V_{IL}	V_{CC}=2.0V	-	-	0.5	V
		V_{CC}=4.5V	-	-	1.35	V
		V_{CC}=6.0V	-	-	1.8	V
HIGH-level output voltage	V_{OH}	V_I=V_{IH} or V_{IL}; I_O=-20uA; V_{CC}=2.0V	1.9	-	-	V
		V_I=V_{IH} or V_{IL}; I_O=-20uA; V_{CC}=4.5V	4.4	-	-	V
		V_I=V_{IH} or V_{IL}; I_O=-20uA; V_{CC}=6.0V	5.9	-	-	V
		V_I=V_{IH} or V_{IL}; I_O=-4.0mA; V_{CC}=4.5V	3.84	-	-	V
		V_I=V_{IH} or V_{IL}; I_O=-5.2mA; V_{CC}=6.0V	5.34	-	-	V
LOW-level output voltage	V_{OL}	V_I=V_{IH} or V_{IL}; I_O=20uA; V_{CC}=2.0V	-	-	0.1	V
		V_I=V_{IH} or V_{IL}; I_O=20uA; V_{CC}=4.5V	-	-	0.1	V
		V_I=V_{IH} or V_{IL}; I_O=20uA; V_{CC}=6.0V	-	-	0.1	V
		V_I=V_{IH} or V_{IL}; I_O=4.0mA; V_{CC}=4.5V	-	-	0.33	V
		V_I=V_{IH} or V_{IL}; I_O=5.2mA; V_{CC}=6.0V	-	-	0.33	V
input leakage current	I_I	V_I=V_{CC} or GND; V_{CC}=6.0V	-	-	±1	uA
supply current	I_{CC}	V_I=V_{CC} or GND; I_O=0A; V_{CC}=6.0V	-	-	20	uA
AiP74HCT08						
HIGH-level input voltage	V_{IH}	V_{CC}=4.5V to 5.5V	2.0	-	-	V
LOW-level	V_{IL}	V_{CC}=4.5V to 5.5V	-	-	0.8	V

input voltage							
HIGH-level output voltage	V_{OH}	$V_I=V_{IH}$ or V_{IL}	I_O=-20uA; V_{CC}=4.5V	4.4	-	-	V
			I_O=-4.0mA; V_{CC}=4.5V	3.84	-	-	V
LOW-level output voltage	V_{OL}	$V_I=V_{IH}$ or V_{IL}	I_O=20uA; V_{CC}=4.5V	-	-	0.1	V
			I_O=5.2mA; V_{CC}=4.5V	-	-	0.33	V
input leakage current	I_I	$V_I=V_{CC}$ or GND; V_{CC}=6.0V		-	-	±1	uA
supply current	I_{CC}	$V_I=V_{CC}$ or GND; I_O=0A; V_{CC}=5.5V		-	-	20	uA
additional supply current	ΔI_{CC}	per input pin; $V_I=V_{CC}$-2.1V; I_O=0A; other inputs at V_{CC} or GND; V_{CC}=4.5V to 5.5V		-	-	270	uA

3.3.3、DC Characteristics 3

(T_{amb}=-40℃ to +125℃, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
AiP74HC08							
HIGH-level input voltage	V_{IH}	V_{CC}=2.0V		1.5	-	-	V
		V_{CC}=4.5V		3.15	-	-	V
		V_{CC}=6.0V		4.2	-	-	V
LOW-level input voltage	V_{IL}	V_{CC}=2.0V		-	-	0.5	V
		V_{CC}=4.5V		-	-	1.35	V
		V_{CC}=6.0V		-	-	1.8	V
HIGH-level output voltage	V_{OH}	$V_I=V_{IH}$ or V_{IL}	I_O=-20uA; V_{CC}=2.0V	1.9	-	-	V
			I_O=-20uA; V_{CC}=4.5V	4.4	-	-	V
			I_O=-20uA; V_{CC}=6.0V	5.9	-	-	V
			I_O=-4.0mA; V_{CC}=4.5V	3.7	-	-	V
			I_O=-5.2mA; V_{CC}=6.0V	5.2	-	-	V
LOW-level output voltage	V_{OL}	$V_I=V_{IH}$ or V_{IL}	I_O=20uA; V_{CC}=2.0V	-	-	0.1	V
			I_O=20uA; V_{CC}=4.5V	-	-	0.1	V
			I_O=20uA; V_{CC}=6.0V	-	-	0.1	V
			I_O=4.0mA; V_{CC}=4.5V	-	-	0.4	V
			I_O=5.2mA; V_{CC}=6.0V	-	-	0.4	V
input leakage current	I_I	$V_I=V_{CC}$ or GND; V_{CC}=6.0V		-	-	±1	uA
supply current	I_{CC}	$V_I=V_{CC}$ or GND; I_O=0A; V_{CC}=6.0V		-	-	40	uA
AiP74HCT08							
HIGH-level input voltage	V_{IH}	V_{CC}=4.5V to 5.5V		2.0	-	-	V
LOW-level input voltage	V_{IL}	V_{CC}=4.5V to 5.5V		-	-	0.8	V
HIGH-level output voltage	V_{OH}	$V_I=V_{IH}$ or V_{IL}	I_O=-20uA; V_{CC}=4.5V	4.4	-	-	V
			I_O=-4.0mA; V_{CC}=4.5V	3.7	-	-	V
LOW-level output voltage	V_{OL}	$V_I=V_{IH}$ or V_{IL}	I_O=20uA; V_{CC}=4.5V	-	-	0.1	V
			I_O=5.2mA; V_{CC}=4.5V	-	-	0.4	V
input leakage	I_I	$V_I=V_{CC}$ or GND;		-	-	±1	uA

current		V_{CC}=6.0V				
supply current	I_{CC}	V_I=V_{CC} or GND; I_O=0A; V_{CC}=5.5V	-	-	40	uA
additional supply current	ΔI_{CC}	per input pin; V_I=V_{CC}-2.1V; I_O=0A; other inputs at V_{CC} or GND; V_{CC}=4.5V to 5.5V	-	-	294	uA

3.3.4、AC Characteristics 1

(T_{amb}=25℃, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
AiP74HC08							
nA, nB to nY propagation delay	t_{pd}	see Figure 5[1]	V_{CC}=2.0V	-	25	90	ns
			V_{CC}=4.5V	-	9	18	ns
			V_{CC}=5.0V;C_L=15pF	-	7	-	ns
			V_{CC}=6.0V	-	7	15	ns
transition time	t_t	see Figure 5[2]	V_{CC}=2.0V	-	19	75	ns
			V_{CC}=4.5V	-	7	15	ns
			V_{CC}=6.0V	-	6	13	ns
power dissipation capacitance	C_{PD}	per package; V_I=GND to V_{CC}[3]		-	10	-	pF
AiP74HCT08							
nA, nB to nY propagation delay	t_{pd}	see Figure 5[1]	V_{CC}=4.5V	-	14	24	ns
			V_{CC}=5.0V;C_L=15pF	-	11	-	ns
transition time	t_t	see Figure 5[2]	V_{CC}=4.5V	-	7	15	ns
power dissipation capacitance	C_{PD}	per package; V_I=GND to V_{CC}–1.5V[3]		-	20	-	pF

Note:

[1] t_{pd} is the same as t_{PLH} and t_{PHL}.

[2] t_t is the same as t_{THL} and t_{TLH}.

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in uW).

P_D=(C_{PD} ×V_{CC}^2 ×f_i ×N)+∑(C_L ×V_{CC}^2 ×f_o) where:

f_i=input frequency in MHz;

f_o=output frequency in MHz;

C_L=output load capacitance in pF;

V_{CC}=supply voltage in V;

N=number of inputs switching;

∑(C_L ×V_{CC}^2 ×f_o)=sum of outputs.

3.3.5、AC Characteristics 2

(T_{amb}=-40℃ to +85℃, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
AiP74HC08							
nA, nB to nY propagation delay	t_{pd}	see Figure 5[1]	V_{CC}=2.0V	-	-	115	ns
			V_{CC}=4.5V	-	-	23	ns
			V_{CC}=6.0V	-	-	20	ns
transition time	t_t	see Figure 5[2]	V_{CC}=2.0V	-	-	95	ns
			V_{CC}=4.5V	-	-	19	ns
			V_{CC}=6.0V	-	-	16	ns
AiP74HCT08							
nA, nB to nY propagation delay	t_{pd}	see Figure 5[1]	V_{CC}=4.5V	-	-	30	ns
transition time	t_t	see Figure 5[2]	V_{CC}=4.5V	-	-	19	ns

Note:

[1] t_{pd} is the same as t_{PLH} and t_{PHL}.

[2] t_t is the same as t_{THL} and t_{TLH}.

3.3.6、AC Characteristics 3

(T_{amb}=-40℃ to +125℃, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
AiP74HC08							
nA, nB to nY propagation delay	t_{pd}	see Figure 5[1]	V_{CC}=2.0V	-	-	135	ns
			V_{CC}=4.5V	-	-	27	ns
			V_{CC}=6.0V	-	-	23	ns
transition time	t_t	see Figure 5[2]	V_{CC}=2.0V	-	-	110	ns
			V_{CC}=4.5V	-	-	22	ns
			V_{CC}=6.0V	-	-	19	ns
AiP74HCT08							
nA, nB to nY propagation delay	t_{pd}	see Figure 5[1]	V_{CC}=4.5V	-	-	36	ns
transition time	t_t	see Figure 5[2]	V_{CC}=4.5V	-	-	22	ns

Note:

[1] t_{pd} is the same as t_{PLH} and t_{PHL}.

[2] t_t is the same as t_{THL} and t_{TLH}.

4、Testing Circuit

4.1、AC Testing Circuit

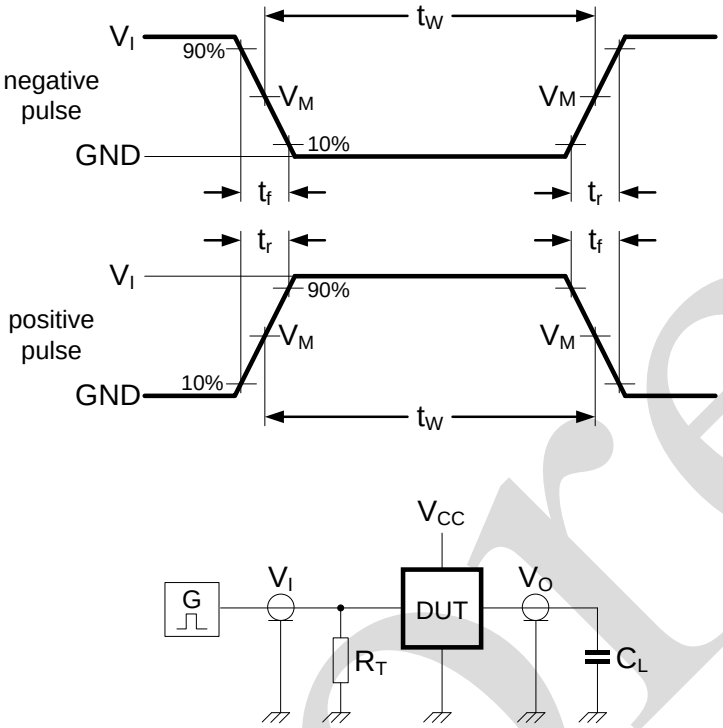


Figure 4. Test circuit for measuring switching times

Definitions for test circuit:

C_L=load capacitance including jig and probe capacitance.

R_T=termination resistance should be equal to the output impedance Z_o of the pulse generator.

4.2、AC Testing Waveforms

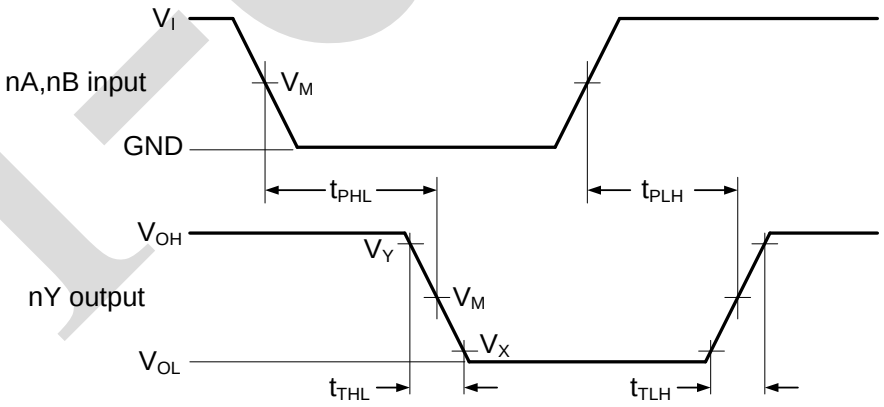


Figure 5. Input to output propagation delays

4.3、Measurement Points

Type	Input	Output		
	V_M	V_M	V_X	V_Y
AiP74HC08	$0.5\times V_{CC}$	$0.5\times V_{CC}$	$0.1\times V_{CC}$	$0.9\times V_{CC}$
AiP74HCT08	1.3V	1.3V	$0.1\times V_{CC}$	$0.9\times V_{CC}$

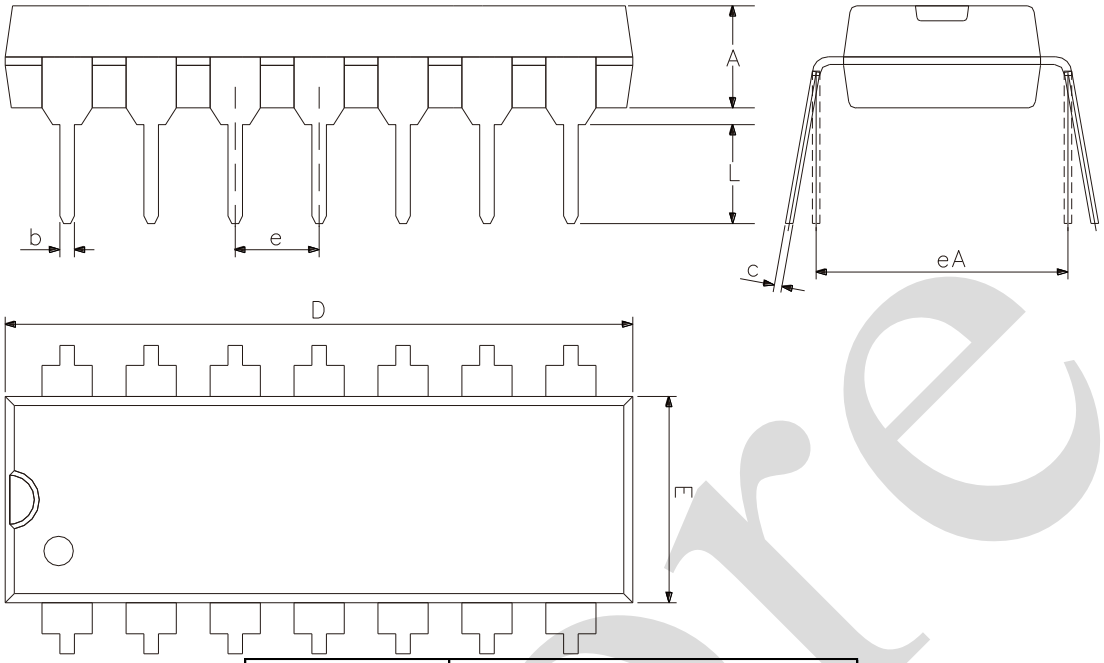
4.4、Test Data

Type	Input		Load	Test
	V_I	t_r , t_f	C_L	
AiP74HC08	V_{CC}	6.0ns	15pF, 50pF	t_{PLH}, t_{PHL}
AiP74HCT08	3.0V	6.0ns	15pF, 50pF	t_{PLH}, t_{PHL}

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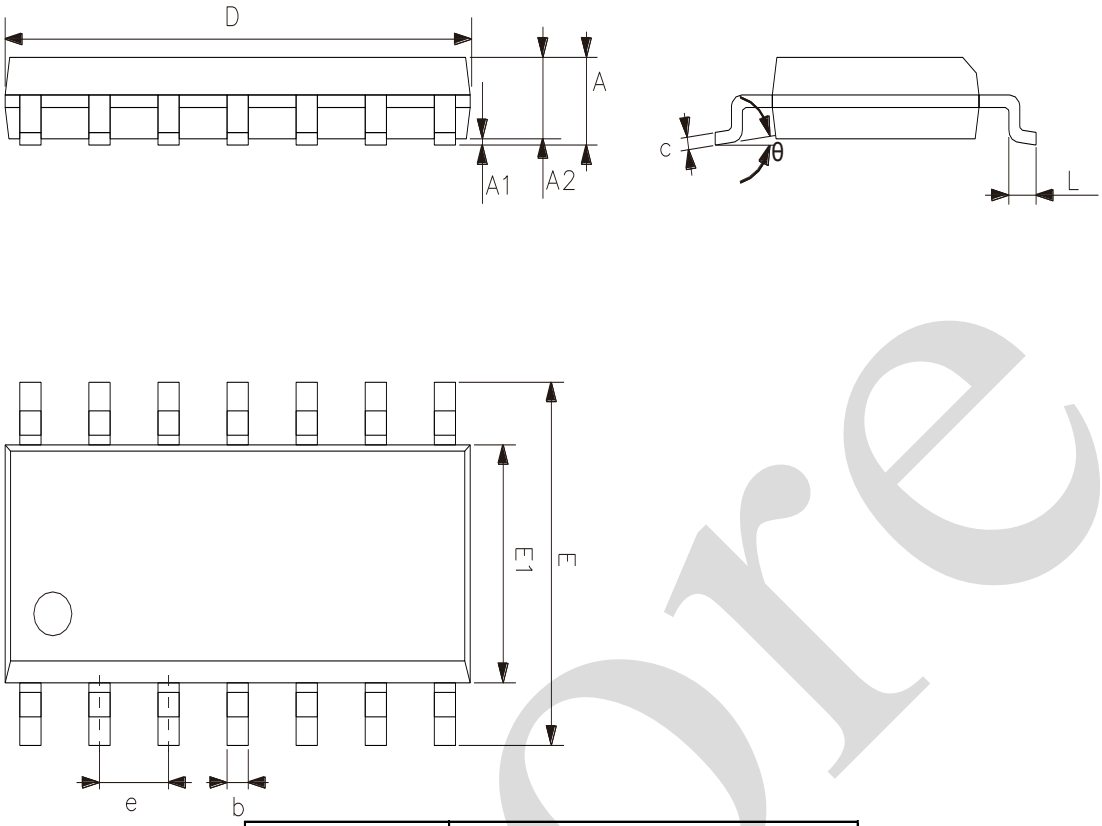
5、Package Information

5.1、DIP14



2023/12/A	Dimensions In Millimeters	
Symbol	**Min**	**Max**
A	3.05	3.60
b	0.33	0.56
c	0.20	0.36
D	18.80	19.40
E	6.20	6.60
e	2.54	
eA	7.62	10.90
L	2.92	—

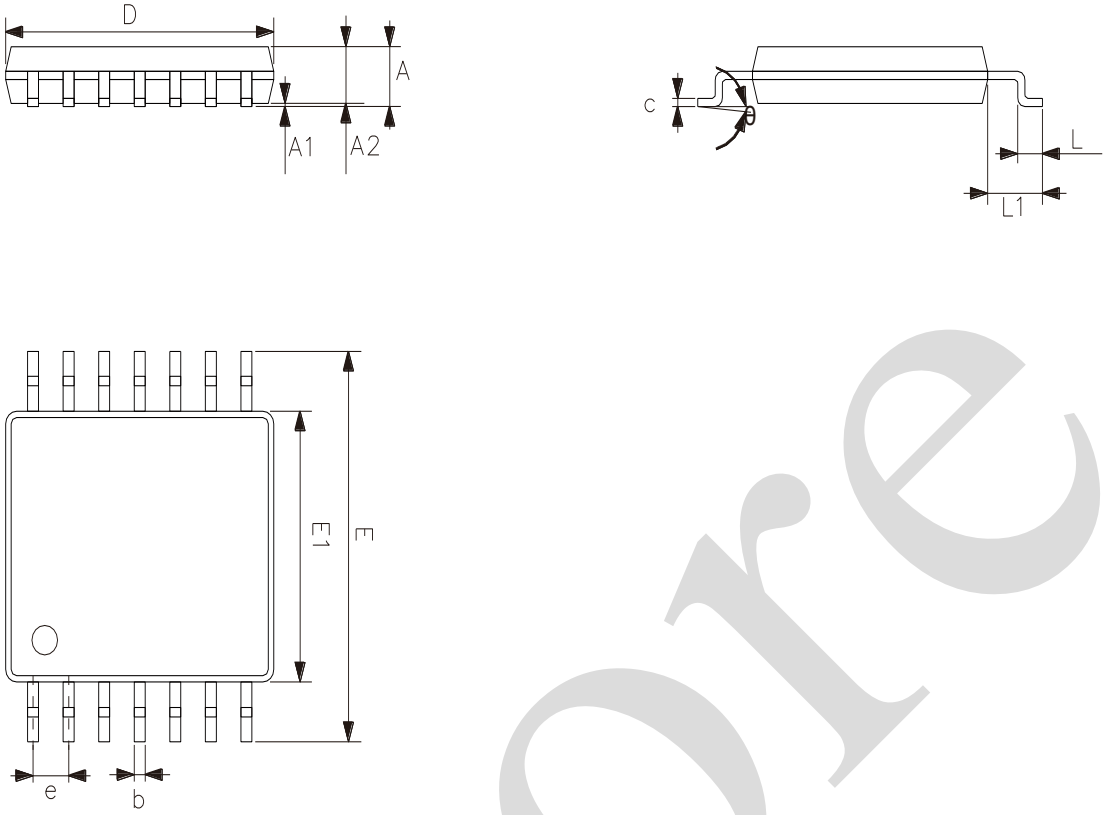
5.2、SOP14



2023/12/A	Dimensions In Millimeters	
Symbol	**Min.**	**Max.**
A	1.50	1.75
A1	0.05	0.25
A2	1.30	—
b	0.33	0.50
c	0.19	0.25
D	8.43	8.76
E	5.80	6.25
E1	3.75	4.00
e	1.27	
L	0.40	0.89
θ	0°	8°

i-core

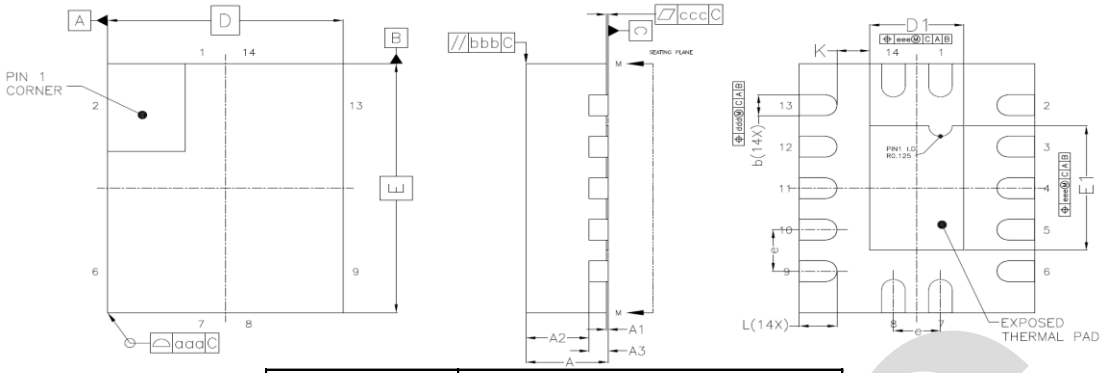
5.3、TSSOP14



2023/12/A	Dimensions In Millimeters	
Symbol	**Min**	**Max**
A	—	1.20
A1	0.05	0.15
A2	0.80	1.05
b	0.19	0.30
c	0.09	0.20
D	4.90	5.10
E1	4.30	4.50
E	6.20	6.60
e	0.65	
L	0.45	0.75
L1	1.00	
θ	0°	8°

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5.4、DHVQFN14



2024/10/A	Dimensions In Millimeters	
Symbol	**Min**	**Max**
A	0.80	1.00
A1	0	0.05
A2	0.60	0.70
A3	0.203 REF	
D	2.40	2.60
E	2.90	3.10
e	0.50	
b	0.18	0.30
L	0.30	0.50
D1	0.85	1.15
E1	1.35	1.65
K	0.35	

6、Statements And Notes

6.1、The name and content of Hazardous substances or Elements in the product

Part name	Hazardous substances or Elements									
	Lead and lead compounds	Mercury and mercury compounds	Cadmium and cadmium compounds	Hexavalent chromium compounds	Polybrominated biphenyls	Polybrominated biphenyl ethers	Dibutyl phthalate	Butylbenzyl phthalate	Di-2-ethylhexyl phthalate	Diisobutyl phthalate
Lead frame	○	○	○	○	○	○	○	○	○	○
Plastic resin	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
The lead	○	○	○	○	○	○	○	○	○	○
Plastic sheet installed	○	○	○	○	○	○	○	○	○	○
explanation	○：Indicates that the content of hazardous substances or elements in the detection limit of the following the SJ/T11363-2006 standard. ×：Indicates that the content of hazardous substances or elements exceeding the SJ/T11363-2006 Standard limit requirements.									

6.2、Notes

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