MLX90392

3D Magnetometer

Datasheet



Features and Benefits

- 3-axis magnetometer device suitable for compass and position sensors applications Triaxis→ Hall Technology
- Suitable for space constrained applications (only 2 x 2.5 x 0.4 mm³)
- Compatible with I²C (0.1, 0.4, 1.0 MHz)
- Low power application Power-down current of 1.5µA
- Supply voltage of 1.8V
- Ambient temperature range from -40°C to 85°C
- Digital Output
 - o 16-bit Magnetic (XYZ)
 - o 16-bit Temperature
- At runtime selectable modes
 - o Single Measurement
 - Continuous Mode up to 1.4kHz (XYZ)
 - o Power-Down Mode
- RoHS Compliant & Green Package



UTDFN-8

Application Examples

- Power tools screwdriver trigger
- Home security door/window opening detection
- Knobs for white goods rotary, linear, push
- PC peripheral mouse roller
- Gaming joystick, camera gimbal
- Anti-tamper for energy metering
- Human-machine interface

Description

The device measures magnetic fields along the 3 axis and is specifically designed for micro-power applications. Those measurements and the IC temperature are transferred upon request over I²C communication channel. The device transmits compensated 16-bit raw measurement data.

The MLX90392 is available in 2 magnetic versions:

- ±5mT range for low noise applications
- ±50mT range for position sensor applications





1. Ordering Information

Ordering Code	Temperature	Package	Туре	Output	7-bit I ² C address	Packing
MLX90392ELQ-AAA-010-RE	-40°C to 85°C	UTDFN-8 2x2.5	±5mT	I ² C	0x0C	Reel
MLX90392ELQ-AAA-011-RE	-40°C to 85°C	UTDFN-8 2x2.5	±50mT	I ² C	0x0C	Reel
MLX90392ELQ-AAA-013-RE	-40°C to 85°C	UTDFN-8 2x2.5	±50mT	I ² C	0x3C	Reel
Table 1: Ordering codes						

Legend:





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3. Glossary of Terms

Term	Description
NC	Not connected
ADC	Analog-to-digital converter
LSB	Least significant bit
MSB	Most significant bit
Gauss (G)	Units for magnetic flux density – 1mT = 10G
RMS	Root mean square
POR	Power on reset
NV	Non-volatile
DSP	Digital signal processing



4. Pins Description and Block Diagram

4.1. Pins Description

Pin #	Name (I ² C)	Description
1	SDA	[I/O] Bus data
2	VDD	[S] Supply
3	VSS	[S] Ground
4	SCL	[I] Bus clock
5	Not used	Not connected
6	Not used	Not connected
7	Not used	Not connected
8	Not used	Not connected

Table 2: Pin description

The exposed pad of the UTDFN-8 package can be left floating or shorted to ground.

5. Block Diagram



Figure 1: IC block diagram



6. Conditions and Specifications

6.1. Absolute Maximum Ratings (AMR)

Operating characteristics, $T_A = -40^{\circ}$ C to 85°C (unless otherwise specified)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Supply voltage	V _{DD}			2.3	V	<48h
Reverse voltage protection	V _{DD-rev}			-0.3	V	Room temp, <48h
Output voltage	Vsda, Vscl			2.3	V	<48h
Reverse output voltage	VSDAREV			-0.3	V	
Reverse clock output voltage	VSCLREV			-0.3	V	
ESD HBM (all pins)				2	kV	
ESD CDM (all pins)				500	V	
Operating temperature	TA	-40		+85	°C	
Junction temperature	TJUNC			+85	°C	
Storage temperature	Tstorage	-40		150	°C	
Thermal resistance	R _{thja}		230		°C/W	Junction to ambient 1s0p board
			40		°C/W	Junction to ambient multi layered pcb
Thermal resistance	Rthjc		3.4		°C/W	Junction to case
Magnetic flux density		-1		1	Т	
able 2: Absolute maximum ratings						

Table 3: Absolute maximum ratings

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute maximumrated conditions for extended periods may affect device reliability.

6.2. Operating Conditions

6.2.1. General Operating Conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Operating temperature	TA	-40		+85	°C	
Storage temperature	T _{storage}	-40		150	°C	
Table A: Conoral operating condition	e.					

Table 4: General operating conditions

6.2.2. Electrical Operating Conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Supply voltage	V _{DD}	1.65	1.8	1.95	V			
Table 5: Electrical exercises conditions								

 Table 5: Electrical operating conditions

6.2.3. Magnetic Operating Conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Magnetic flux density		-5		5	mT	Version -010
Magnetic flux density		-50		50	mT	Version -011 Version -013

Table 6: Magnetic operating conditions



6.2.4. I²C Timings Conditions

Electrical Parameter	Symbol	I ² C Standard Mode		I ² C Fast Mode Plus		Unit
	Symbol	Min.	Max.	Min.	Max.	Unit
SCL clock frequency	f (SCL)	0	100	0	1000	kHz
SCL clock low time	t _w (SCLL)	4.7		0.5		μs
SCL clock high time	t _w (SCLH)	4		0.26		μs
SDA setup time	t _{su} (SDA)	250		50		ns
SDA data hold time	th (SDA)	0	3.45	0	0.45	μs
START condition hold time	t _h (ST)	4		0.26		μs
REPEATED START condition setup time	t _{su} (SR)	4.7		0.26		μs
STOP condition setup time	t _{su} (SP)	4		0.26		μs
Bus free time between STOP and START condition	t _w (SP:ST)	4.7		0.5		μs

Table 7: General I²C timing specification



Figure 2: I²C timing diagram



6.3. Electrical Specifications

Operating characteristics, $T_A = -40^{\circ}C$ to 85°C (unless otherwise specified)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
ower On Reset (rising edge)	Vpor_lh	1.44	1.5	1.55	V	
ower On Reset (falling edge)	VPOR_HL	1.35	1.4	1.45	V	
Conversion current	IDD,CONVXY	2.2	2.7	3.5	mA	XY axis, ver010
	I _{DD,CONVZ}	2.7	3.6	5.2	mA	Z axis, ver010
Conversion current	Idd,convxy	1.4	1.8	2.7	mA	XY axis, ver011 & -013
	I _{DD,CONVZ}	1.9	2.8	4.3	mA	Z axis, ver011 & -013
Conversion current	Idd,convt	0.6	0.73	0.85	mA	Temperature
Counting state current	I _{DD,CNT}	6	11	16	μΑ	
Power-down current	DD,PD	0.4	1.5	3	μΑ	
Average current 100Hz refresh	Idd,avg100-0	1.8	2.3	3.2	mA	Version -010 Continuous mode XYZT, OSR_HALL=1 DIG_FILTXY=4 DIG_FILTZ=5 DIG_FILT_TEMP=1 OSR_TEMP=1 Temp Comp enabled
Average current	IDD,AVG100-1	1.2	1.7	2.7	mA	Version -011 & -013 Continuous mode XYZT,
100Hz refresh						OSR_HALL=1 DIG_FILTXY=4 DIG_FILTZ=5 DIG_FILT_TEMP=1 OSR_TEMP=1 Temp Comp enabled
Average current	DD,AVG100-1	0.7	0.95	1.5	mA	Version -011 & -013
100Hz refresh						Continuous mode XYZT, OSR_HALL=1 DIG_FILTXY=3 DIG_FILTZ=4 DIG_FILT_TEMP=1 OSR_TEMP=1 Temp Comp enabled
Average current	IDD,AVG200-0	1.8	2.45	3.5	mA	Version -010
200Hz refresh						Continuous mode XYZT, OSR_HALL=1 DIG_FILTXY=3 DIG_FILTZ=4 DIG_FILT_TEMP=1 OSR_TEMP=1 Temp Comp enabled
Average current	DD,AVG200-1	1.3	1.85	2.8	mA	Version -011 & -013
200Hz refresh						Continuous mode XYZT OSR_HALL=1 DIG_FILTXY=3 DIG_FILTZ=4 DIG_FILT_TEMP=1 OSR_TEMP=1 Temp Comp enabled

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Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Temperature sensor resolution ⁽¹⁾	T _{RES}	48	50	52	LSB ₁₆ /°C	
Temperature sensor accuracy	T _{LIN}	-3		3	°C	±3sigma
Input level high ⁽²⁾	VIH	56	59	63	%V _{DD}	SDA, SCL
Input level low ⁽²⁾	VIL	43	48	54	%V _{DD}	SDA, SCL
Input level hysteresis	VI, HYST	5	11	17	%V _{DD}	SDA, SCL
Input capacitance ⁽²⁾	Cin		5	10	рF	SDA, SCL
Output level low	Vol		0.5	1	%V _{DD}	SDA (Static, 1mA load)
Output on resistance	Rdson	5	8	14	Ω	±3sigma
Output leakage current			0.1	0.3	μΑ	

Table 8: Electrical operating conditions

 $^{^{1}}$ The data format is 2's complement with 0 lsb corresponding to 0°C

 $^{^{2}}$ This specification relates to the sensor and not the $I^{2}C$ bus



6.4. Magnetic Specifications

Operating characteristics, $T_A = -40^{\circ}$ C to 85°C (unless otherwise specified)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
XY magnetic sensitivity	SENS _{XY}	0.135 1.33	0.15 1.5	0.165 1.67	μT/LSB	Version -010 Version -011 & -013 Room temperature
Z magnetic sensitivity	SENSz	0.135 1.33	0.15 1.5	0.165 1.67	μT/LSB	Version -010 Version -011 & -013 Room temperature
Magnetic measurement range	Brange	±4421 ±43560	±4912 ±49128	±5403 ±54696	μΤ μΤ	Version -010 Version -011 & -013 Room temperature
RMS noise ⁽³⁾	N _{XYZ}		2.2	2.4	μTrms	Version -010 Room temperature with temperature compensation – 5ms conv time (DIG_FILTXY=3, DIG_FILTZ=4, OSR_HALL=1)
RMS noise ⁽³⁾	Nxyz		1.5	1.7	μTrms	Version -010 Room temperature without temperature compensation 5ms conv time (DIG_FILTXY=3, DIG_FILTZ=4, OSR_HALL=1)
RMS noise ⁽³⁾	Nxyz		11	12	μTrms	Version -011 & -013 Room temperature without temperature compensation – 5ms conv time (DIG_FILTXY=3, DIG_FILTZ=4, OSR_HALL=1)
RMS noise ⁽³⁾	Nxyz		20	21	μTrms	Version -011 & -013 Room temperature with temperature compensation - 5ms conv time (DIG_FILTXY=3, DIG_FILTZ=4, OSR_HALL=1)
Sensitivity drift	SENSTHD	-10		10	%	vs. Ta=35°C
Hysteresis able 9: Magnetic specifications	Bh	200			μΤ	

³ Not validated by any production test, only verified by characterization



6.5. Timing Specifications

Operating characteristics, T_A = -40°C to 85°C (unless otherwise specified)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
	Fr1	10	100	700	Hz	With temperature compensation ⁽⁵⁾
Output refresh rate ⁽⁴⁾	Fr2			1400	Hz	Without temperature compensation ⁽⁵⁾
Oscillator trimming accuracy	Tosc_trim	-5	0.5	5	%	
Oscillator thermal drift	Tosc_thd	-5	0	5	%	
	T _{CONVM}	105	111	117	μs	Time per axis DIG_FILT=0, OSR=0
Magnetic axis conversion	T _{CONVM}	202	213	224	μs	Time per axis DIG_FILT=0, OSR=1
time ⁽⁶⁾	TCONVM	912	960	1008	μs	Time per axis DIG_FILT=3, OSR=1
	TCONVM	13.1	13.8	14.5	ms	Time per axis DIG_FILT=7, OSR=1
Start-up time	Tstartup		0.15	1.2	ms	Reset to power- down mode
DSP time	T _{DSP}	360	380	400	μs	with temp compensation enabled ⁽³⁾
		220	235	250	μs	with temp compensation disabled ⁽³⁾

Table 10: Timing specifications

⁴ Fr1 and Fr2 are defined as the period between two set of measurements. It is relevant for the Continuous measurement mode and is defined by the parameter MODE[3:0]. TREFRESH is adjustable with the following settings: 10Hz, 20Hz, 50Hz, 100Hz, 200Hz, 500Hz and 1.4kHz. The default value in the non-volatile memory is 100Hz.

 $^{^{\}rm 5}$ The temperature compensation can be enabled or disabled by the user.

⁶ This conversion time is defined as the time to acquire a single axis of the magnetic flux density. When measuring XYZ, they are obtained through time-multiplexing. The conversion time is programmable through DIG_FILT for magnetic and temperature conversion. The total conversion time is obtained by summing up the magnetic & temperature conversion time.





Figure 3: Conversion time

The above graph can be expressed with the following formula:

 $T_{conv}(DIG_FILT) = \frac{16}{F_{clk}} + \frac{OSR}{F_{clk}} \cdot \left(2^{DIG_FILT+2} + 4\right) \qquad F_{clk} = 2.4MHz \quad typical$



6.6. Accuracy Specifications

Operating characteristics, $T_A = -40$ °C to 85°C (unless otherwise specified). All specifications in this chapter are given with ±3 sigma.

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Resolution ⁽⁷⁾			16		bits	XYZ
Offset ⁽⁸⁾	Offsx Offsy Offsz	-1100 -1100 -300	-200 -200 300	700 700 900	LSB ₁₆	Version -010 Output at 0Gauss. Room temperature
Offset thermal drift X-axis Offset thermal drift Y-axis Offset thermal drift Z-axis		-840 -750 -440		1030 870 470	LSB ₁₆	Version -010 vs. 35°C
Offset ⁽⁸⁾	Offsx Offsy Offsz	-150 -330 -340	40 -130 80	220 70 500	LSB ₁₆	Version -011 & -013 Output at 0Gauss. Room temperature
Offset thermal drift X-axis Offset thermal drift Y-axis Offset thermal drift Z-axis		-120 -100 -220		90 120 190	LSB ₁₆	Version -011 & -013 vs. 35°C
Mismatch on the raw signals X, Y and Z	Smismxy Smismxz Smismyz	-5 -5 -3	-1 -2 -1	2 1 1	%	Version -010
Thermal drift of sensitivity mismatch		-110		225	ppm/°C	Version -010
Mismatch on the raw signals X, Y and Z	Smismxy Smismxz Smismyz	-3 7 6	1 10 9	5 13 11	%	Version -011 & -013
Thermal drift of sensitivity mismatch		-140		265	ppm/°C	Version -011 & -013
Cross-axis sensitivity ⁽⁹⁾	Sxyi Syxi Sxzi Szxi Syzi Syzi	-1.8 -2.7 -8.0 -5.0 -5.7 -4.8	0.5 -0.7 -3.2 -0.2 0.5 0.9	2.6 1.3 1.8 4.5 6.6 3	%	Version -010 (verified by characterization / not by final testing)
Cross-axis sensitivity ⁽⁹⁾	S _{XYi} Syxi Sxzi Szxi Syzi Syzi	-1.5 -1.7 -1.3 -3.0 -9.6 -3.3	0.2 0.0 4.9 1.0 -6.3 -1.6	1.9 1.8 12.4 5.6 -4.1 -0.5	%	Version -011 & -013 (verified by characterization / not by final testing)

Table 11: Accuracy specifications

⁷ The data format is 2's complement, further explanation can be found on chapter 7.1.2.6.

⁸ Value of measurement data register on shipment test without applying magnetic field on purpose. These values are guaranteed in the operating magnetic field range.

⁹ The cross axis sensitivity is measured by applying a force field on one axis and measured on another axis. For instance, SXYi means that a field was applied along X axis and measured along Y axis.



7. Functional Description & Interfaces

7.1. Operating Modes

MLX90392 has the following application modes

- 1. Power-down mode
- 2. Single measurement mode
- 3. Continuous measurement mode (10Hz, 20Hz, 50Hz, 100Hz, 200Hz, 500Hz, 700Hz and 1.4kHz)
- 4. Self-test mode

Operating Mode	Start of Mode	End of Mode (Return to power-down mode)	Measurement Data
Single measurement	Command to enter mode 1 or 9	Measurement finished	(T)XYZ
Continuous mode	Command to enter mode 2, 3, 4, 5, 10, 11, 12, 13	Transition to other mode	(T)XYZ
Power-down mode	Power up or command to enter mode 0, 7, 8, 15	Transition to other mode	-
Self-test mode	Command to enter mode 6 and 14 from power-down mode	Measurement finished	Z

For single measurement, mode 1 and 9 will be similar. Same for continuous mode and self-test mode.



7.1.1. Single Measurement Mode

When the *Single measurement mode* is set, a magnetic measurement is started. After a measurement and when the signal processing is finished, the measurement data is stored to the data registers (**X**, **Y** and **Z**). After this, the sensor will go to the *power-down mode* automatically.

While going to the *power-down mode*, **MODE[3:0]** bits turns to 0. At the same time, **DRDY** bit (Data Ready) in **STAT1** register turns to High.

When any of measurement data register (**X**, **Y** and **Z**) is read, **DRDY** bit turns to Low. It remains High when switching from *power-down mode* to another mode.



When the sensor is measuring, the data registers (**X**, **Y** and **Z**) keep the previous data. Therefore, it is possible to read out data even during measurement periods.

MODE[3:0]	Single Meas Power Down #1	Power Down	Single Mees #2Power Down	Single Mees	Power Down
X,Y,Z		X,Y,Z (#1)	X, Y,Z (#1)		X,Y,Z (#3)
DRDY					
I2C_READ	///////		×3′,Ź (#1)		X,Y,Z (#3)

Figure 5: Single measurement mode when data read started during measurement period



7.1.2. Continuous Measurement Mode

When the "*Continuous measurement*" mode is set, the measurement starts periodically. After measurement and signal processing is finished, the measurement data is stored to the data registers (**X**, **Y**, and **Z**). Almost all internal blocks are disabled ("*Counting*" power state).

After a measurement period, **the device** wakes up automatically from "*Counting*" power state and starts a new measurement.

The Continuous measurement mode ends when "power-down" mode (**MODE[3:0]** bits = 0) is set. If the measurement period is changed while <u>the device</u> is already configured in "*Continuous measurement*" mode, a new measurement starts.

STAT1 and measurement data registers (**X**, **Y** and **Z**) will not be initialized by this.



Figure 6: Continuous measurement mode

7.1.2.1. Data Ready

When the measurement data is stored and ready to be read, the **DRDY** bit (Data ready) in STAT register is set to High. When a measurement is performed correctly, the device sets the Data Ready bit before going back to "*Counting*" power state.

7.1.2.2. Normal Read Sequence

The stored measurement data is protected during the data reading. There is no update of the data during this time. Consequently, the following sequence should be followed:

- 1. Check if the Data is Ready or not by polling DRDY bit of STAT1 register
 - a. **DRDY**: Data Ready. The Data is ready when set High.
- 2. Reading of the STAT1 register will not trigger the protection.
- Read measurement data When any of the measurement data register (X, Y, or Z) is read, the device enables the protection as soon as the register is copied into the I²C sending register. When data reading starts, DRDY (Data ready) bit turns Low.
- 4. Read STAT2 register (required for data consistency provides information on overflow and data skip)

When this read sequence is followed and there is no attempted I²C read during measurement, reading of STAT2 sets the DOR bit to low (see I/O Registers for reference).





7.1.2.3. Data Read Start during Measurement

When the sensor is measuring, the measurement data registers (**X**, **Y** and **Z**) keep the previous data. Therefore, it is possible to read out data even in measurement period.





7.1.2.4. Data Skip

If the available data is not read before a new measurement ends, the DRDY bit (Data Ready) remains High. However, a new set of measurement data will replace the previous one.



Figure 9: Data skip: When data is not read

If the available data is read while a new measurement is being performed, this set of data will be protected. This is also the case even if the reading procedure finishes after the measurement. Consequently, this new set of data is skipped.



Figure 10: Data skip: When data read has not been finished before the next measurement end



7.1.2.5. End Operation

Set the power-down mode (**MODE[3:0**] bits = **0**) to end the Continuous measurement mode.

7.1.2.6. Magnetic Sensor Overflow

Version -010

The sum of absolute values of each axis must be smaller than **4912µT** to avoid an overflow. (BRG: **0.15µT/LSB**)

|X|+|Y|+|Z| < **4912μT**

2's complement	Hex	Dec	Magnetic flux density [μT]
0111_1111_1111_0000	7FF0	32752	4912
0000_0000_0000_0001	0001	1	0.15
0000_0000_0000_0000	0000	0	0
1111_1111_1111_1111	FFFF	-1	-0.15
1000_0000_0001_0000	8010	-32752	-4912

Table 12: Measurement magnetic data format

Version -011 & -013

The sum of absolute values of each axis must be smaller than **49.13mT** to avoid an overflow. (BRG: **1.5μT/LSB**)

|X|+|Y|+|Z| < 49.13mT

2's complement	Hex	Dec	Magnetic flux density [mT]
0111_1111_1111_0000	7FF0	32752	49.13
0000_0000_0000_0001	0001	1	0.0015
0000_0000_0000_0000	0000	0	0
1111_1111_1111_1111	FFFF	-1	-0.0015
1000_0000_0001_0000	8010	-32752	-49.13

Table 13: Measurement magnetic data format

The calculation is done as follows: |X|+|Y|+|Z| < 32752

When the magnetic field exceeds this limitation, this is a Magnetic Sensor Overflow. When magnetic sensor overflow occurs, **HOVF** bit turns to "High". The **HOVF** bit is updated as soon as the measurement data register (**X**, **Y** and **Z**) is updated.



7.1.3. Self-Test Mode (recommended for version -010)

The self-test mode is used to check if the magnetic sensor is working normally. When the self-test mode is set, a magnetic field is generated by the internal magnetic source and a measurement is performed. The data is stored to the data registers (X, Y and Z). The sensor will then go to power-down mode automatically. The correct sequence to set the self-test mode is described below:

7.1.3.1. Self-Test Sequence

- 1. Set power-down mode.
- 2. Set Self-test mode.
- 3. Check Data Ready or not by polling DRDY bit (Data Ready) of STAT1 register.
 - a. When the data is ready, proceed to the next step.
- 4. Read measurement data. (X, Y and Z)

7.1.3.2. Self-Test Judgment (used by the customer)

The following criteria will help the user to decide if the sensor is correctly calibrated.

	DeltaX[15:0]	DeltaY[15:0]	DeltaZ[15:0]	Units
Criteria	-150 ≤ X ≤ 150	-150 ≤ Y ≤ 150	-1200 ≤ Z ≤ -400	LSB16

The typical magnetic field generated by the internal coil is around 135µT at room temperature. The self-test judgement are the expected values over temperature and voltage variations.

7.1.4. Power-Down Mode

In power-down mode, the device is in minimal power consumption state. All internal blocks including the oscillator are disabled except the POR circuit. Only the communication over the I²C interface is maintained. The digital handling of the communication is clocked by the I²C master clock. All registers remain accessible and the data stored in read/write registers remains.



7.2. Output Protocol (I²C) Description

7.2.1. Command Implementation

The following I²C commands are implemented:

- **MEM_DIRECT_READ**: reads data from memory space, starting from the default address 0x00
- MEM_READ: the start address will be specified in the command and the address will be incremented for continuous reading until an I²C stop is detected.
- MEM_WRITE: the start address will be specified followed by the data to be stored at addresses starting from the given start address and incremented until an I²C stop is detected.
- ADDRESSED_RESET: reset of the device, based on the I²C Slave Address (reset of addressed devices on the I²C bus only)

In the next sections, the format of the different I^2C commands is explained. The following legend is used:





7.2.1.1. Read Commands

There are two read commands that are implemented

- MEM_DIRECT_READ: reads data from memory space, starting from the default address 0x00
- **MEM_READ**: the start address will be specified in the command and the address will be incremented for continuous reading until an I²C stop is detected.

7.2.1.1.1.MEM_DIRECT_READ (direct read) Command

MEM_DIRECT_READ: reads data from memory space, starting from the default address 0x00



NOTES:

- Incremental readout return 0x00 when address out of valid space
- NAK is needed from master to allow going to STOP

7.2.1.1.2. MEM_READ (addressed read)

MEM_READ: the start address will be specified in the command and the address will be incremented for continuous reading until an I²C stop (P) is detected.

Incremental read-out starting at a given address (Register Start Address).

Normally it will read 1x register only, but the slave will continue to transmit data of sequential register addresses until the master terminates the communication.



Important! A repeated START is required to perform an "addressed read". Without repeated START, the command will be seen as a "direct read".

As soon as incremental addressing leaves the address space, the slave will respond with all 0x00. NOTES:

- Incremental readout return 0x00 when address out of valid space
- NAK is needed from master to allow going to STOP



7.2.1.2. MEM_WRITE (addressed write) Command

MEM_WRITE: the start address will be specified followed by the data to be stored at addresses starting from the given start address and incremented until an I²C stop (P) is detected.

Incremental write starting at a given address (Register Start Address).

Normally you write 1x register only, but optionally the master can continue to transmit data of sequential register addresses to reduce the communication time when a lot of registers should be written.



The slave is sending AK/NAK based on the fact whether it was able to write data

The slave will automatically increment the address of the read out byte, independent if it gave an AK or a NAK to the master. It is up to the master to re-write the byte afterwards.

When the device is busy with the write operation, new write commands will be ignored. A read operation will return invalid data.

7.2.1.3. ADDRESSED_RESET: Addressed reset



The addressed reset command brings the device back into a state like it was after power-on. The I²C slave address is used, which means that only the addressed devices on the I²C bus will be reset.



7.3. Memory Items Description

7.3.1. Memory Structure

The MLX90392 has registers (ports) of 16 addresses. Each address consists of 8 bits data. Data is transferred to or received from the external CPU via the I²C interface.

Address	Name	Description	R/W	7	6	5	4	3	2	1	0
0x00	STAT1	Status Register 1	R	-	-	-	-	RT	-		DRDY
0x01	X[7:0]	X-axis Measurement Magnetic Data [7:0]	R	-	-	-	-	-		-	-
0x02	X[15:8]	X-axis Measurement Magnetic Data [15:8]	R	-	-	-	-	-		-	· -
0x03	Y[7:0]	Y-axis Measurement Magnetic Data [7:0]	R	-	-	-	-	-	-	-	-
0x04	Y[15:8]	Y-axis Measurement Magnetic Data [15:8]	R	-		-	-	-	-	-	1.4
0x05	Z[7:0]	Z-axis Measurement Magnetic Data [7:0]	R	-	-	-	-		-	~	-
0x06	Z[15:8]	Z-axis Measurement Magnetic Data [15:8]	R	-	8	×	-	÷	8	×.	÷
0x07	STAT2	Status Register 2	R	-	-	-		-	-	DOR	HOVF
0x08	T[7:0]	Temperature Measurement Data Lower 8-bit	R	-	8	×	÷		Ξ.	Ξ.	÷
0x09	T[15:8]	Temperature Measurement Data Higher 8-bit	R	-	-	-	-			÷.	-
0x0A	CID	Company ID [7:0]	R	-		÷.	-			н.	
0x0B	DID	Device ID [7:0]	R	-	-	-	-	-		-	-
0x0C		Not used		-		-	-	-			-
0x0D		Not used		-	-		-	-		-	-
0x0E		Not used		-	~	-	-	-		~	-
0x0F		Not used		-	-		-	-	-	-	-
0x10	CTRL	Control Register (Application Mode)	R/W	. = .	~	-	-		MO	DE[3:0]	
0x11	RST	Reset = 0x06	R/W	-	-	-	-		-	-	-
0x12		Not used			-			-	-	in (-
0x13		Not used		-	-	-	-		-		-
0x14	OSR_DIG_FILT	OSR_DIG_FILT[7:0]	R/W	OSR_HALL	OSR_TEMP	DIG_FI	LT_HALL_XY[2:0]	DIG	FILT_TEM	P[2:0]
0x15	T_EN_DIG_FILT_Z	CUST_CTR	R/W	DNC=1	DNC=0	T_COMP_EN	DNC=1		DIG	FILT_HALL	Z[2:0]

Table 14: Memory map

DNC=Do Not Change

The **STAT1** register is mapped on address **0x00**, since it is the default address of **MEM_DIRECT_READ** (direct read) command.

The idea is that first the user has to read the status bits **DRDY** to check if there is new data and if there is new data, to continue the command to read the registers **X**, **Y** and **Z**.



7.3.2. I/O Registers Description

7.3.2.1. Address 0x00: STAT1[7:0]

7	6	5	4	3	2	1	0
STAT1_7	STAT1_6	STAT1_5	STAT1_4	RT	STAT1_2	STAT1_1	DRDY
 RW-0	RW-0	RW-0	RW-0	RW-1	RW-0	RW-0	RW-0

NOTE: R=Read access; W=Write access; value following dash (-) = value after reset

Bit 7-4	STAT1[7:4] Reserved (Not used)
Bit 3	RT The device is reset
	0 – The device was not reset
	1 – The device was reset and this is the first reading. Automatically set to 0 when the first reading
	of STAT register is done.
Bit 2-1	STAT1[2:1] Reserved (Not used)
Bit 0	DRDY Data ready
	DRDY bit turns to "1" when data is ready in "Single measurement" mode, "Continuous
	measurement" mode or "Self-test" mode. It returns to "0" when any one the measurement data
	register (X, Y, or Z) is read.
	0 – Normal
	1 – Data is ReaDY

7.3.2.2. Addresses 0x01-0x06: XYZ[15:0]

0x01	X[7:0] LSByte of X-axis
0x02	X[15:8] MSByte of X-axis
0x03	Y[7:0] LSByte of Y-axis
0x04	Y[15:8] MSByte of Y-axis
0x05	Z[7:0] LSByte of Z-axis
0x06	Z[15:8] MSByte of Z-axis



7.3.2.3. Address 0x07: STAT2[7:0]

7	6	5	4	3	2	1	0
STAT2_7	STAT2_6	STAT2_5	STAT2_4	STAT2_3	STAT2_2	DOR	HOVF
RW-0	RW-0	RW-0	RW-0	RW-0	RW-0	RW-0	RW-0

NOTE: R=Read access; W=Write access; value following dash (-) = value after reset

Bit 7-2	STAT2[7:2] Reserved (Not used)
Bit 1	DOR Data overrun
	DOR bit turns to "1" when data has been skipped in "Continuous measurement" mode. It returns to
	"0" when the data registers (X, Y, Z) are read.
	0 – Normal
	1 – Data OverRun
Bit 0	HOVF Magnetic sensor overflow
	0 – Normal
	1 – Magnetic Sensor OVerFlow occured

In "Single measurement" mode, "Continuous measurement" mode and "Self-test" mode, the magnetic sensor may overflow even though the measurement data register is not saturated. In this case, measurement data is not correct and HOVF bit turns to "1". When the measurement data register is updated, HOVF bit is updated.

7.3.2.4. Addresses 0x08-0x09: T[15:0]

0x08	T[7:0] LSByte of the temperature
0x09	T[15:8] MSByte of the temperature

7.3.2.5. Address 0x0A: CID[7:0]

0x0A	CID[7:0]	Company ID

7.3.2.6. Address 0x0B: DID[7:0]

OxOB DID[



7.3.2.7. Address 0x10: CTRL[7:0]

7	6	5	4	3	2	1	0	
CTRL_7	CTRL_6	CTRL_5	CTRL_4	MODE3	MODE2	MODE1	MODE0	
RW-0	RW-0	RW-0	RW-0	RW-0	RW-0	RW-0	RW-0	

NOTE: R=Read access; W=Write access; value following dash (-) = value after reset

Bit 7-4	CTRL[7:4] Reserved (Not used)				
Bit 3-0	MODE[3:0] Application mode				
	0 – Power-down mode				
	1 – Single measurement mode				
	2 – Continuous measurement mode 10Hz				
	3 – Continuous measurement mode 20Hz				
	4 – Continuous measurement mode 50Hz				
	5 – Continuous measurement mode 100Hz				
	6 – Self-test mode				
	7 – Power-down mode				
	8 – Power-down mode				
	9 – Single measurement mode				
	10 – Continuous measurement mode 200Hz				
	11 – Continuous measurement mode 500Hz				
	12 – Continuous measurement mode 700Hz				
	13 – Continuous measurement mode 1.4kHz				
	14 – Self-test mode				
	15 – Power-down mode				

7.3.2.8. Address 0x11: RST[7:0]

0x11

RST[7:0] Addressed RESET when users sends an I2C_ADDRESSED_RESET command

7.3.2.9. Addresses 0x12-0x13: Not used



7.3.2.10. Address 0x14: OSR_DIG_FILT[7:0]

7	6	5	4	3	2	1	0
OSR_HALL	OSR_TEMP	DIG_FILT_HA LL_XY2	DIG_FILT_HA LL_XY1	DIG_FILT_HA LL_XY0	DIG_FILT_TE MP2	DIG_FILT_TE MP1	DIG_FILT_TE MP0
RW-0	RW-0	RW-0	RW-0	RW-0	RW-0	RW-0	RW-0

NOTE: R=Read access; W=Write access; value following dash (-) = value after reset

Bit 7	OSR_HALL Over Sampling Ratio setting for the magnetic measurements
	0-32
	1-64
Bit 6	OSR_TEMP Over Sampling Ratio setting for the temperature measurement
	0-32
	1-64
Bit 5-3	DIG_FILT_HALL_XY[2:0] DIG_FILT setting for X and Y magnetic measurements
	0 – 0.113ms @ OSR = 0; 0.220ms @ OSR = 1
	1 – 0.167ms @ OSR = 0; 0.327ms @ OSR = 1
	2 – 0.273ms @ OSR = 0; 0.540ms @ OSR = 1
	3 – 0.487ms @ OSR = 0; 0.967ms @ OSR = 1
	4 – 0.913ms @ OSR = 0; 1.820ms @ OSR = 1
	5 – 1.767ms @ OSR = 0; 3.527ms @ OSR = 1
	6 – 3.473ms @ OSR = 0; 6.940ms @ OSR = 1
	7 – 6.887ms @ OSR = 0; 13.767ms @ OSR = 1
Bit 2-0	DIG_FILT_TEMP[2:0] DIG_FILT setting for the temperature measurements
	0 – 0.113ms @ OSR = 0; 0.220ms @ OSR = 1
	1 – 0.167ms @ OSR = 0; 0.327ms @ OSR = 1
	2 – 0.273ms @ OSR = 0; 0.540ms @ OSR = 1
	3 – 0.487ms @ OSR = 0; 0.967ms @ OSR = 1
	4 – 0.913ms @ OSR = 0; 1.820ms @ OSR = 1
	5 – 1.767ms @ OSR = 0; 3.527ms @ OSR = 1
	6 – 3.473ms @ OSR = 0; 6.940ms @ OSR = 1
	7 – 6.887ms @ OSR = 0; 13.767ms @ OSR = 1



7.3.2.11. Addresses 0x15: CUST_CTRL[7:0]

7	6	5	4	3	2	1	0
DNC = 1	DNC = 0	T_COMP_EN	DNC = 1	CUST_CTRL3	DIG_FILT_HA LL_Z2	DIG_FILT_HA LL_Z1	DIG_FILT_HA LL_Z0
RW-0	RW-0	RW-0	RW-0	RW-0	RW-0	RW-0	RW-0

NOTE: R=Read access; W=Write access; value following dash (-) = value after reset

r									
Bit 7	DNC=1 Value is preloaded from OTP with 0b1. Do not change it!								
Bit 6	DNC=0 Value is preloaded from OTP with 0b0. Do not change it!								
Bit 5	T_COMP_EN Enable or disable the temperature measurement and compensation								
	0 – Disabled								
	1 – Enabled								
Bit 4	DNC=1 Value is preloaded from OTP with 0b1. Do not change it!								
Bit 3	CUST_CTRL3 Reserved (Not used)								
Bit 2-0	DIG_FILT_HALL_Z[2:0] DIG_FILT setting for Z magnetic measurements								
	0 – 0.113ms @ OSR = 0; 0.220ms @ OSR = 1								
	1 – 0.167ms @ OSR = 0; 0.327ms @ OSR = 1								
	2 – 0.273ms @ OSR = 0; 0.540ms @ OSR = 1								
	3 – 0.487ms @ OSR = 0; 0.967ms @ OSR = 1								
	4 – 0.913ms @ OSR = 0; 1.820ms @ OSR = 1								
	5 – 1.767ms @ OSR = 0; 3.527ms @ OSR = 1								
	6 – 3.473ms @ OSR = 0; 6.940ms @ OSR = 1								
	7 – 6.887ms @ OSR = 0; 13.767ms @ OSR = 1								



7.4. Flowchart





7.5. Performance Graphs

- 7.5.1. Noise Performance
- 7.5.1.1. Version -010 Without temperature compensation



Figure 16: version -010 - XY axis RMS noise (typical)



Figure 17: version -010 - Z axis RMS noise (typical)

Note: When OSR_HALL is set to 0, the above values have to be scaled by sqrt(2).







Figure 18: version -010 - XY axes RMS noise (typical)



Figure 19: version -010 - Z axis RMS noise (typical)











Figure 21: version -011 & -013 - Z axis RMS noise (typical)





7.5.1.4. With temperature compensation and maximum field





Figure 23: version -011 & -013 - Z axis RMS noise (typical)



7.6. Temperature Compensation

The MLX90392 has a built-in temperature compensation, which is done by a piecewise linear approximation of the temperature coefficient of the Hall plates. A reference temperature is chosen (TREF=35°C), where the result at any temperature, higher than TREF is adjusted by a gain SENT_TC_HT and if the temperature is lower than TREF - by SENS_TC_LT. These two coefficients are calibrated at Melexis and are lumped into the parameter name SENS_TC in the equation below.

$$XYZ_{18_0} = XYZ_RAW_{18_0} \cdot \left[1 + \frac{SENS_TC_{11_0} \cdot (TEMP_{15_0} - TREF_{15_0})}{2} \right]$$

SENS_TC_N is a scaling factor needed for the fixed-point calculations. It is determined and written at Melexis during production test.

In case the temperature compensation is not needed, bit 5 in T_EN_DIG_FILT_Z register is set to 0. This also disables the temperature measurement and the term in the square brackets of the formula above is equal to 1.

The operation is executed on the 19 bits raw magnetic data which is consequently truncated to 16 bit and loaded into the results registers.

8. Application Information

8.1. Recommended Application Diagram



Figure 24: Recommended application diagram

Note: R_{SDA} and R_{SCL} are part of the bus specifications. Please refer to it.



9. Package and Manufacturability Information

9.1. ESD Precaution

Electronic semiconductor products are sensitive to Electro Static Discharge (ESD). Always observe Electro Static Discharge control procedures whenever handling semiconductor products.

9.2. Package Information

2.60

1.70

1.35

1.85

0.45

0.24

9.2.1. Dimensions

UTDFN 2x2.5mm



Ε

P1 P2

G

L

Κ

b

e

2.40 1.45

1.25

1.75

0.25

0.20

0.16

0.40 BSC



9.2.2. Sensing Element Placement





Figure 25: Field convention (Top view of the package with pin 1 at the bottom left)

9.2.3. Marking





9.3. Standard Information on Soldering Processes

Our products are classified and qualified regarding soldering technology, solderability and moisture sensitivity level according to standards in place in Semiconductor industry.

For further details about test method references and for compliance verification of selected soldering method for product integration, Melexis recommends reviewing on our web site the General Guidelines soldering recommendation (<u>http://www.melexis.com/en/quality-environment/soldering</u>)

For all soldering technologies deviating from the one mentioned in above document (regarding peak temperature, temperature gradient, temperature profile etc.), additional classification and qualification tests have to be agreed upon with Melexis.

For package technology embedding trim and form post-delivery capability, Melexis recommends consulting the dedicated trim&form recommendation application note: lead trimming and forming recommendations (http://www.melexis.com/en/documents/documentation/application-notes/lead-trimming-and-forming-recommendations).

Melexis is contributing to global environmental conservation by promoting lead free solutions. For more information on qualifications of RoHS compliant products (RoHS = European directive on the Restriction Of the use of certain Hazardous Substances) please visit the quality page on our website: http://www.melexis.com/en/quality-environment.



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11. Revision History

Revision	Date	Change history
001	13-Jan-2021	First datasheet issue (JED)
002	15-Apr-2021	Revised specifications. Package dimensions updated + Cosmetic changes (JED)
003	07-Jun-2022	Cosmetic changes, corrections, added version -013



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