

# R1516x Series

# 150mA 36V Input LDO Regulator

NO.EA-258-140703

### **OUTLINE**

The R1516x Series are CMOS-based high-voltage resistant and low supply current voltage regulator ICs that provide the minimum 150mA of output voltage. Internally, the R1516x Series consists of a Foldback Protection Circuit, and a Thermal Shutdown Circuit in addition to the basic regulator circuits. The operating temperature range is between –40°C to 105°C, and the maximum input voltage is 36V. All these features allow the R1516x Series to become an ideal power source of electric home appliances.

The R1516x Series are available in fixed output voltage options between 1.8V and 6.2V in 0.1V steps. The output voltage accuracy is ±1%.

The R1516x Series are available in two types of packages: SOT-89-5 that is for high-density mounting and HSOP-6J that is for high wattage.

### **FEATURES**

Input Voltage Range
• Supply Current Typ. 29µA
• Standby Current ····· Typ. 0.1µA
Output Voltage Temperature Coefficient Typ. ±100ppm/°C
• Output Current Min. 150mA (Vout=5.0V, VIN=8.0V)
Line Regulation
• Output Voltage Accuracy ······ ±1% (Vouт ≥ 3.2V, Topt=25°C)
• Packages SOT-89-5, HSOP-6J
Output Voltage Range · 1.8V to 6.2V (0.1V steps)
(For other voltages, please refer to MARK INFORMATIONS.)
Built-in Foldback Protection Circuit
Built-in Thermal Shutdown Circuit Stops at 160°C
Operating Temperature

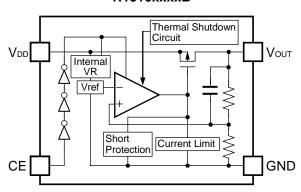
## **APPLICATIONS**

- Power source for home appliances such as refrigerators, rice cookers, electric hot-water pot.
- Power source for notebook PCs, digital TVs, cordless phones, and private LAN system.
- Power source for office equipment machines such as copiers, printers, facsimiles, scanners, projectors.

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# **BLOCK DIAGRAMS**

#### R1516xxxxB



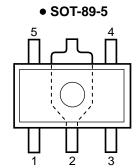
# **SELECTION GUIDE**

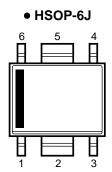
The output voltage and the package for the ICs can be selected at the user's request.

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R1516HxxxB-T1-FE	SOT-89-5	1,000pcs	Yes	Yes
R1516SxxxB-E2-FE	HSOP-6J	1,000pcs	Yes	Yes

xxx : The output voltage can be designated in the range of 1.8V(018) to 6.2V(062) in 0.1V steps. (For other voltages, please refer to MARK INFORMATIONS.)

# **PIN CONFIGURATIONS**





# **PIN DESCRIPTIONS**

### • SOT-89-5

Pin No.	Symbol	Description
1	Vоит	Output Pin
2	GND*	Ground Pin
3	CE	Chip Enable Pin ("H" Active)
4	GND*	Ground Pin
5	V <sub>DD</sub>	Input Pin

<sup>\*)</sup> The GND pin must be wired together when it is mounted on board.

### • HSOP-6J

Pin No.	Symbol	Description
1	Vouт	Output Pin
2	GND*	Ground Pin
3	CE	Chip Enable Pin ("H" Active)
4	GND*	Ground Pin
5	GND*	Ground Pin
6	V <sub>DD</sub>	Input Pin

<sup>\*)</sup> The GND pin must be wired together when it is mounted on board.

#### R1516x

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# **ABSOLUTE MAXIMUM RATINGS**

Symbol	Item	Rating	Unit
Vin	Input Voltage	-0.3~50	V
Vin	Peak Input Voltage*1	60	V
Vce	Input Voltage (CE Pin)	-0.3∼V <sub>IN</sub> +0.3≦50	V
Vouт	Output Voltage	-0.3∼V <sub>IN</sub> +0.3≦50	V
Іоит	Output Current	250	mA
D-	Power Dissipation (SOT-89-5)*2	900	m\//
P <sub>D</sub>	Power Dissipation (HSOP-6J)*2	1700	mW
Topt	Operating Temperature Range	-40 to +105	°C
Tstg	Storage Temperature Range	-55 to +125	°C

<sup>\*1)</sup> Duration time: 200ms

## **ABSOLUTE MAXIMUM RATINGS**

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

<sup>\*2)</sup> For Power Dissipation, please refer to PACKAGE INFORMATION.

# **ELECTRICAL CHARACTERISTICS**

The specifications in  $\square$  are applicable under the condition of -40°C  $\leq$  Topt  $\leq$  105°C.

● R1516xxxxB

Symbol	Item	Conditions		Min.	Тур.	Max.	Unit	
Vin	Input Voltage			4		36	V	
Iss	Supply Current	VIN=VOUT+3.0V, IOUT=0mA			29	45	μΑ	
Istandby	Standby Current	VIN=36V, VCE	=0\	,		0.1	1.0	μΑ
Vouт	Output Voltage	VIN=VOUT+3.0		V <sub>OUT</sub> ≥ 3.2V	×0.99 ×0.98		×1.01 ×1.02	V
<b>V</b> 001	Output Voltage   lour=1mA		Vout < 3.2V	×0.985 ×0.975		×1.015 ×1.025	V	
louт	Output Current	Please refer to "Output		Current by	y Output \	/oltage".		
$\Delta$ Vουτ/ $\Delta$ Iουτ	Load Regulation	Please refer to "Load Re		egulation b	y Output	Voltage".		
ΔVουτ/ΔVιν	Line Regulation	Іоит=1mA	(Vol.	$+1.5V \le V_{IN} \le 36V,$ $v_{IT} \ge 2.5V)$ $v_{IT} \le V_{IN} \le 36V,$ $v_{IT} < 2.5V)$		0.1	0.7	%/V
V <sub>DIF</sub>	Dropout Voltage	Please refer to "Dropout Voltage by Output Voltage			Voltage".			
ΔVουτ/ΔTopt	Output Voltage Temperature Coefficient	V <sub>IN</sub> =V <sub>OUT</sub> +3.0V, I <sub>OUT</sub> =1mA -40°C ≤ T <sub>Opt</sub> ≤ 105°C			±100		ppm/°C	
Isc	Short Current Limit	V <sub>OUT</sub> =0V			50		mA	
Vсен	CE Input Voltage "H"			1.3		Vin	V	
Vcel	CE Input Voltage "L"			0		0.35	V	
TTSD	Thermal Shutdown Temparature	Junction Temeprature		150	160		°C	
T <sub>TSR</sub>	Thermal Shutdown Released Temparature	Junction Temeprature			125		°C	

## Output Current by Output Voltage

Topt=25°C

Output Voltage	Output Current Ιουτ (mA)		
<b>V</b> оит <b>(V)</b>	Condition	Min.	
1.8 ≤ Vout < 3.0	VIN=VOUT+5.0V		
3.0 ≤ Vout < 5.0	VIN=VOUT+4.0V	150	
$5.0 \le V_{\text{OUT}} \le 6.2$	VIN=VOUT+3.0V		

# ● Load Regulation by Output Voltage

Topt=25°C

Output Voltage Load Regulation (mV)			
<b>V</b> оит <b>(V)</b>	Condition	Тур.	Max.
$1.8 \le V_{\text{OUT}} \le 3.0$	V V .2.0V	30 (Vout=3.0V)	70
3.0 < Vout ≤ 5.0	$V_{IN}=V_{OUT}+3.0V$ $1mA \le I_{OUT} \le 40mA$	40 (Vouт=5.0V)	105
5.0 < Vout ≤ 6.2	1111A \(\sigma\) 1001 \(\sigma\) 40111A	50 (Vouт=6.2V)	125

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#### Dropout Voltage by Output Voltage

(Iout=20mA)

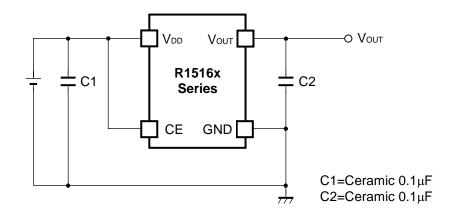
Topt=25°C

(lout=20mA)	Topt=25°C
Output Voltage Vout (V)	Dropout Voltage
	V <sub>DIF</sub> (V)
	Max.
Vоит=1.8	2.30
Vоит=1.9	2.20
Vоит=2.0	2.10
Vоит=2.1	2.00
Vоит=2.2	1.90
Vоит=2.3	1.80
Vоит=2.4	1.70
Vоит=2.5	1.60
Vоит=2.6	1.50
Vоит=2.7	1.40
V <sub>О</sub> UT=2.8	1.30
V <sub>оит</sub> =2.9	1.20
Vоит=3.0	1.10
Vоuт=3.1	1.06
Vоит=3.2	1.02
Vоит=3.3	0.98
Vоит=3.4	0.94
Vоит=3.5	0.90
Vоит=3.6	0.86
Vоит=3.7	0.82
Vоит=3.8	0.78
Vоит=3.9	0.74
V <sub>оит</sub> =4.0	0.70
V <sub>оит</sub> =4.1	0.69
V <sub>оит</sub> =4.2	0.68
Vоит=4.3	0.67
Vout=4.4	0.66
Vout=4.5	0.65
Vout=4.6	0.64
Vоит=4.7	0.63
Vout=4.8	0.62
Vout=4.9	0.61
5.0 ≤ Vout ≤ 6.2	0.60

## RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

### TYPICAL APPLICATION



## **TECHNICAL NOTES**

When using the R1516x Series, please consider the following points.

#### **Phase Compensation**

The R1516x Series provide the constant-voltage without using C1 and C2 capacitors. However, if the input line is too long, C1 should be connected. To minimize the input voltage fluctuation and the transient output voltage fluctuation that is caused by the load fluctuation, C2 size should be increased. Please refer to the Basic Test Circuit below when connecting a  $0.1\mu\text{F}$  to  $20\mu\text{F}$  C1 capacitor from  $V_{\text{DD}}$  to GND, and also connecting a  $0.1\mu\text{F}$  to  $20\mu\text{F}$  C2 capacitor from  $V_{\text{OUT}}$  to GND. The C1 and C2 capacitors,  $V_{\text{DD}}$ , GND and  $V_{\text{OUT}}$  should be connected as close as possible to each other.

#### **GND Wiring on Boards**

For SOT-89-5 package, please connect the No.2 pin and the No.4 pin to the ground plane on the board. For HSOP-6J package, please connect the No.2 pin, the No.4 pin and the No.5 pin to the ground plane on the board.

#### **Thermal Shutdown**

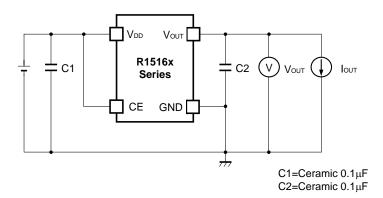
The thermal shutdown is included, which limits the junction temperature to a maximum 160°C (Typ.). Under extreme conditions when the junction temperature begins to rise above 160°C, the output is turned off, reducing the output current to zero. When the junction temperature drops below +125°C (Typ.), the output is turned on again and the output current is restored to its nominal value. The output repeats turning on and off to form a pulse shaped output unless the causes of the temperature rise are removed.

#### Chip Enable (CE) Circuit

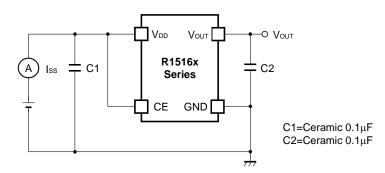
The electrical potential level of chip enable (CE) pin should not be set in between V<sub>CEH</sub> and V<sub>CEL</sub>. Using the electrical potentials in between V<sub>CEH</sub> and V<sub>CEL</sub> may cause the increase of supply current and may result in unstable output.

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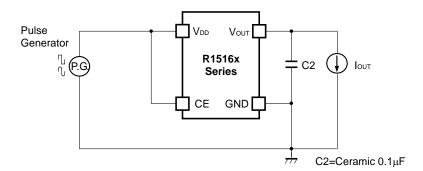
# **TEST CIRCUITS**



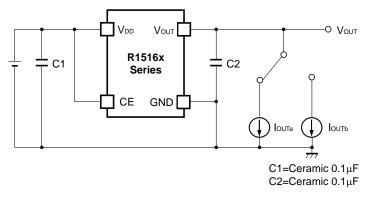
**Basic Test Circuit** 



**Test Circuit for Supply Current** 



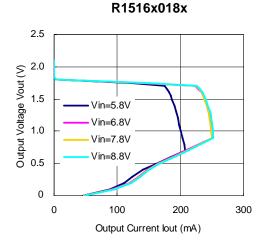
**Test Circuit for Line Transient Response** 

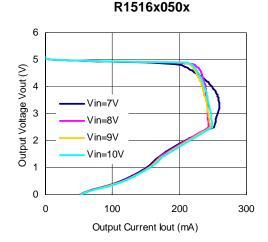


**Test Circuit for Load Transient Response** 

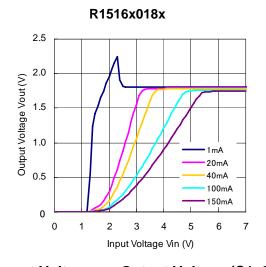
# **Typical Characteristics**

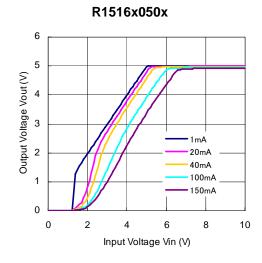
1) Output Voltage vs. Output Current (C1=0.1µF, C2=0.1µF, Topt=25°C)



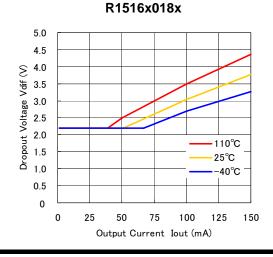


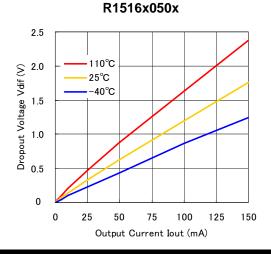
2) Output Voltage vs. Input Voltage (C1=0.1μF, C2=0.1μF, Topt=25°C)



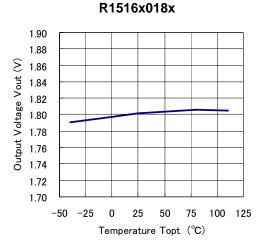


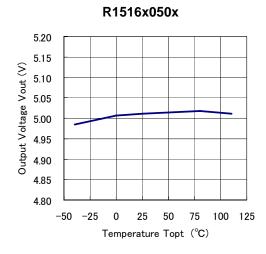
3) Dropout Voltage vs. Output Voltage (C1=0.1μF, C2=0.1μF, Topt=25°C)



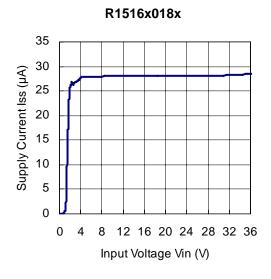


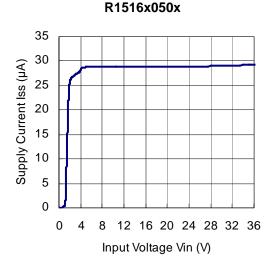
## 4) Output Voltage vs. Temperature (C1=0.1 $\mu$ F, C2=0.1 $\mu$ F)



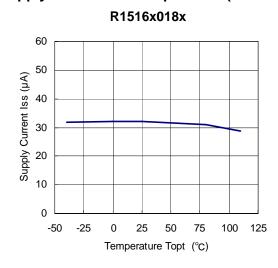


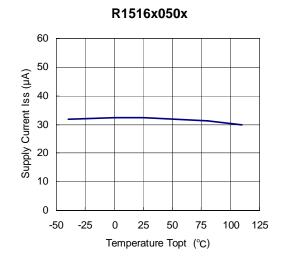
## 5) Supply Current vs. Input Votlage (C1=0.1μF, C2=0.1μF, Topt=25°C)



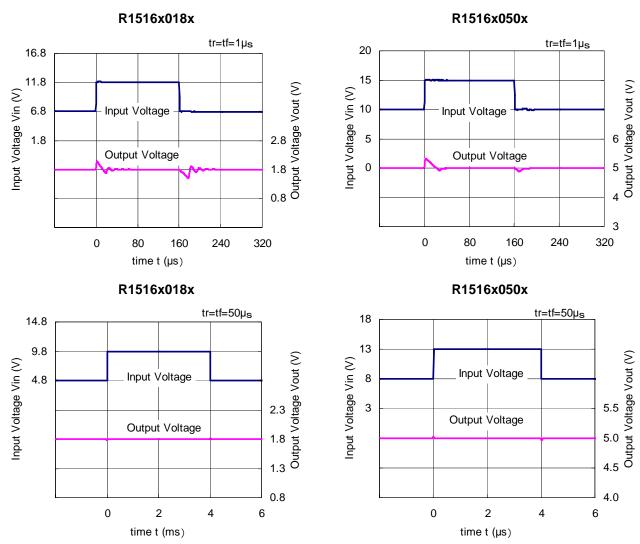


## 6) Supply Current vs. Temperature (C1=0.1 $\mu$ F, C2=0.1 $\mu$ F)

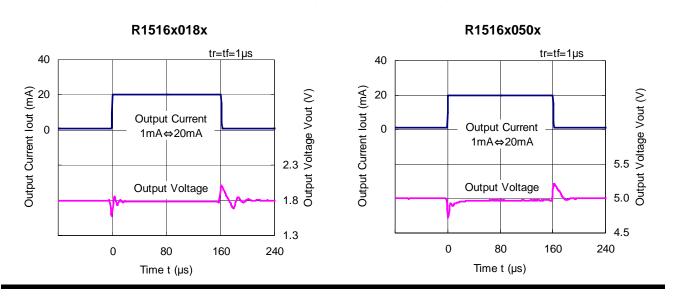


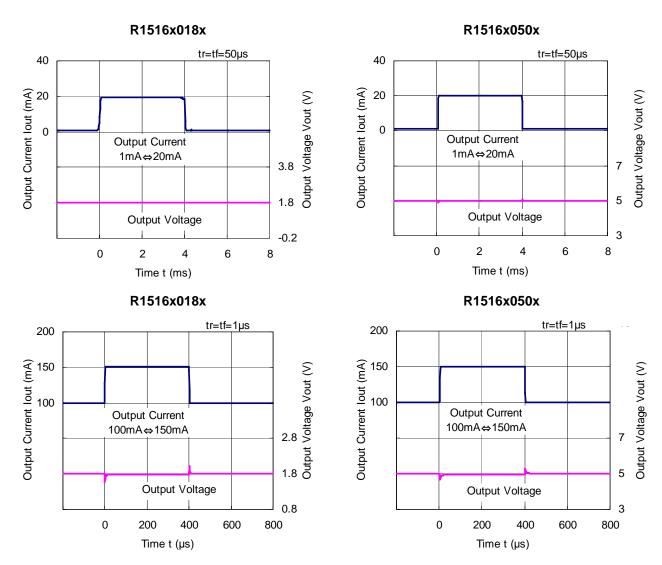


## 7) Input Transient Response (C1=none, C2=Ceramic 0.1µF, Iout=1mA, Topt=25°C)

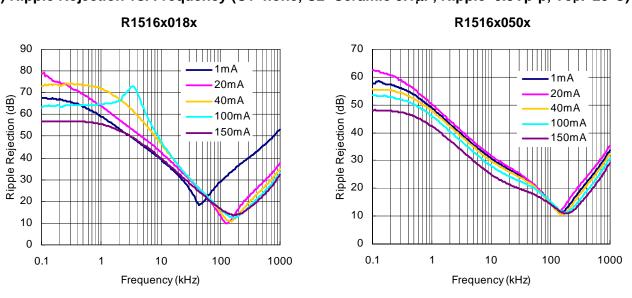


### 8) Load Transient Response (C<sub>1</sub>=Ceramic 0.1μF, C<sub>2</sub>=Ceramic 0.1μF, Topt=25°C)

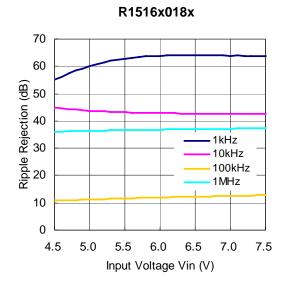


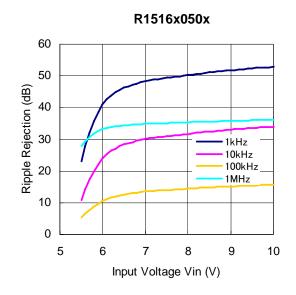


### 9) Ripple Rejection vs. Frequency (C1=none, C2=Ceramic 0.1μF, Ripple=0.5Vp-p, Topt=25°C)



# 10) Ripple Rejection vs. Input Voltage (C1=none, C2=Ceramic 0.1µF, lour=20mA, Ripple=0.5Vp-p, Topt=25°C)







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- 6. We are making our continuous effort to improve the quality and reliability of our products, but semiconductor products are likely to fail with certain probability. In order to prevent any injury to persons or damages to property resulting from such failure, customers should be careful enough to incorporate safety measures in their design, such as redundancy feature, fire containment feature and fail-safe feature. We do not assume any liability or responsibility for any loss or damage arising from misuse or inappropriate use of the products.
- 7. Anti-radiation design is not implemented in the products described in this document.
- 8. The X-ray exposure can influence functions and characteristics of the products. Confirm the product functions and characteristics in the evaluation stage.
- 9. WLCSP products should be used in light shielded environments. The light exposure can influence functions and characteristics of the products under operation or storage.
- 10. There can be variation in the marking when different AOI (Automated Optical Inspection) equipment is used. In the case of recognizing the marking characteristic with AOI, please contact our sales or our distributor before attempting to use AOI.
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